

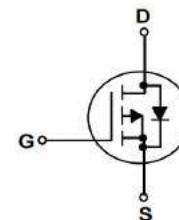


## Features

- P-Channel
- Low Gate Charge
- High Power and current handing capability
- Lead free product is acquired
- 100% EAS Tested

$V_{DS}$	-60	V
$R_{DS(on),TYP}$ @ $V_{GS}=-10$ V	80	mΩ
$R_{DS(on),TYP}$ @ $V_{GS}=-4.5$ V	97	mΩ
$I_D$	-13	A

TO-252



Part ID	Package Type	Marking	Packing
ZT80P06D	TO-252	ZT80P06D	2500pcs/reel

**Absolute Maximum Ratings  $T_A = 25^\circ\text{C}$ , unless otherwise specified**

Symbol	Parameter		Rating	Unit
<b>Common Ratings (<math>T_c=25^\circ\text{C}</math> Unless Otherwise Noted)</b>				
$V_{GS}$	Gate-Source Voltage		$\pm 20$	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage		-60	V
$T_J$	Maximum Junction Temperature		175	°C
$T_{STG}$	Storage Temperature Range		-55 to 175	°C
$I_{DM}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c=25^\circ\text{C}$	-52	A
<b>Mounted on Large Heat Sink</b>				
$I_D$	Drain Current-Continuous	$T_c=25^\circ\text{C}$	-13	A
		$T_c=100^\circ\text{C}$	-9.2	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	40.5	W
		$T_c=100^\circ\text{C}$	20	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		3.7	°C/W
<b>Drain-Source Avalanche Ratings</b>				
EAS	Avalanche Energy, Single Pulsed (Note 2)		56	mJ



**Electrical Characteristics ( $T_j=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ <math>T_j=25^\circ\text{C}</math> (unless otherwise stated)</b>						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-60	--	--	V
$I_{DS}$	Zero Gate Voltage Drain Current	$V_{DS}=-60\text{V}, V_{GS}=0\text{V}$	--	--	-1	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	$\pm 100$	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-1.0	-1.8	-2.5	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=-10\text{V}, I_D=-10\text{A}$	--	80	100	$\text{m}\Omega$
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=-4.5\text{V}, I_D=-8\text{A}$	--	97	129	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS}=-5\text{V}, I_D=-10\text{A}$	--	15	--	S

**Dynamic Electrical Characteristics @  $T_j = 25^\circ\text{C}$  (unless otherwise stated)**

C <sub>iss</sub>	Input Capacitance	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	1436	--	pF
C <sub>oss</sub>	Output Capacitance		--	46	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	34	--	pF
Q <sub>g</sub>	Total Gate Charge	$V_{DS}=-30\text{V}, I_D=-10\text{A}, V_{GS}=-10\text{V}$	--	25	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	3	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	7	--	nC

**Switching Characteristics**

T <sub>d(on)</sub>	Turn-on Delay Time	$V_{DS}=-30\text{V}, R_L=3\Omega, R_G=3\Omega, V_{GS}=-10\text{V}$	--	9.5	--	ns
T <sub>r</sub>	Turn-on Rise Time		--	5.3	--	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		--	28	--	ns
T <sub>f</sub>	Turn-Off Fall Time		--	5.8	--	ns

**Source-Drain Diode Characteristics @  $T_j = 25^\circ\text{C}$  (unless otherwise stated)**

I <sub>SD</sub>	Source-Drain Current (Body Diode)		--	--	-13	A
V <sub>SD</sub>	Forward on voltage <sup>(Note 1)</sup>	$I_S = -10\text{A}, V_{GS}=0\text{V}$	--	--	-1.2	V
T <sub>rr</sub>	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_F = -10\text{A}, V_{GS}=0\text{V}, \frac{dI}{dt}=100\text{A}/\mu\text{s}$	--	34	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	37	--	nC

Notes :

1.Repetitive Rating: Pulse width limited by maximum junction temperature.

2.EAS condition:  $T_j=25^\circ\text{C}, V_{DD}=40\text{V}, V_G=-10\text{V}, R_g=25\Omega, L=0.5\text{mH}$ .

## Typical Electrical And Thermal Characteristics (Curves)

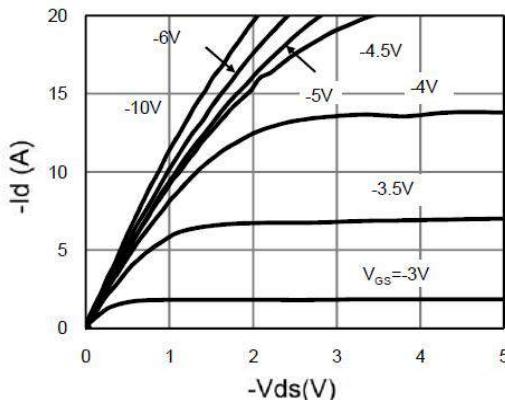


Figure 1. Output Characteristics

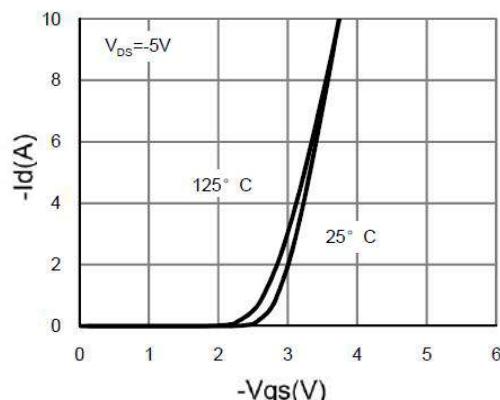


Figure 4. Transfer Characteristics

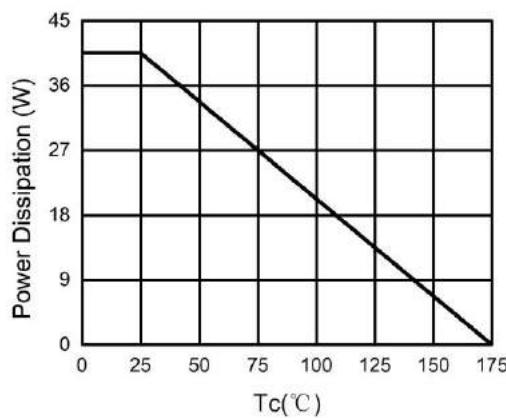


Figure 2. Power Dissipation

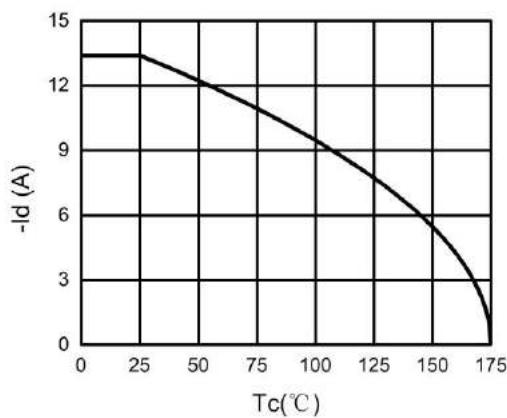


Figure 5. Drain Current

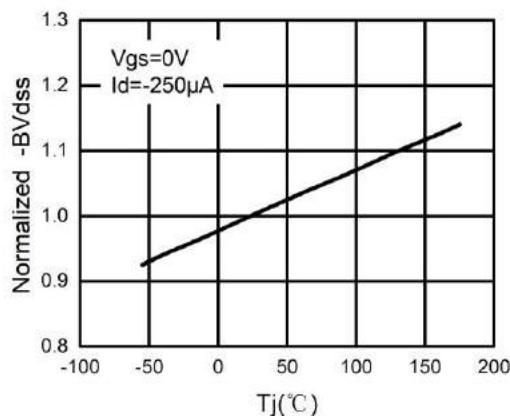


Figure 3.  $BV_{dss}$  vs Junction Temperature

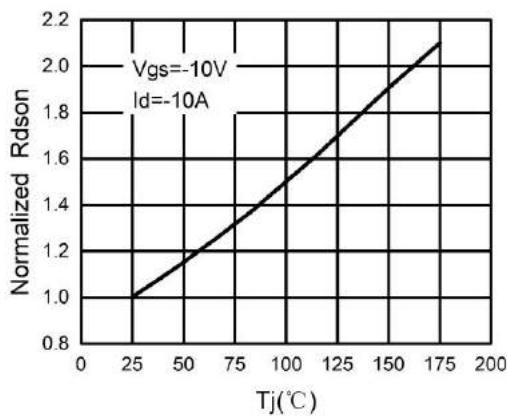


Figure 6.  $R_{DS(on)}$  vs Junction Temperature

## Typical Electrical And Thermal Characteristics (Curves)

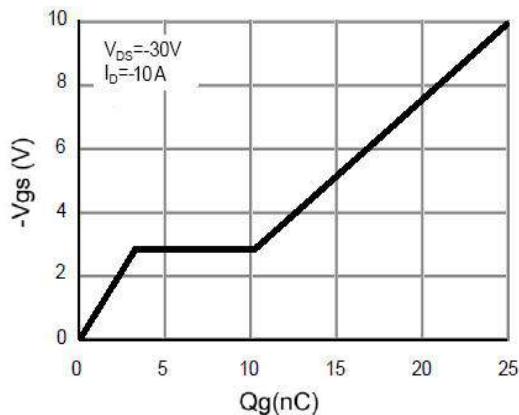


Figure 7. Gate Charge Waveforms

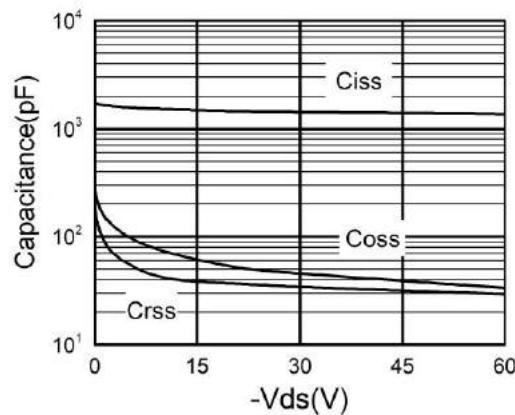


Figure 9. Capacitance

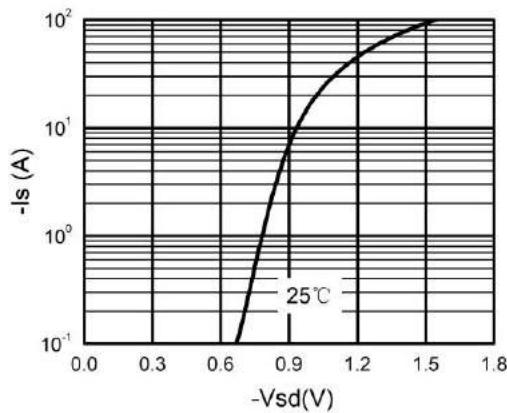


Figure 8. Body-Diode Characteristics

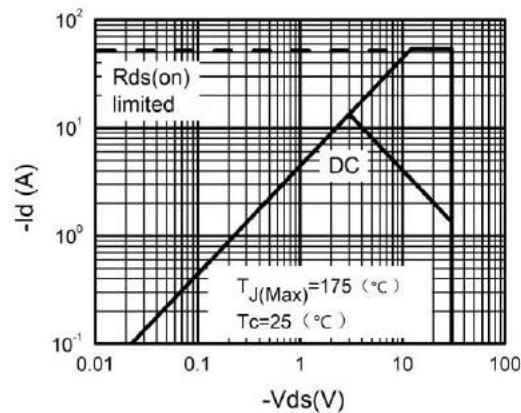


Figure 10. Maximum Safe Operating Area

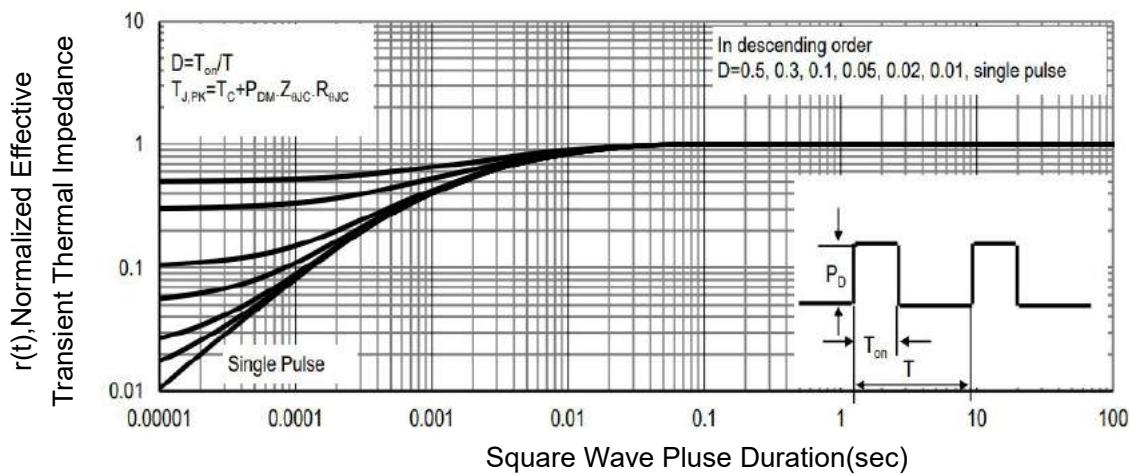
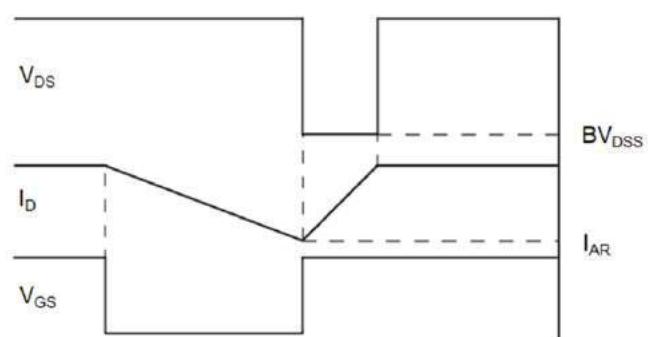
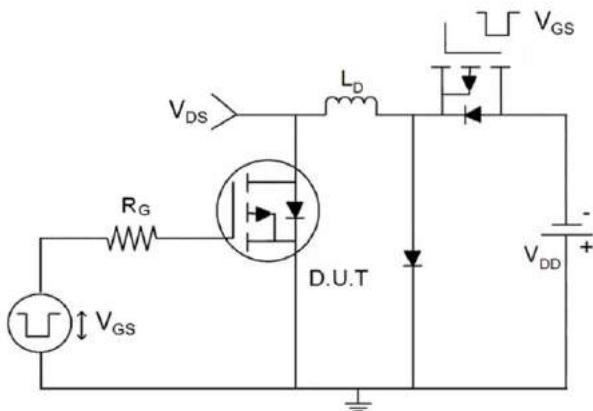


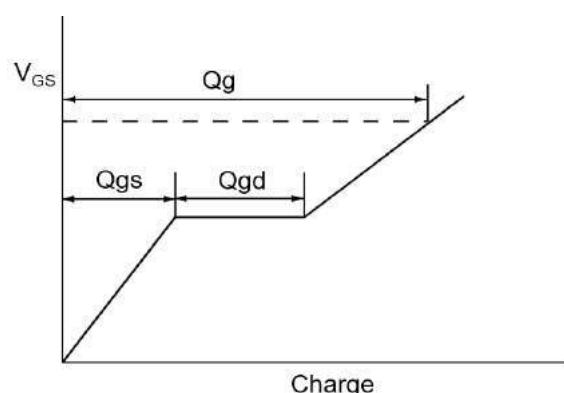
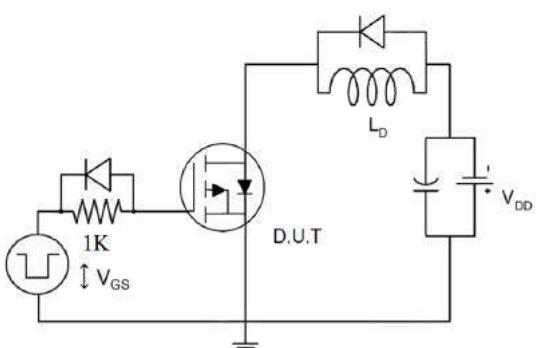
Figure 11. Normalized Maximum Transient Thermal Impedance

## Test Circuit

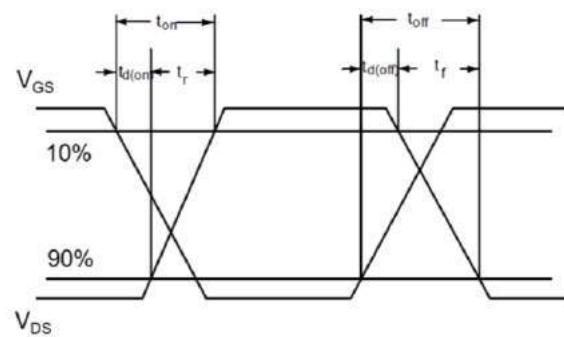
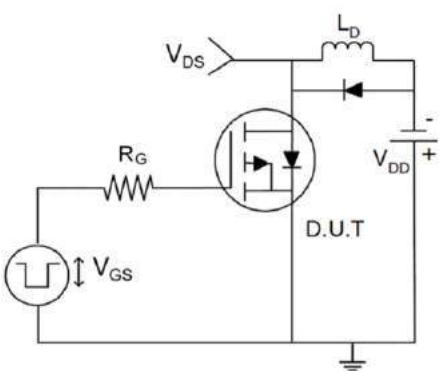
### 1) $E_{AS}$ Test Circuits



### 2) Gate Charge Test Circuit

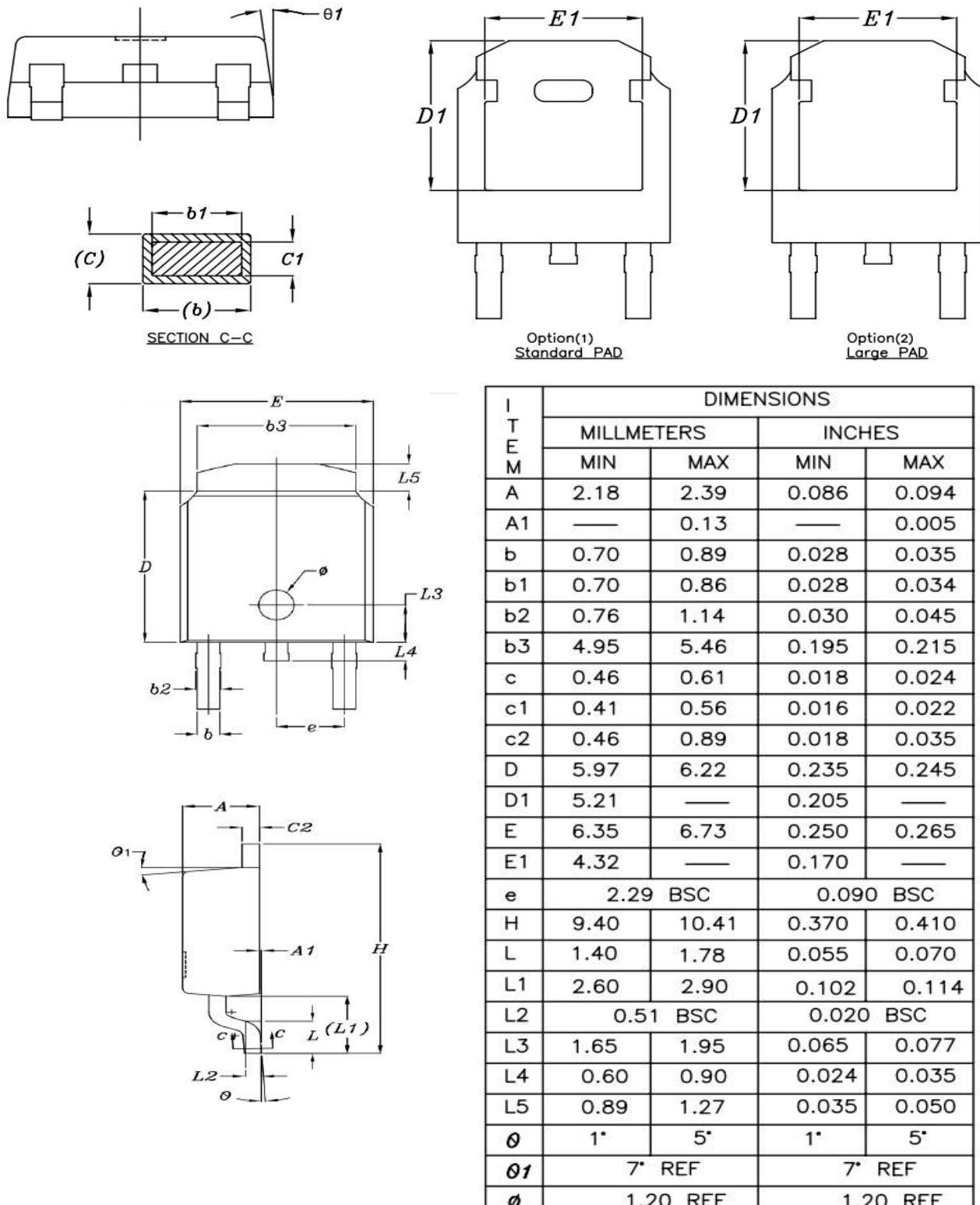


### 3) Switch Time Test Circuit





## TO-252 Package Information



## Customer Service

Sales and Service:

[zj@ztasemi.com](mailto:zj@ztasemi.com)