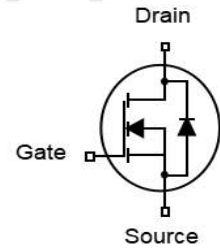


Features

- N-Channel
- Fast Switching
- Low Gate Charge and $R_{DS(ON)}$
- Low Reverse transfer capacitances
- 100% EAS Tested

V_{DS}	100	V
$R_{DS(on),TYP@ V_{GS}=10V}$	60	m Ω
$R_{DS(on),TYP@ V_{GS}=4.5V}$	76	m Ω
I_D	18	A

TO-252



Part ID	Package Type	Marking	Packing
ZTG60N10D	TO-252	ZTG60N10D	2500pcs/reel

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	100	V	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c = 25^\circ\text{C}$ 72	A	
Mounted on Large Heat Sink				
I_D	Drain Current-Continuous	$T_c = 25^\circ\text{C}$	18	A
		$T_c = 100^\circ\text{C}$	12	A
P_D	Maximum Power Dissipation	31	W	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	4	$^\circ\text{C/W}$	
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	52	$^\circ\text{C/W}$	
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed (Note 2)	22	mJ	

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ T_J=25°C (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =100V, V _{GS} =0V	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.3	1.9	2.3	V
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =5A	--	60	75	mΩ
R _{DS(on)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _D =4A	--	76	95	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =50V, V _{GS} =0V, f=1MHz	--	170	--	pF
C _{oss}	Output Capacitance		--	57	--	pF
C _{rss}	Reverse Transfer Capacitance		--	1.8	--	pF
R _g	Gate Resistance	f=1MHz	--	2.5	--	Ω
Q _g	Total Gate Charge	V _{DS} =50V, I _D =10A, V _{GS} =10V	--	3.7	--	nC
Q _{gs}	Gate-Source Charge		--	0.8	--	nC
Q _{gd}	Gate-Drain Charge		--	1.0	--	nC
Switching Characteristics						
T _{d(on)}	Turn-on Delay Time	V _{DS} =50V, I _D =10A, R _L =0.75Ω, R _G =3Ω, V _{GS} =10V	--	7.8	--	ns
T _r	Turn-on Rise Time		--	16.1	--	ns
T _{d(off)}	Turn-Off Delay Time		--	16.8	--	ns
T _f	Turn-Off Fall Time		--	13.9	--	ns
Source- Drain Diode Characteristics @ T_J = 25°C (unless otherwise stated)						
I _{SD}	Source-Drain Current (Body Diode)		--	--	18	A
V _{SD}	Forward on voltage	I _S =0.5A, V _{GS} =0V	--	--	1.2	V
T _{rr}	Reverse Recovery Time	T _J =25°C, I _S =10A, V _{DD} =50V di/dt=100A/μs	--	22	--	ns
Q _{rr}	Reverse Recovery Charge		--	18	--	nC

Notes:

- 1 : Repetitive rating; pulse width limited by maximum junction temperature
2 : L=0.5 mH, R_g=25Ω, Starting T_J=25 °C

Characteristics Curve:

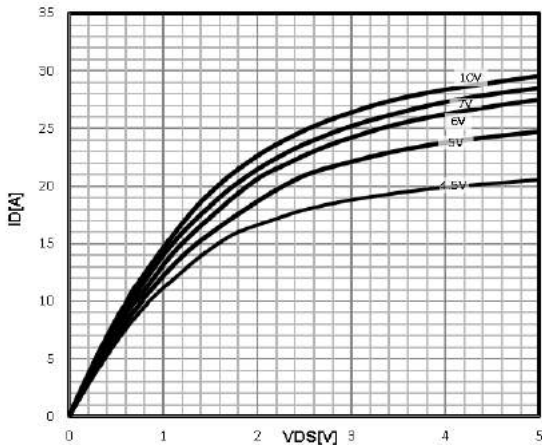


Figure 1 . output characteristics
 $I_D=f(V_{DS})$

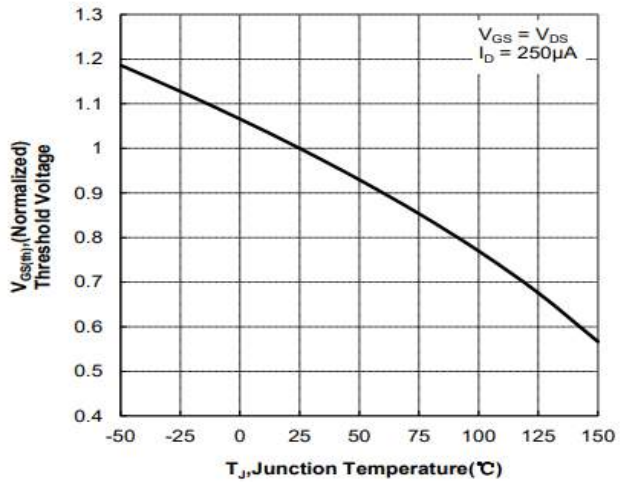


Figure 4. Gate Threshold Voltage
 $V_{TH}=f(T_j); I_D=250\mu A$

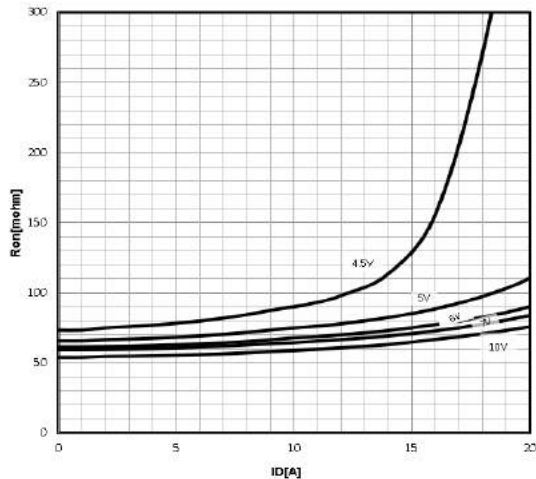


Figure 2 . drain-source on resistance
 $R_{DS(on)}=f(I_D)$

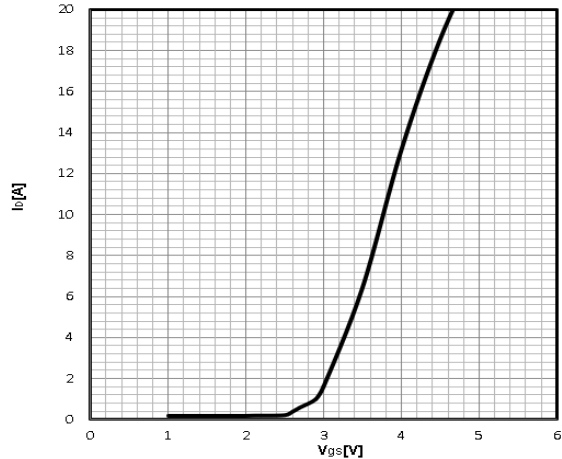


Figure 5 . transfer characteristics
 $I_D=f(V_{GS})$

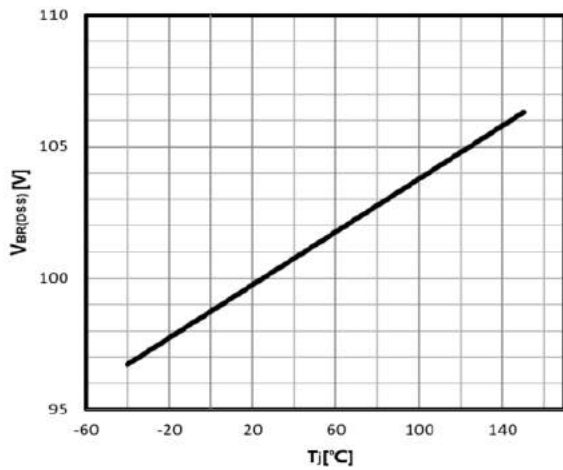


Figure 3 . Drain-source breakdown voltage
 $V_{BR(DSS)}=f(T_j); I_D=250\mu A$

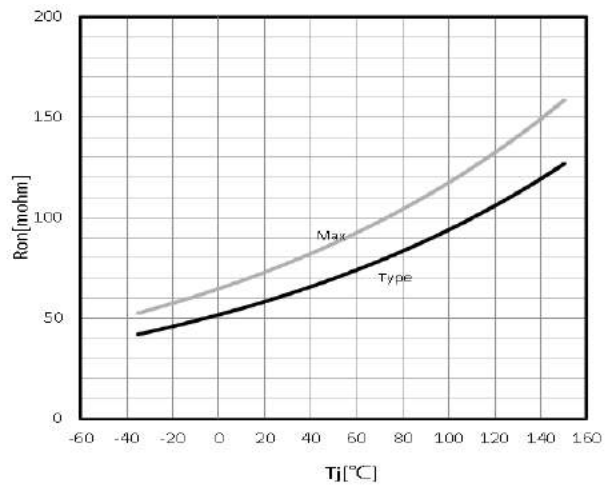


Figure 6 . Drain-source on-state resistance
 $R_{DS(on)}=f(T_j); I_D=5A; V_{GS}=10V$

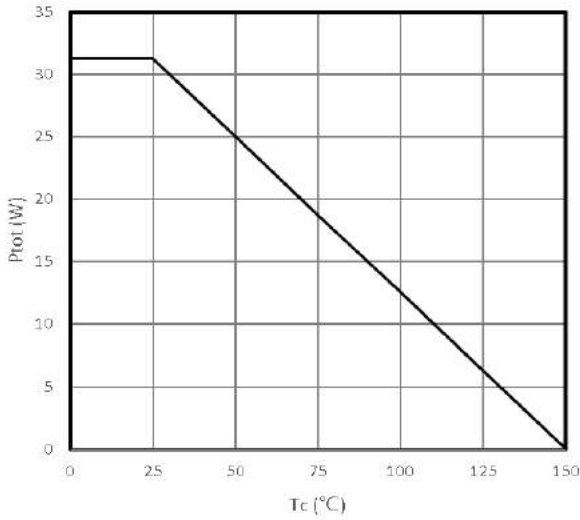


Figure 7 · Power Dissipation
 $P_{tot}=f(T_c)$

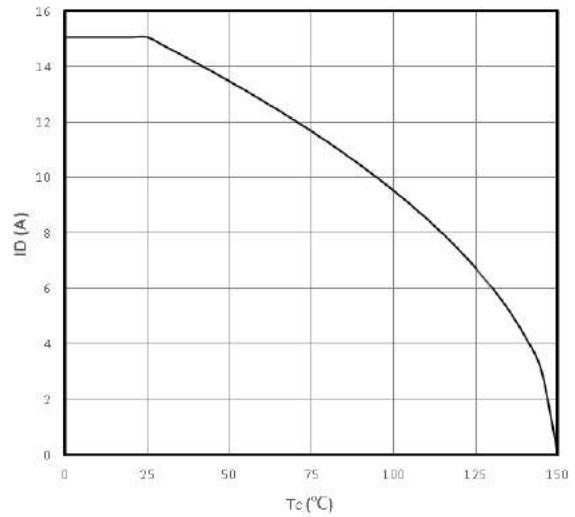


Figure 10. Maximum Drain Current
 $I_D=f(T_c)$

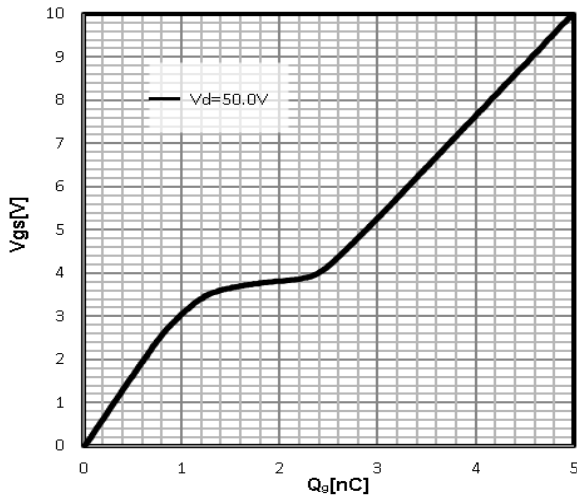


Figure 8. gate charge
 $V_{GS}=f(Q_g)$; $I_D=10A$

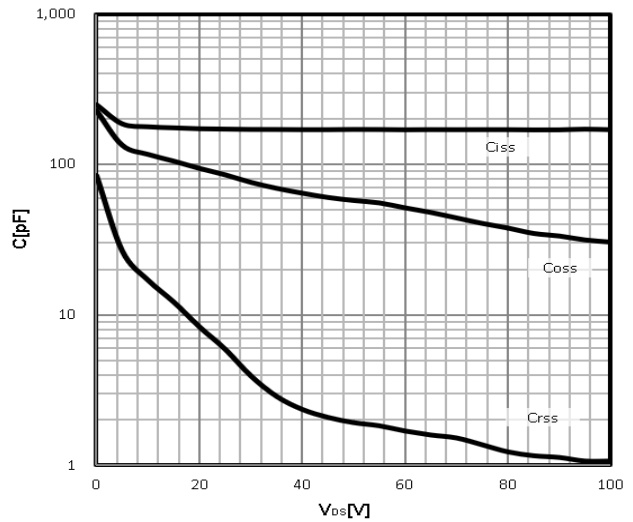


Figure 11. capacitances
 $C=f(V_{DS})$; $V_{GS}=0V$; $f=1MHz$

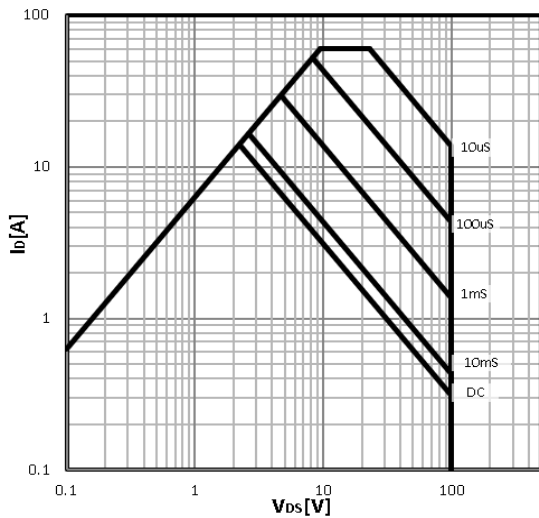


Figure 9. Safe operating area
 $I_D=f(V_{DS})$

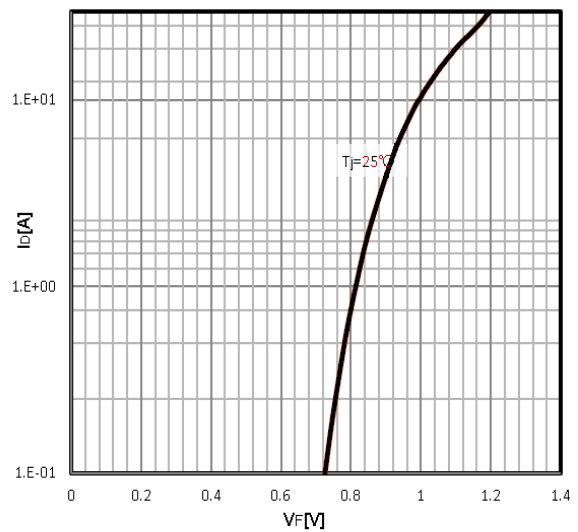
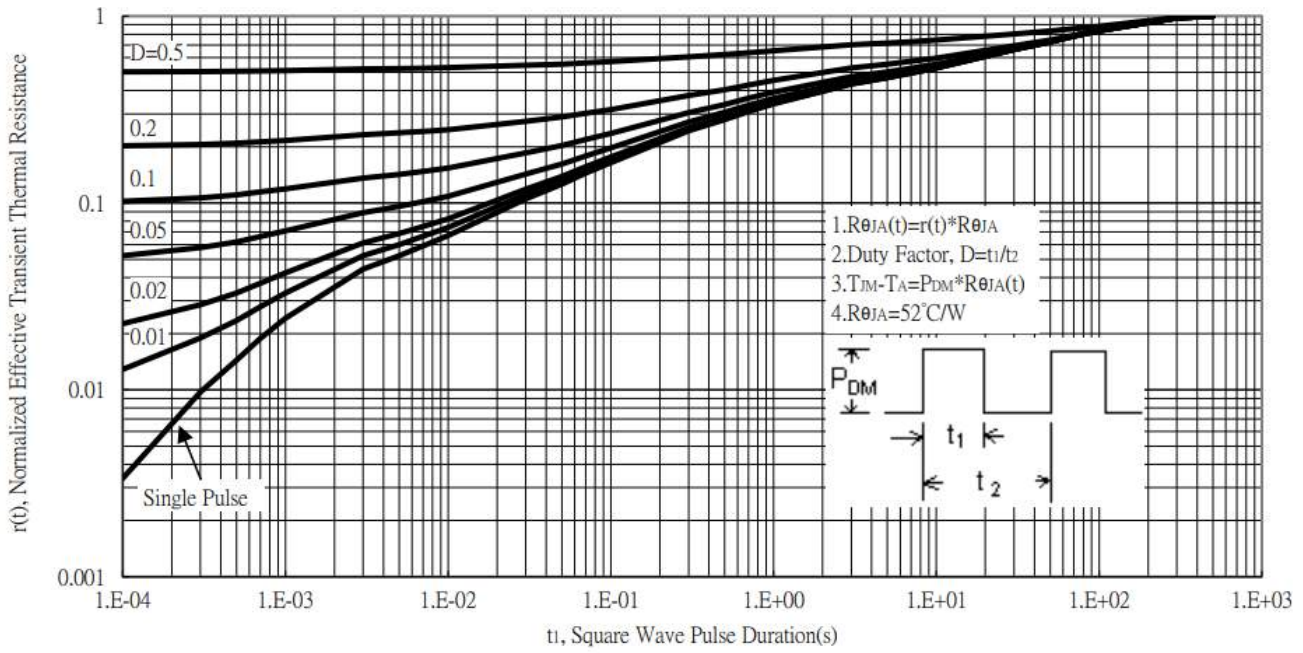


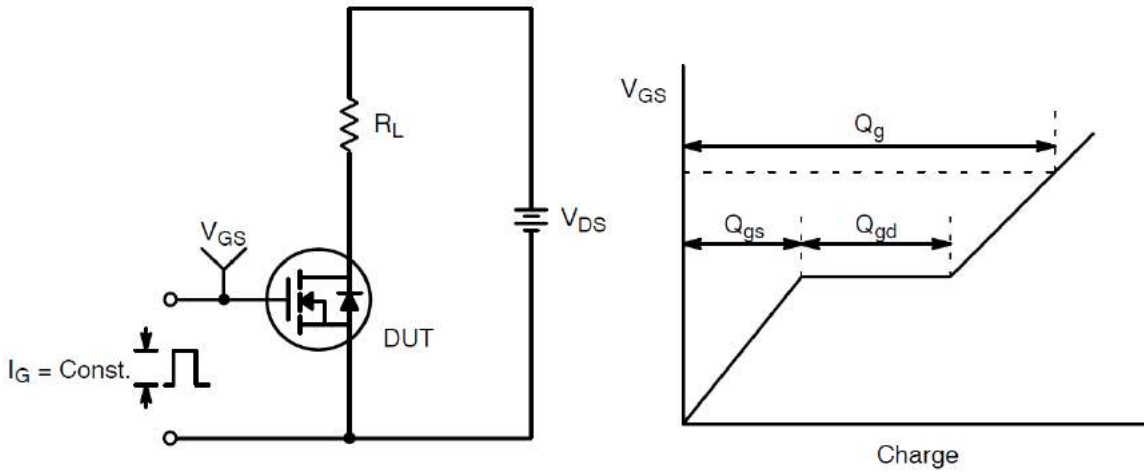
Figure 12. Body Diode Forward Voltage Variation
 $I_F=f(V_{GS})$



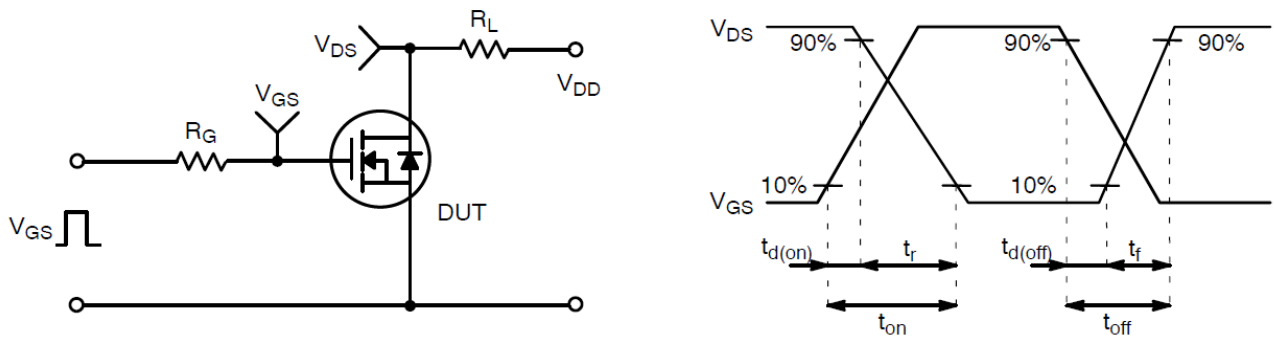
$$Z_{thJC} = f(t_p)$$

Figure 13. Max. transient thermal impedance

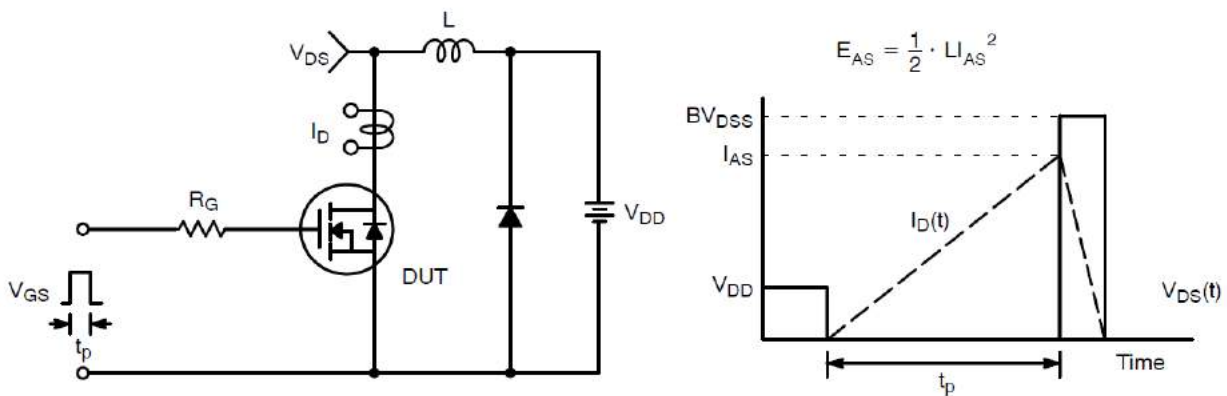
Test Circuit and Waveform:



Gate Charge Test Circuit & Waveform

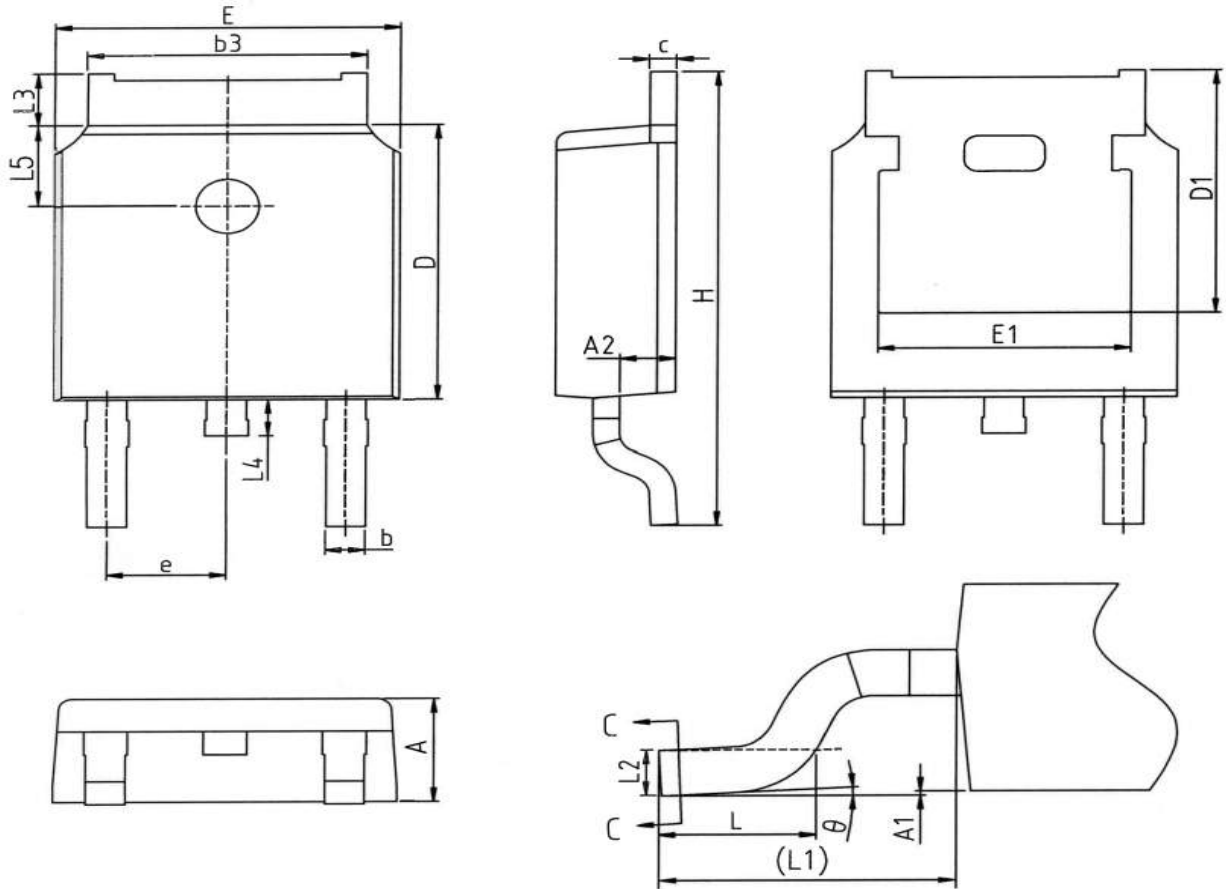


Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

TO-252 Package Information



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.12
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°

Customer Service

Sales and Service:

zj@ztasemi.com