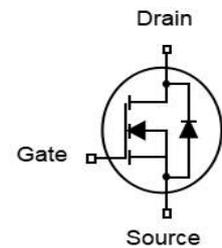


## Features

- N-Channel
- Low FOM  $R_{DS(on)} \times Q_{gd}$
- Ultra-low on-resistance
- Halogen-free
- RoHS compliant
- 100% EAS Tested

$V_{DS}$	150	V
$R_{DS(on),TYP@ V_{GS}=10V}$	5.6	m $\Omega$
$I_D$	140	A

TO-247



Part ID	Package Type	Marking	Packing
ZTG060N15T	TO-247	ZTG060N15T	600pcs/Tape

## Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
<b>Common Ratings (<math>T_c=25^\circ\text{C}</math> Unless Otherwise Noted)</b>				
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	150	V	
$T_J$	Maximum Junction Temperature	150	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
$I_{DM}$	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c = 25^\circ\text{C}$ 560	A	
<b>Mounted on Large Heat Sink</b>				
$I_D$	Drain Current-Continuous (Note 1)	$T_c = 25^\circ\text{C}$	140	A
		$T_c = 100^\circ\text{C}$	100	A
$P_D$	Maximum Power Dissipation	200	W	
$R_{\omega JC}$	Thermal Resistance-Junction to Case	0.47	$^\circ\text{C/W}$	
<b>Drain-Source Avalanche Ratings</b>				
EAS	Avalanche Energy, Single Pulsed (Note 3)	625	mJ	

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub>=25°C (unless otherwise stated)</b>						
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	150	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	--	--	1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	5.6	6.5	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V, f=1MHz	--	5926	--	pF
C <sub>oss</sub>	Output Capacitance		--	544	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	23	--	pF
R <sub>g</sub>	Gate Resistance f=1MHz	f=1MHz	--	2.2	--	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =75V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	83	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	24.8	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	16.9	--	nC
<b>Switching Characteristics</b>						
T <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =75V, R <sub>L</sub> =3.75Ω, R <sub>G</sub> =6Ω, V <sub>GS</sub> =10V	--	32	--	ns
T <sub>r</sub>	Turn-on Rise Time		--	49	--	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		--	80	--	ns
T <sub>f</sub>	Turn-Off Fall Time		--	46	--	ns
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
I <sub>SD</sub>	Source-Drain Current (Body Diode)		--	--	140	A
V <sub>SD</sub>	Forward on voltage	I <sub>S</sub> =20A, V <sub>GS</sub> =0V	--	--	1.2	V
T <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>D</sub> =15A, di/dt=100A/μs	--	90	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	360	--	nC

**Notes:**

1. The max drain current rating is package limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. L = 0.5 mH, V<sub>DD</sub> = 75V, I<sub>AS</sub> = 50 A, R<sub>G</sub> = 25 Ω, Starting T<sub>J</sub> = 25 °C

## Electrical Characteristics Diagrams

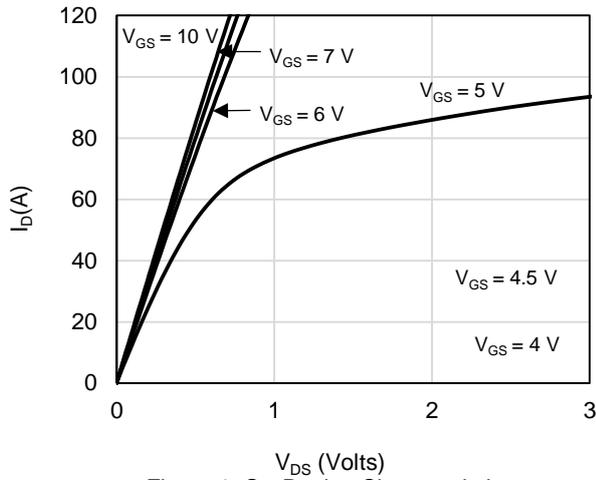


Figure 1: On-Region Characteristics

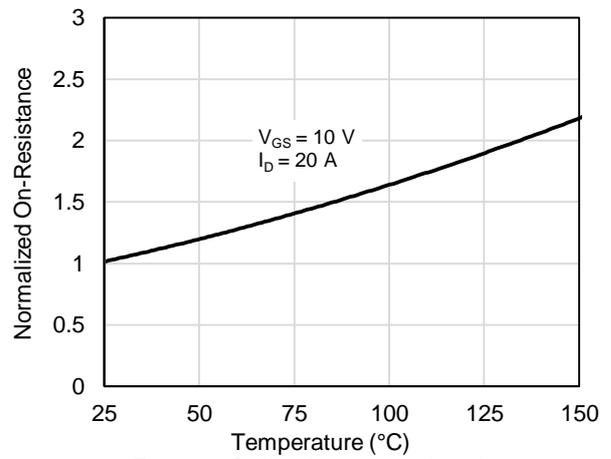


Figure 4: On-Resistance vs. Junction Temperature

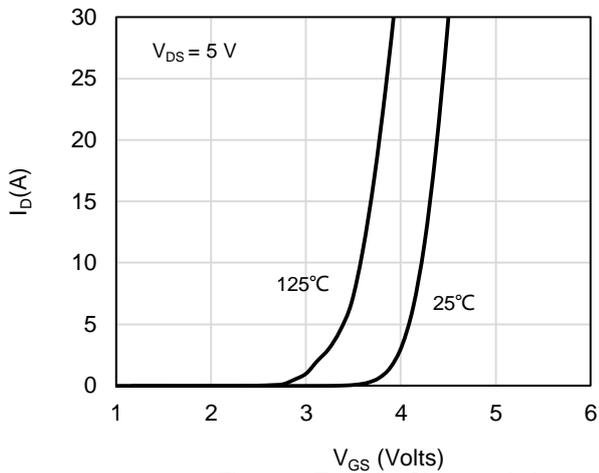


Figure 2: Transfer Characteristics

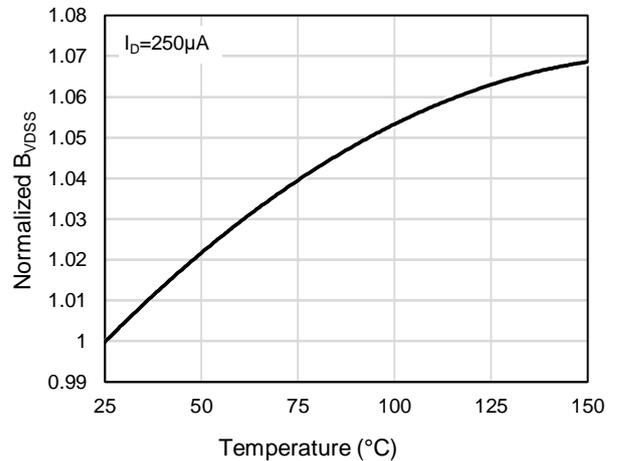


Figure 5: Breakdown Voltage vs. Junction Temperature

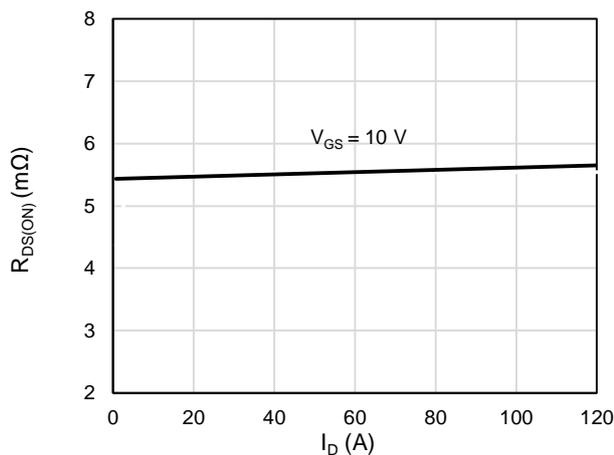


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

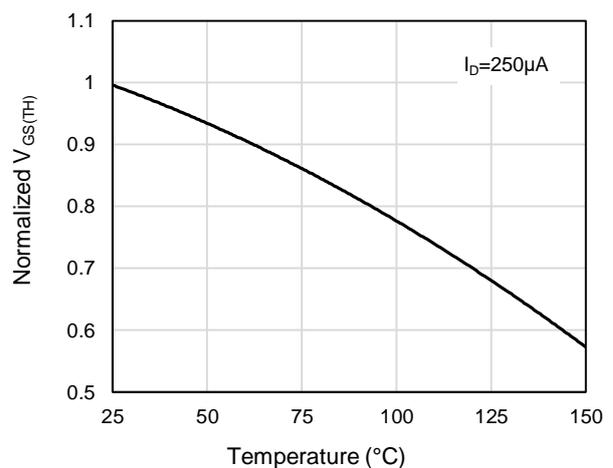


Figure 6: Threshold Voltage vs. Junction Temperature

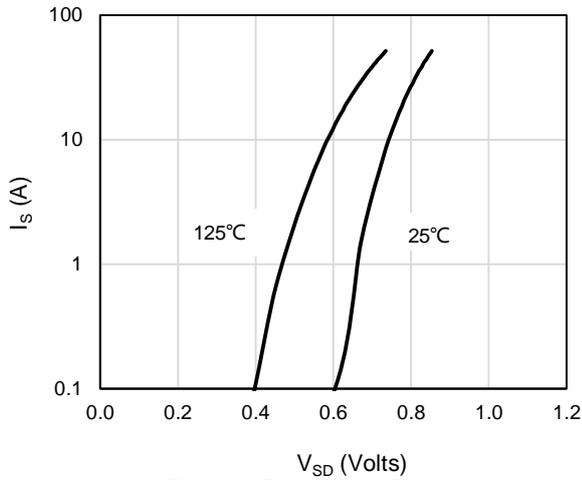


Figure 7: Body-Diode Characteristics

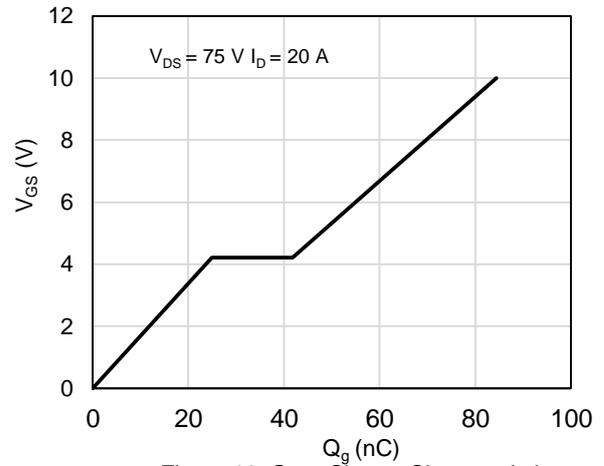


Figure 10: Gate-Charge Characteristics

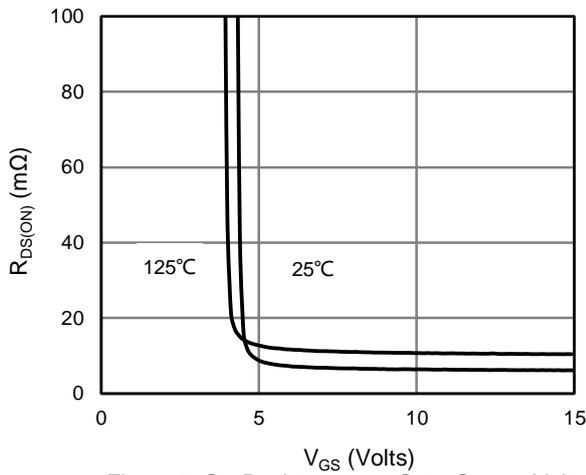


Figure 8: On-Resistance vs. Gate-Source Voltage

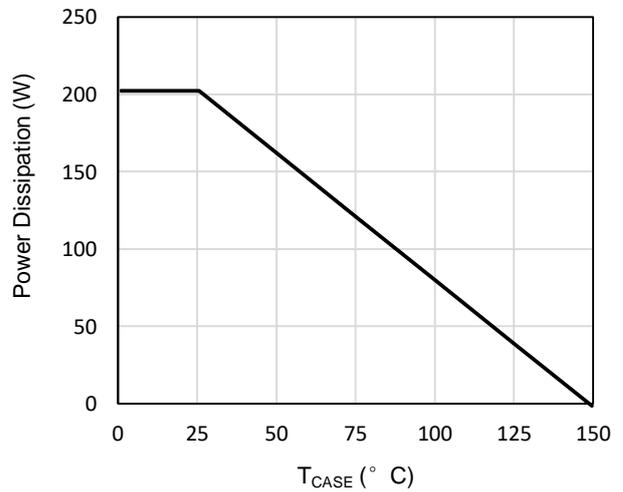


Figure 11: Power De-rating

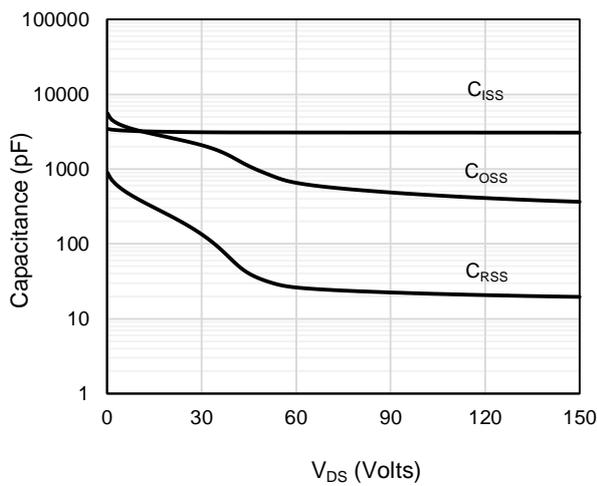


Figure 9: Capacitance Characteristics

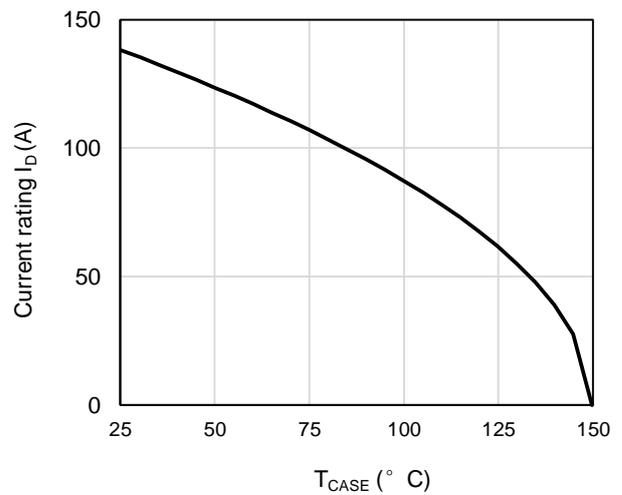


Figure 12: Current De-rating

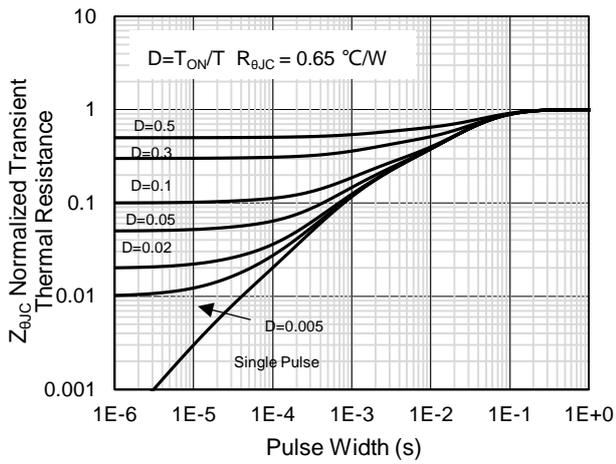


Figure 13: Normalized Maximum Transient Thermal Impedance

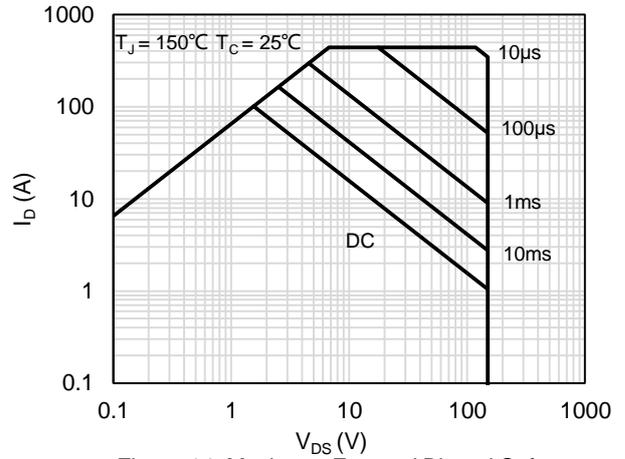
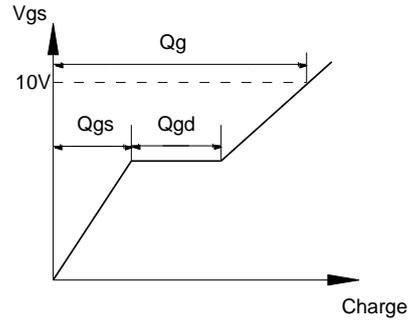
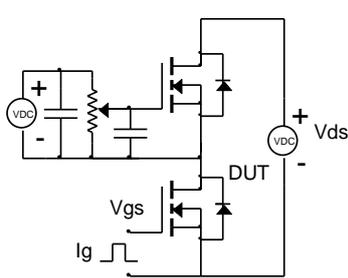


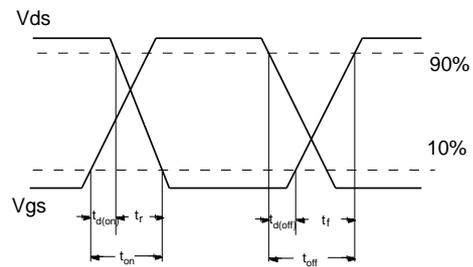
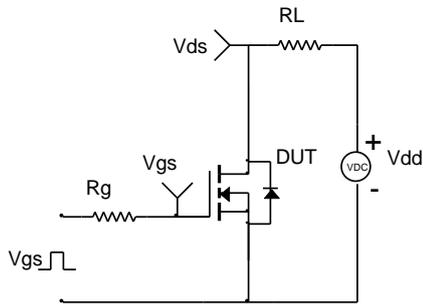
Figure 14: Maximum Forward Biased Safe Operating Area

## Test Circuit and Waveform

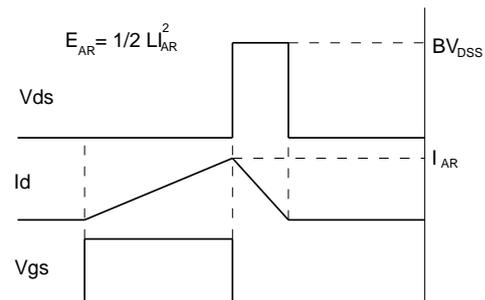
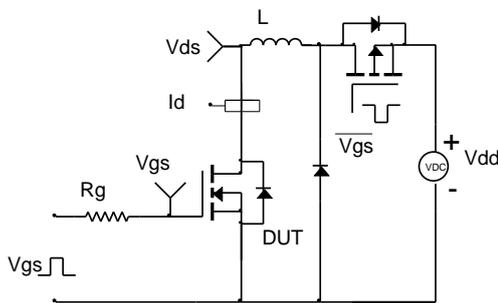
Gate Charge Test Circuit & Waveform



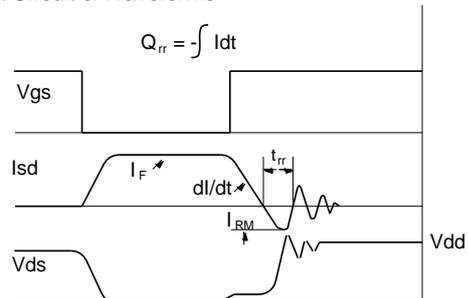
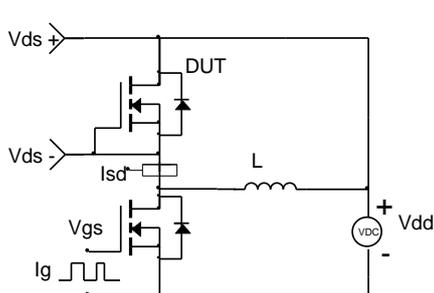
Resistive Switching Test Circuit & Waveforms



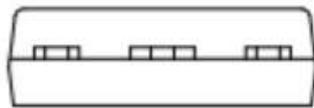
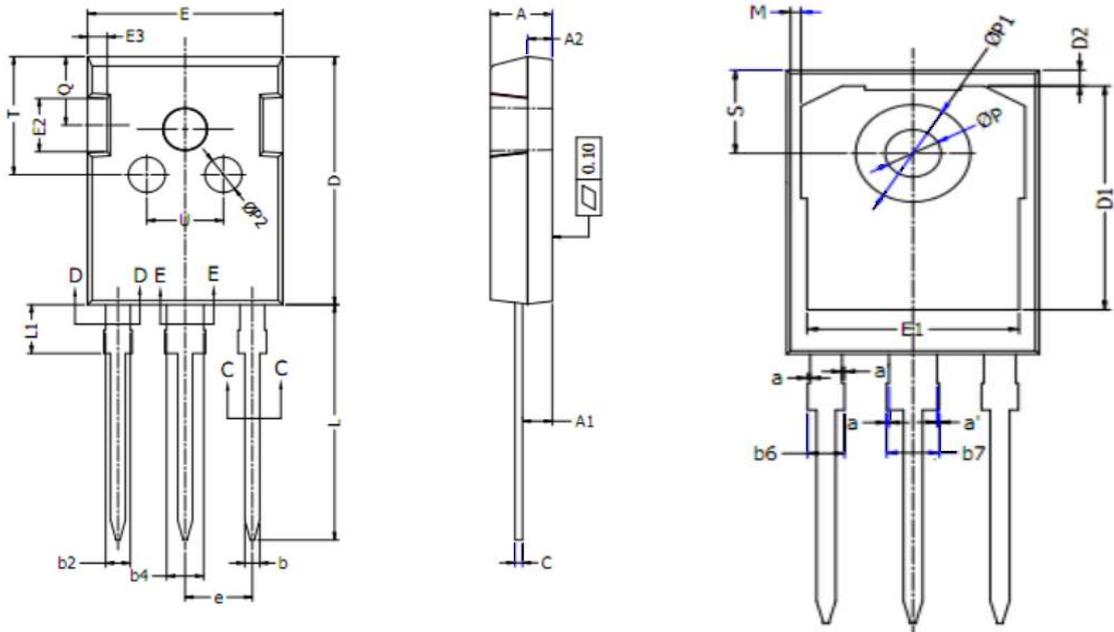
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## TO-247 Package Information



SYMBOL	MIN	NOM	MAX
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0	---	0.15
a'	0	---	0.15
b	1.16	---	1.26
b1	1.15	1.2	1.22
b2	1.96	---	2.06
b3	1.95	2.00	2.02
b4	2.96	---	3.06
b5	2.96	3.00	3.02
b6	---	---	2.25
b7	---	---	3.25
c	0.59	---	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	1.50	1.60	1.70
e	5.436 BSC		
L	19.80	19.92	20.10
L1	---	---	4.30
M	0.35	---	0.95
P	3.40	3.50	3.60
P1	7.00	---	7.40
P2	2.40	2.50	2.60
Q	5.60	---	6.00
S	6.05	6.15	6.25
T	9.80	---	10.20
U	6.00	---	6.40

## Customer Service

Sales and Service:

[zj@ztasemi.com](mailto:zj@ztasemi.com)