



Features

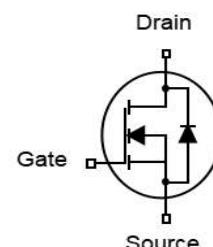
- N-Channel
- Low FOM $R_{DS(on)} \times Q_{gd}$
- Ultra-low on-resistance
- Halogen-free^(Note 1)
- RoHS compliant
- 100% EAS Tested

V_{DS}	150	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	5.5	$\text{m}\Omega$
I_D	130	A

TO-263



Part ID	Package Type	Marking	Packing
ZTG060N15B	TO-263	ZTG060N15B	1000pcs/Reel



Absolute Maximum Ratings $T_A=25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	150	V
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c=25^\circ\text{C}$	440
Mounted on Large Heat Sink			
I_D	Drain Current-Continuous (Note 1)	$T_c=25^\circ\text{C}$	130
		$T_c=100^\circ\text{C}$	70
P_D	Maximum Power Dissipation	190	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.65	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient (Note 4)	50	$^\circ\text{C}/\text{W}$
Drain-Source Avalanche Ratings			
EAS	Avalanche Energy, Single Pulsed (Note 3)	625	mJ



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	150	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=150\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
I _{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
V _{G(th)}	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	3.0	4.0	V
R _{D(on)}	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=20\text{A}$	--	5.5	6.4	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C _{iss}	Input Capacitance	$V_{DS}=75\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	5926	--	pF
C _{oss}	Output Capacitance		--	544	--	pF
C _{rss}	Reverse Transfer Capacitance		--	23	--	pF
R _g	Gate Resistance $f=1\text{MHz}$	$f=1\text{MHz}$	--	2.2	--	Ω
Q _g	Total Gate Charge	$V_{DS}=75\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$	--	83	--	nC
Q _{gs}	Gate-Source Charge		--	24.8	--	nC
Q _{gd}	Gate-Drain Charge		--	16.9	--	nC
Switching Characteristics						
T _{d(on)}	Turn-on Delay Time	$V_{DD}=75\text{V}, R_L=3.75\Omega, R_G=6\Omega, V_{GS}=10\text{V}$	--	32	--	ns
T _r	Turn-on Rise Time		--	49	--	ns
T _{d(off)}	Turn-Off Delay Time		--	80	--	ns
T _f	Turn-Off Fall Time		--	46	--	ns
Source-Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
I _{SD}	Source-Drain Current (Body Diode)		--	--	130	A
V _{SD}	Forward on voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$	--	--	1.0	V
T _{rr}	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_D=15\text{A}, dI/dt=100\text{A}/\mu\text{s}$	--	92	--	ns
Q _{rr}	Reverse Recovery Charge		--	364	--	nC

Notes:

1. The max drain current rating is package limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. L = 0.5 mH, V_{DD} = 75V, I_{AS} = 50 A, R_G = 25 Ω, Starting T_J = 25 °C
4. Mount on minimum PCB layout

Electrical Characteristics Diagrams

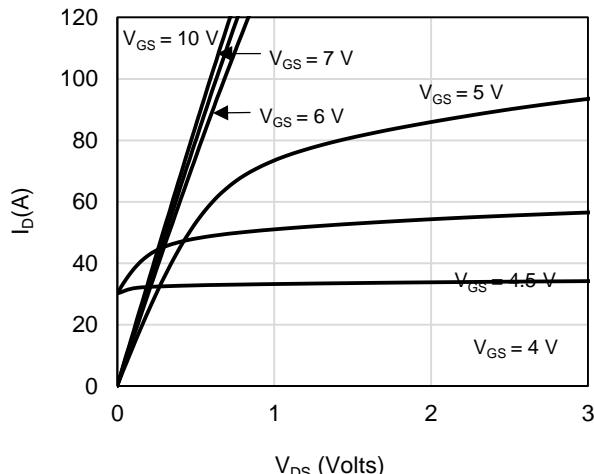


Figure 1: On-Region Characteristics

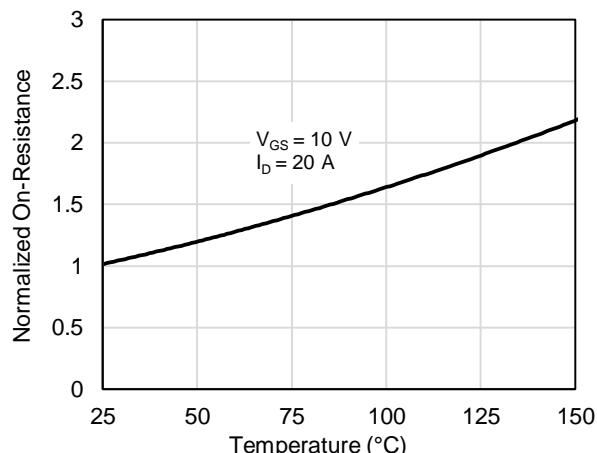


Figure 4: On-Resistance vs. Junction Temperature

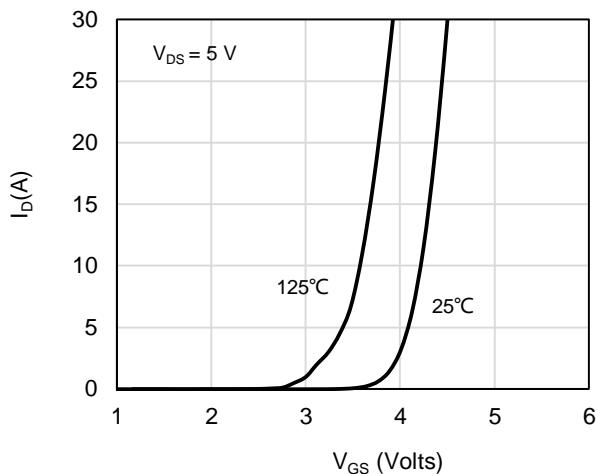


Figure 2: Transfer Characteristics

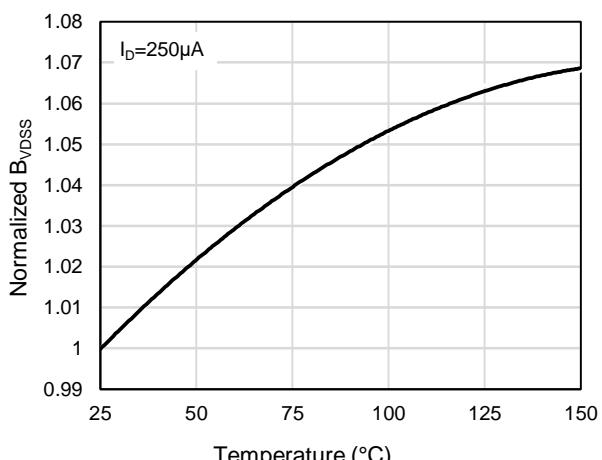


Figure 5: Breakdown Voltage vs. Junction Temperature

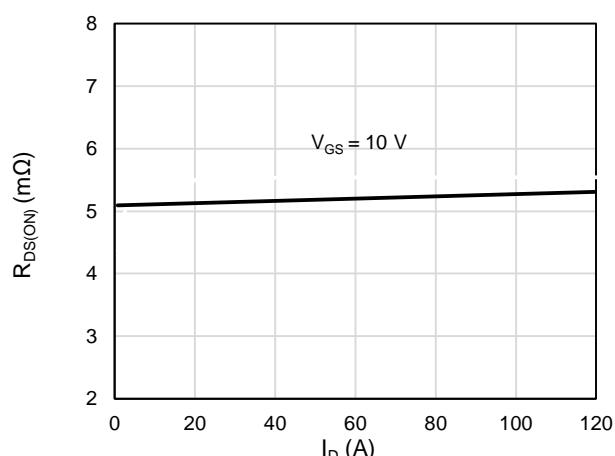


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

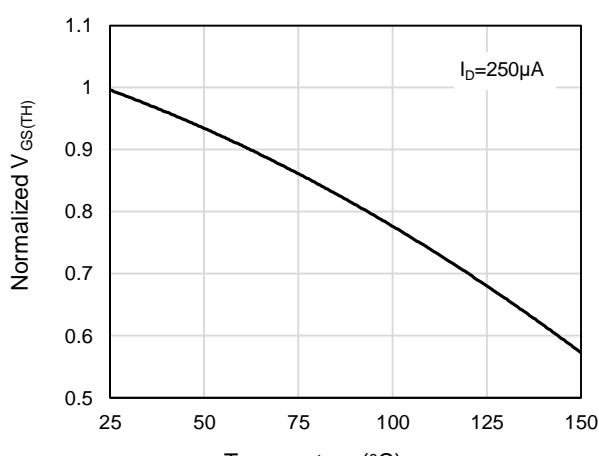


Figure 6: Threshold Voltage vs. Junction Temperature

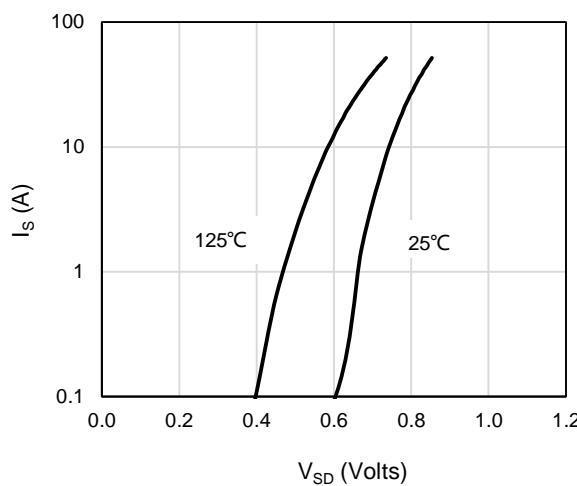


Figure 7: Body-Diode Characteristics

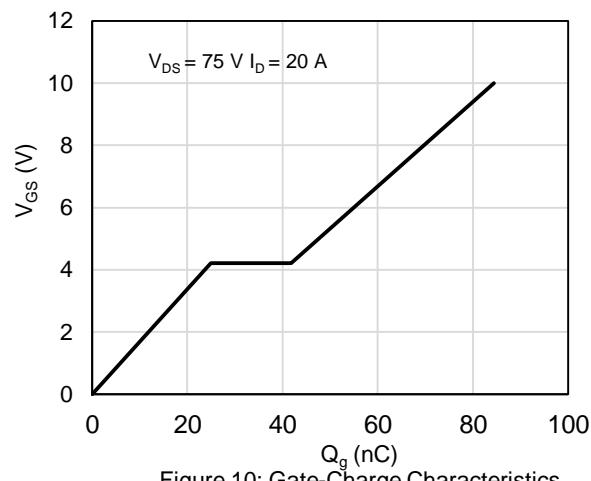


Figure 10: Gate-Charge Characteristics

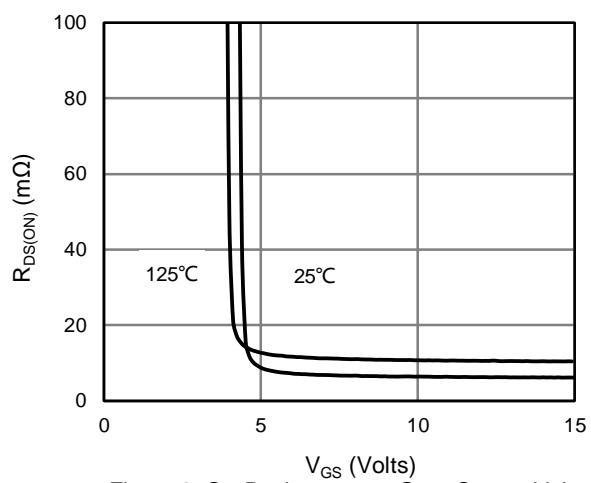


Figure 8: On-Resistance vs. Gate-Source Voltage

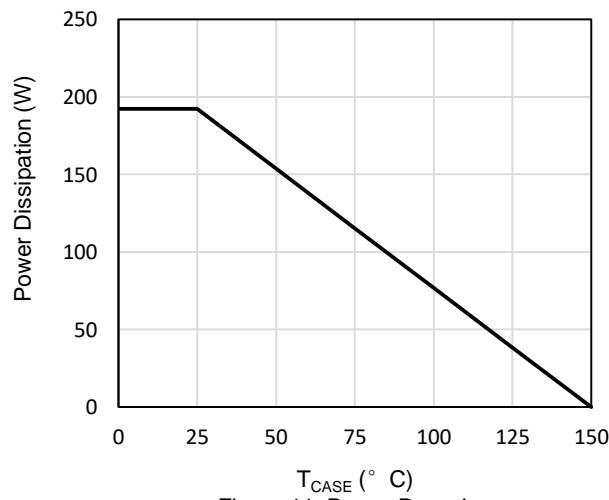


Figure 11: Power De-rating

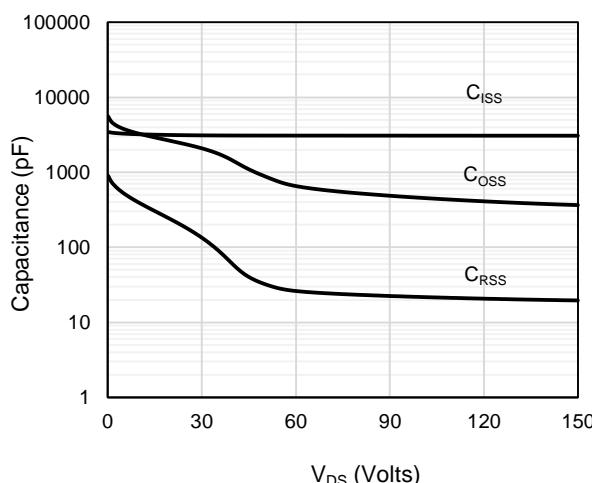


Figure 9: Capacitance Characteristics

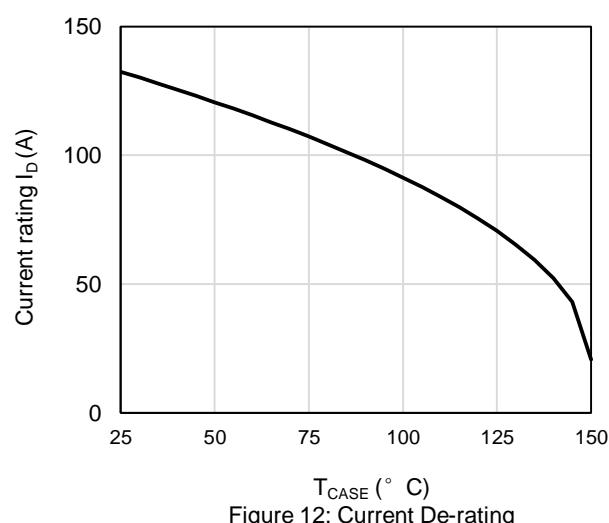


Figure 12: Current De-rating

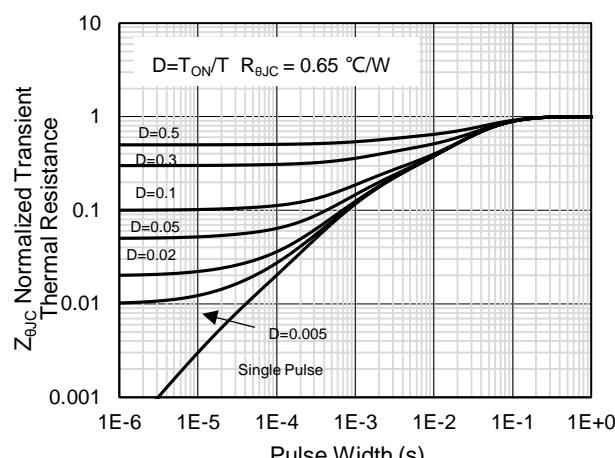


Figure 13: Normalized Maximum Transient Thermal Impedance

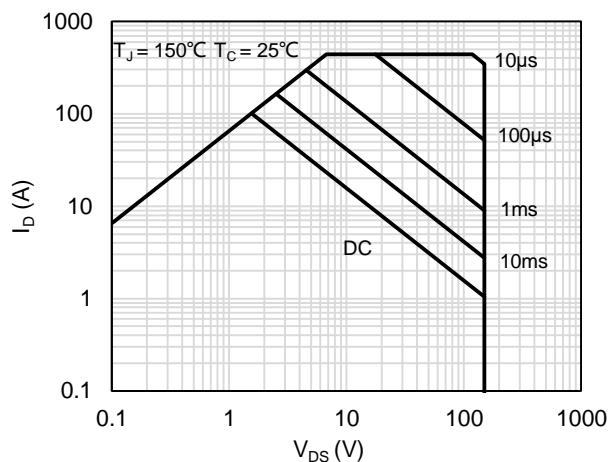
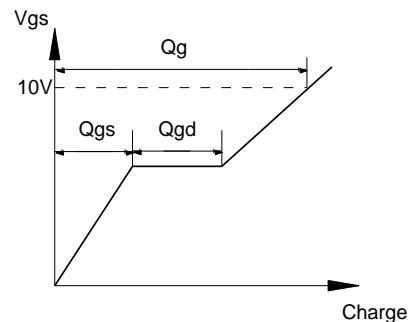
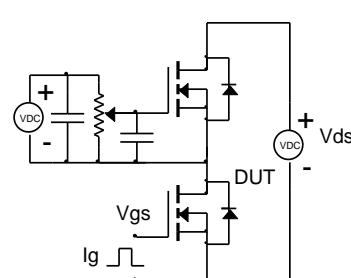


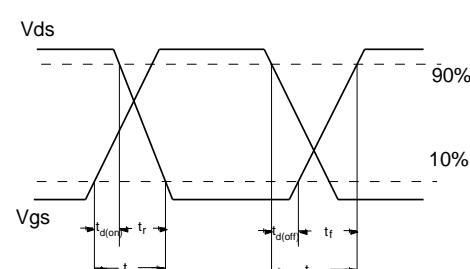
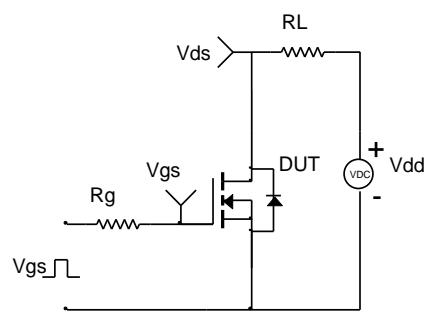
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

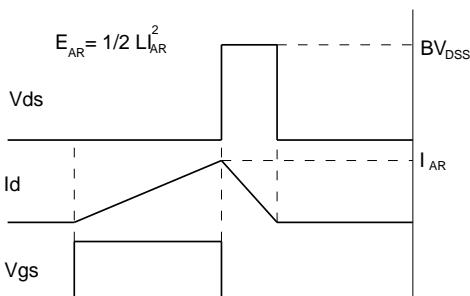
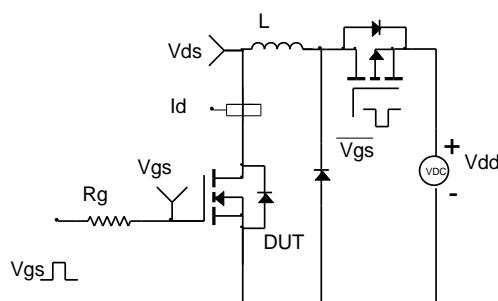
Gate Charge Test Circuit & Waveform



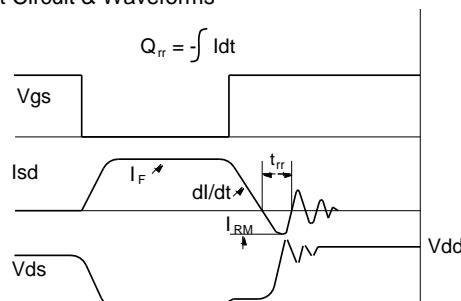
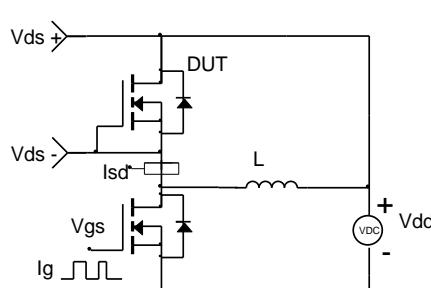
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

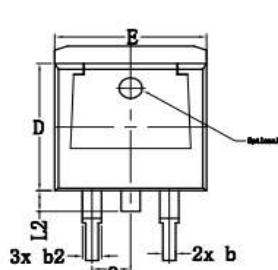


Diode Recovery Test Circuit & Waveforms

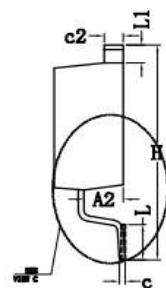




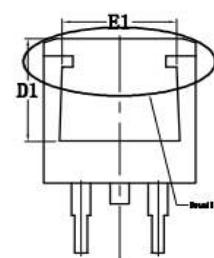
TO-263-2L Package Information



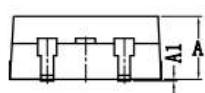
TOP VIEW



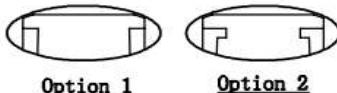
SIDE VIEW(Right)



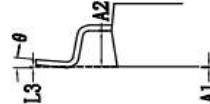
BOTTOM VIEW



SIDE VIEW(Front)



Detail D



VIEW C

SYMBOL	DIMENSIONS			
	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A	4.30	4.86	0.169	0.191
A1	0.00	0.25	0.00	0.010
A2	2.34	2.79	0.092	0.110
b	0.68	0.94	0.027	0.037
b2	1.15	1.35	0.045	0.053
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.38	9.45	0.330	0.372
D1	6.90	8.17	0.272	0.322
E	9.78	10.50	0.385	0.413
E1	6.50	8.60	0.256	0.339
H	14.61	15.88	0.575	0.625
e	2.54 BSC.		0.100 BSC.	
L	1.78	2.79	0.070	0.110
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
L3	0.254 BSC.		0.010 BSC.	
θ	0°	8°	0.00	0.315

Customer Service

Sales and Service:

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