

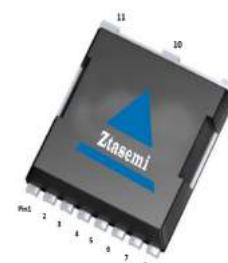


Features

- N-Channel
- Ultra-low on-resistance
- RoHS compliant
- Halogen-free
- 100% EAS Tested

V_{DS}	150	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	5.0	mΩ
I_D	165	A

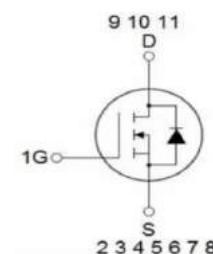
TOLL



RoHS



Part ID	Package Type	Marking	Packing
ZTG050N15L	TOLL	ZTG050N15L	2000pcs/Reel



Absolute Maximum Ratings $T_A=25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	150	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c=25^\circ\text{C}$	440	A
Mounted on Large Heat Sink				
I_D	(Note 1) Drain Current-Continuous	$T_c=25^\circ\text{C}$	165	A
		$T_c=100^\circ\text{C}$	70	A
P_D	Maximum Power Dissipation	190	W	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.65	°C/W	
$R_{\theta JA}$	Thermal Resistance Junction-Ambient (Note 4)	50	°C/W	
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed (Note 3)	625	mJ	



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	150	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=150\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	3.0	4.0	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=20\text{A}$	--	5.0	6.0	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C _{iss}	Input Capacitance	$V_{DS}=75\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	5926	--	pF
C _{oss}	OutputCapacitance		--	544	--	pF
C _{rss}	ReverseTransferCapacitance		--	23	--	pF
R _g	Gate Resistance	f=1MHz	--	2.2	--	Ω
Q _g	Total Gate Charge	$V_{DS}=75\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$	--	83	--	nC
Q _{gs}	Gate-SourceCharge		--	24.8	--	nC
Q _{gd}	Gate-DrainCharge		--	16.9	--	nC
Switching Characteristics						
T _{d(on)}	Turn-on Delay Time	$V_{DD}=75\text{V}, R_L=3.75\Omega, R_G=6\Omega, V_{GS}=10\text{V}$	--	32	--	ns
T _r	Turn-on Rise Time		--	49	--	ns
T _{d(off)}	Turn-Off Delay Time		--	80	--	ns
T _f	Turn-Off Fall Time		--	46	--	ns
Source- Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
I _{SD}	Source-Drain Current (Body Diode)		--	--	165	A
V _{SD}	Forward on voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$	--	--	1.0	V
T _{rr}	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_D=15\text{A}, V_{DD}=75\text{V}$ $di/dt=100\text{A}/\mu\text{s}$	--	92	--	ns
Q _{rr}	Reverse Recovery Charge		--	364	--	nC

Notes:

1. The max drain current rating is package limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. L = 0.5 mH, $V_{DD} = 75\text{V}$, $I_{AS} = 50\text{ A}$, $R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$
4. Mount on minimum PCB layout



Electrical Characteristics Diagrams

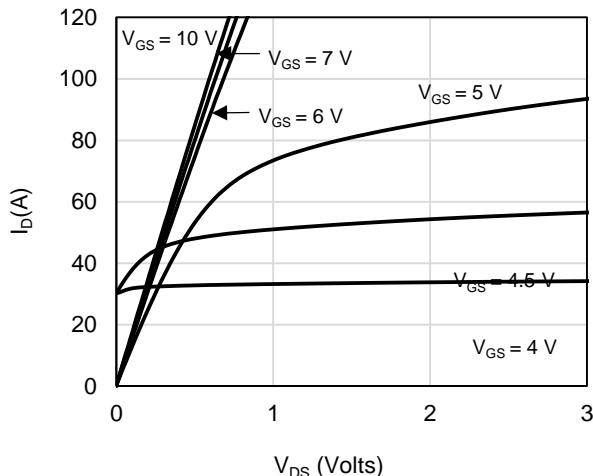


Figure 1: On-Region Characteristics

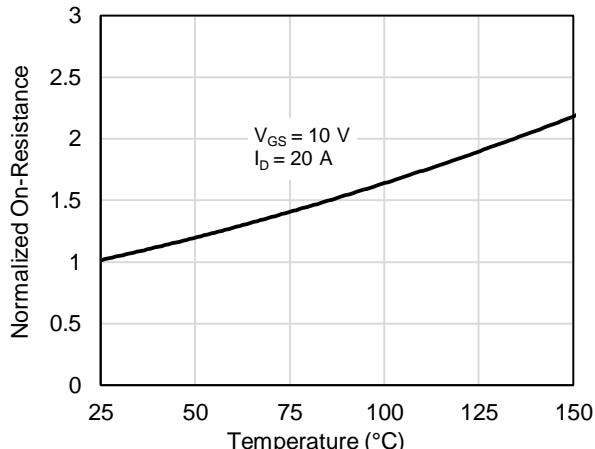


Figure 4: On-Resistance vs. Junction Temperature

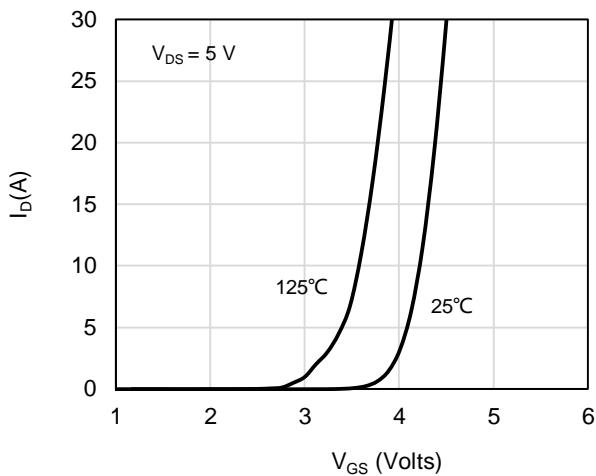


Figure 2: Transfer Characteristics

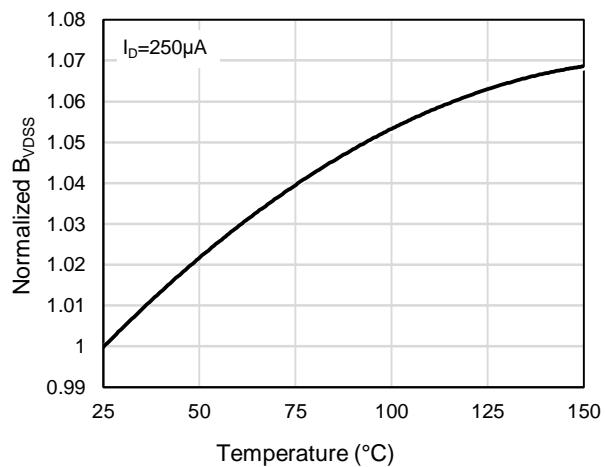


Figure 5: Breakdown Voltage vs. Junction Temperature

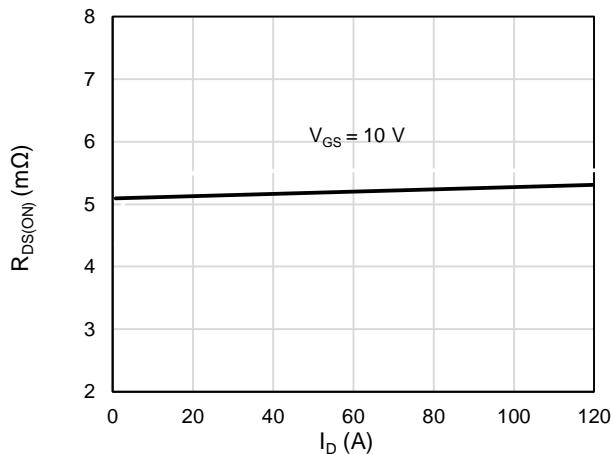


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

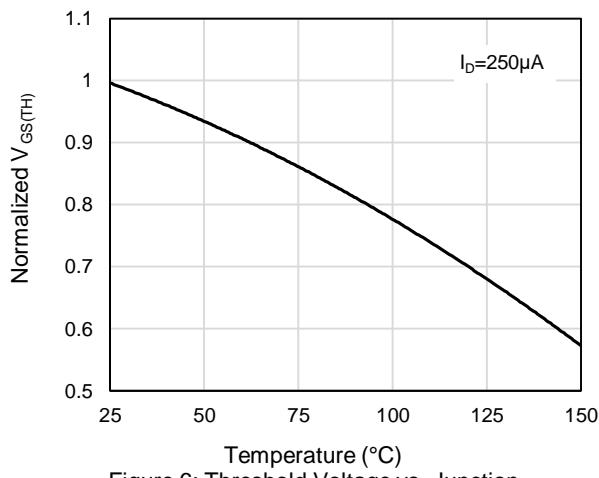


Figure 6: Threshold Voltage vs. Junction Temperature

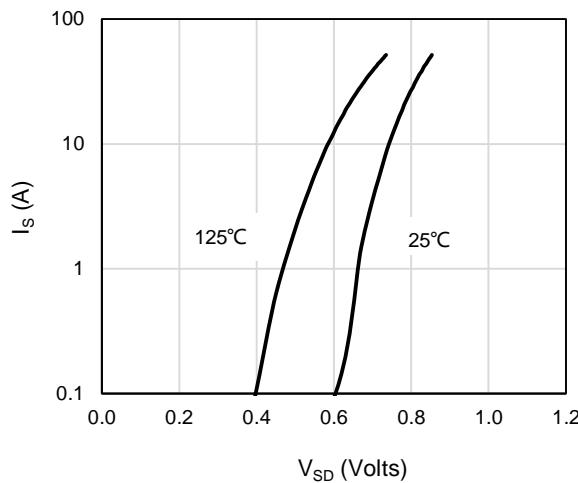


Figure 7: Body-Diode Characteristics

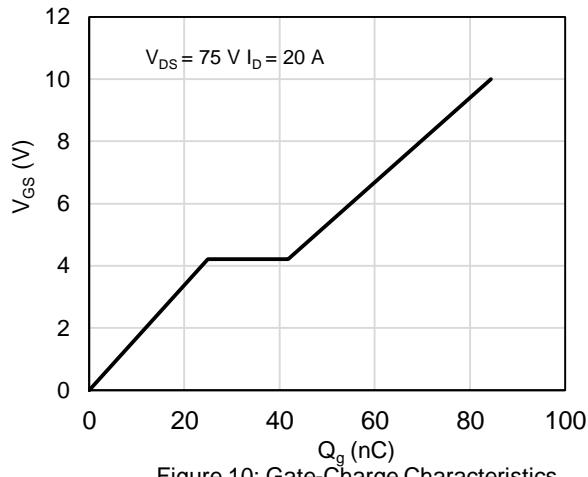


Figure 10: Gate-Charge Characteristics

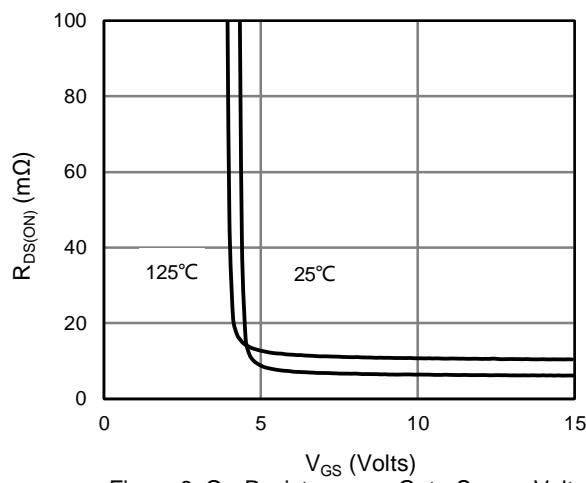


Figure 8: On-Resistance vs. Gate-Source Voltage

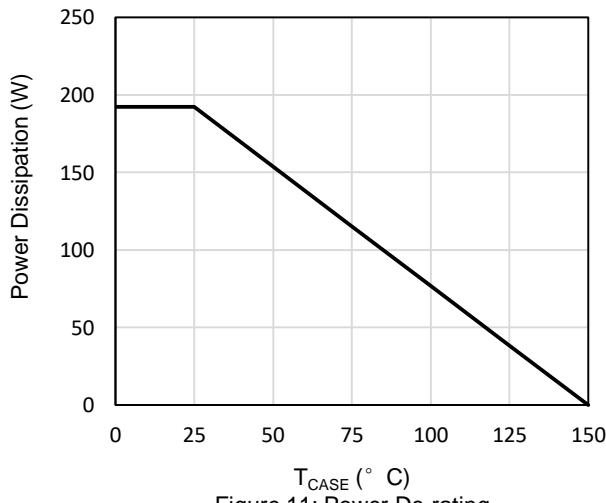


Figure 11: Power De-rating

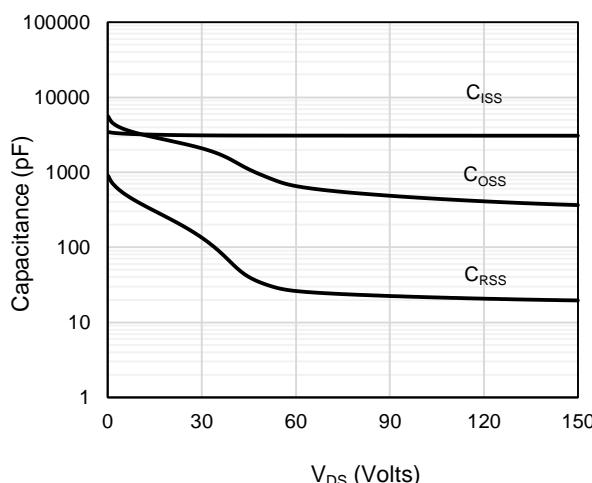


Figure 9: Capacitance Characteristics

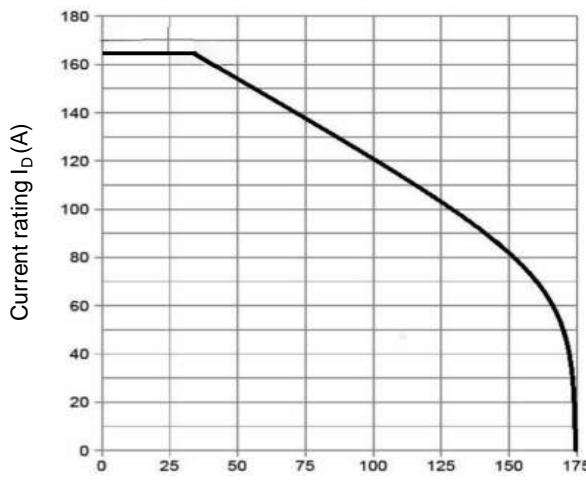


Figure 12: Current De-rating

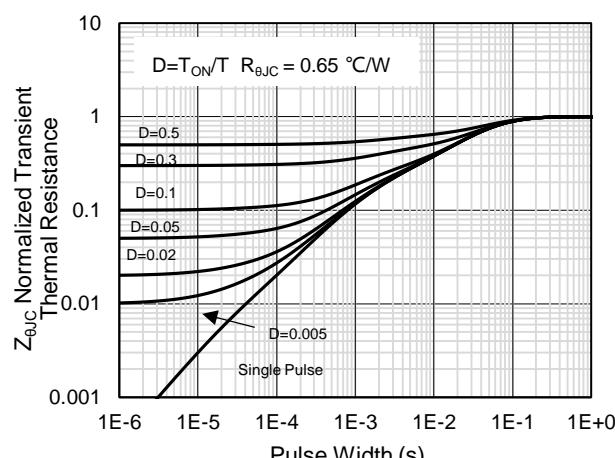


Figure 13: Normalized Maximum Transient Thermal Impedance

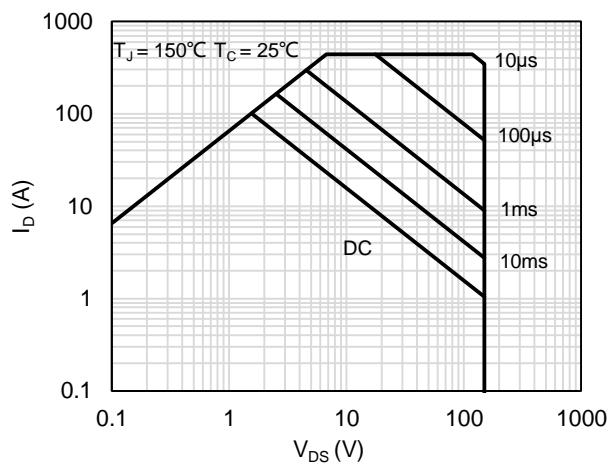
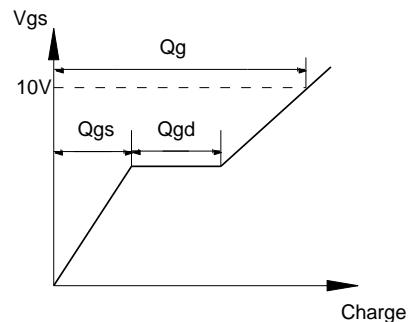
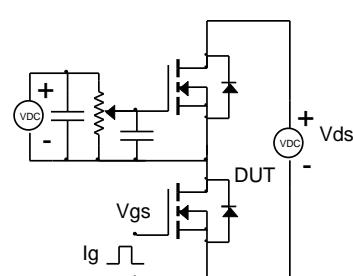


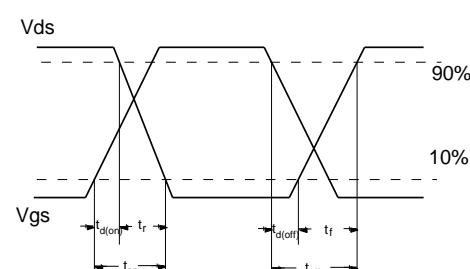
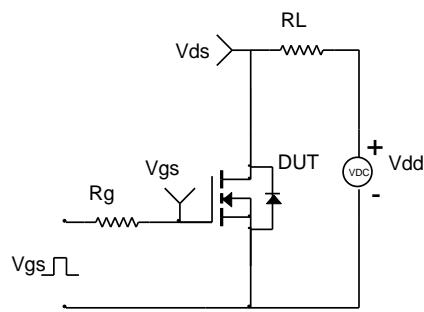
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

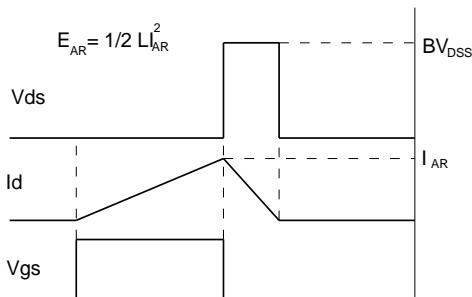
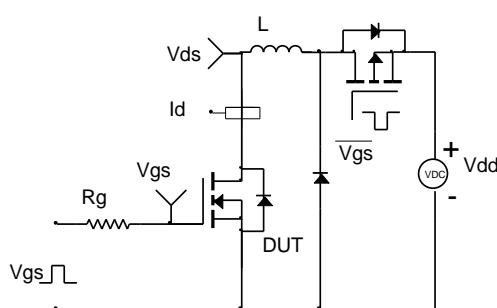
Gate Charge Test Circuit & Waveform



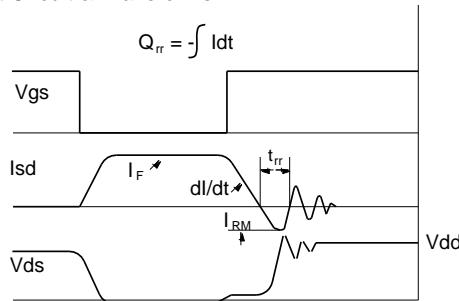
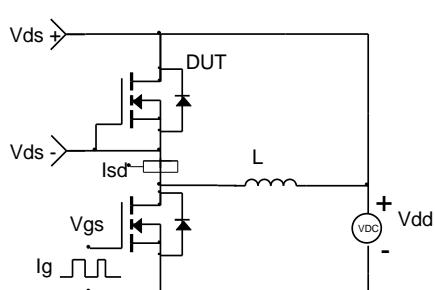
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

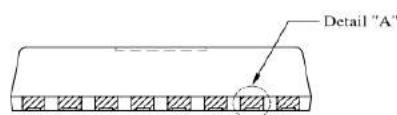
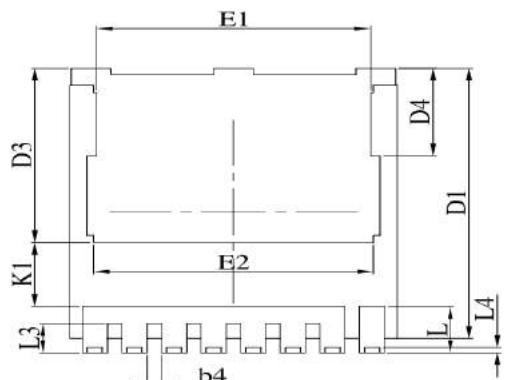
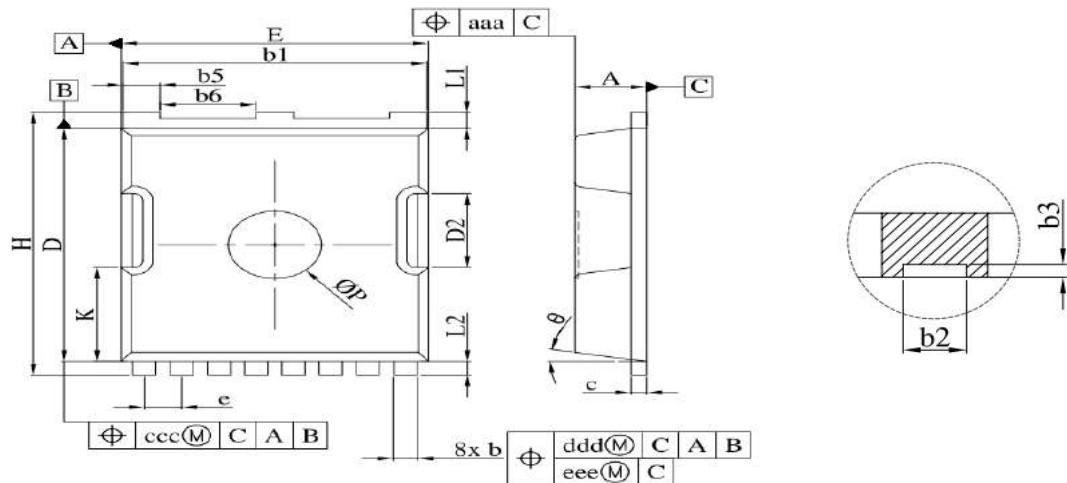


Diode Recovery Test Circuit & Waveforms





TOLL-8L Package Information



SYMBOL	COMMON		
	MILLIMETER		
	MIN.	NOMINAL	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.45	0.55
b3	0.05	0.100	0.35
b4	0.30	0.40	0.50
b5	1.10	1.20	1.30
b6	3.00	3.10	3.20
c	0.40	0.50	0.60
D	10.28	10.38	10.55
D1	10.98	11.08	11.18
D2	3.20	3.30	3.40
D3	7.00	7.15	7.30
D4	3.44	3.59	3.74
e	1.10	1.20	1.30
E	9.80	9.90	10.00
E1	8.20	8.30	8.40
E2	8.35	8.50	8.65
H	11.50	11.68	11.85
K	4.08	4.18	4.28
K1	2.45	---	---
L	1.60	1.90	2.10
L1	0.50	0.70	0.90
L2	0.50	0.60	0.70
L3	1.00	1.20	1.30
L4	0.13	0.23	0.33
P	2.85	3.00	3.15
θ	10° REF		
aaa	0.20		
ccc	0.20		
ddd	0.25		
eee	0.20		

Customer Service

Sales and Service:

zj@ztasemi.com