



Features

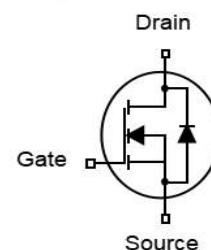
- N-Channel
- Low FOM $R_{DS(on)} \times Q_{gd}$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant
- 100% EAS Tested

V_{DS}	100	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	3.5	$\text{m}\Omega$
$R_{DS(on),TYP} @ V_{GS}=4.5\text{ V}$	4.5	$\text{m}\Omega$
I_D	130	A

TO-263



Part ID	Package Type	Marking	Packing
ZTG035N10B	TO-263	ZTG035N10B	1000pcs/Reel



Absolute Maximum Ratings $T_A=25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	100	V
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c=25^\circ\text{C}$	520
Mounted on Large Heat Sink			
I_D	Drain Current-Continuous	$T_c=25^\circ\text{C}$	130
		$T_c=100^\circ\text{C}$	84
P_D	Maximum Power Dissipation	131.5	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.95	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	50	$^\circ\text{C}/\text{W}$
Drain-Source Avalanche Ratings			
EAS	Avalanche Energy, Single Pulsed (Note 2)	502	mJ



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	100	--	--	V
Idss	Zero Gate Voltage Drain Current	$V_{DS}=100\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
IGSS	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.5	3.0	3.5	V
RDS(on)	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=56\text{A}$	--	3.5	3.9	$\text{m}\Omega$
RDS(on)	Drain-Source On-State Resistance	$V_{GS}=4.5\text{V}, I_D=20\text{A}$	--	4.5	5.9	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
Ciss	Input Capacitance	$V_{DS}=50\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	3802	--	pF
Coss	Output Capacitance		--	1235	--	pF
Crss	Reverse Transfer Capacitance		--	28	--	pF
Rg	Gate Resistance $f=1\text{MHz}$	$f=1\text{MHz}$	--	0.9	--	Ω
Qg	Total Gate Charge	$V_{DS}=50\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$	--	65	--	nC
Qgs	Gate-Source Charge		--	16	--	nC
Qgd	Gate-Drain Charge		--	16	--	nC
Switching Characteristics						
Td(on)	Turn-on Delay Time	$V_{DS}=50\text{V}, I_D=20\text{A}, R_G=5\Omega, V_{GS}=10\text{V}$	--	25	--	ns
Tr	Turn-on Rise Time		--	33	--	ns
Td(off)	Turn-Off Delay Time		--	93	--	ns
Tf	Turn-Off Fall Time		--	75	--	ns
Source-Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
ISD	Source-Drain Current (Body Diode)		--	--	130	A
VSD	Forward on voltage	$I_S=20\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
Trr	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_F=20\text{A}, \frac{dI}{dt}=100\text{A}/\mu\text{s}$	--	82	--	ns
Qrr	Reverse Recovery Charge		--	182	--	nC

Notes

- Repetitive rating; pulse width limited by maximum junction temperature
- $L=0.2\text{mH}$, $R_g=25\Omega$, Starting $T_J=25^\circ\text{C}$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

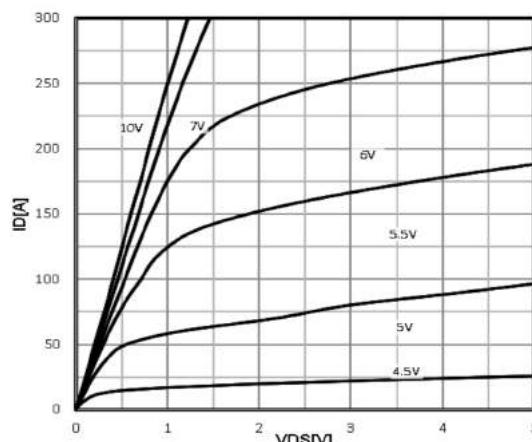


Figure 1 output characteristics
 $I_D=f(V_{DS})$

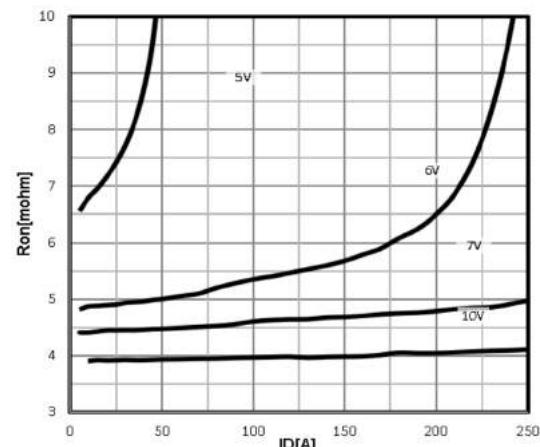


Figure 4 drain -source on resistance
 $R_{DS(on)}=f(I_D)$

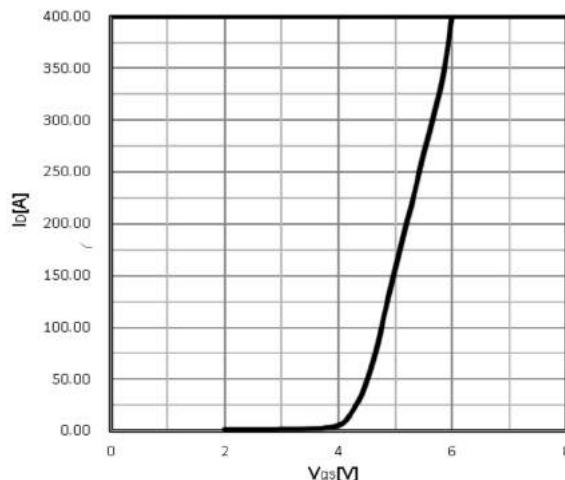


Figure 2 transfer characteristics
 $I_D=f(V_{GS})$

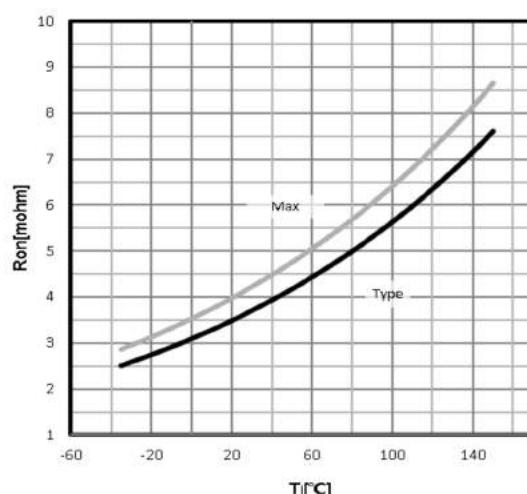


Figure 5 Drain-source on-state resistance
 $R_{DS(on)}=f(T_J); I_D=56\text{A}; V_{GS}=10\text{V}$

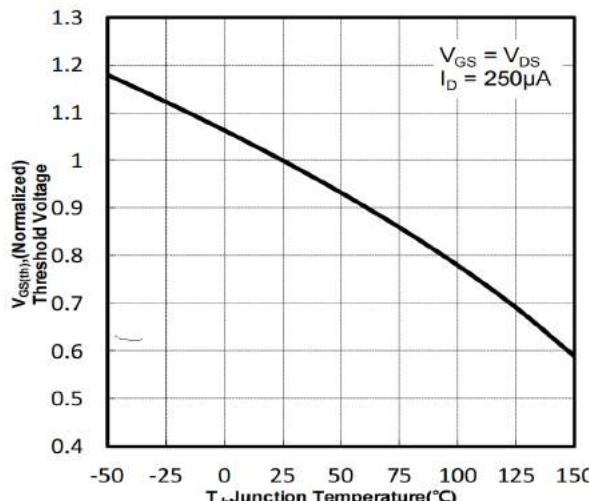


Figure 3 Gate Threshold Voltage
 $V_{TH}=f(T_J); I_D=250\mu\text{A}$

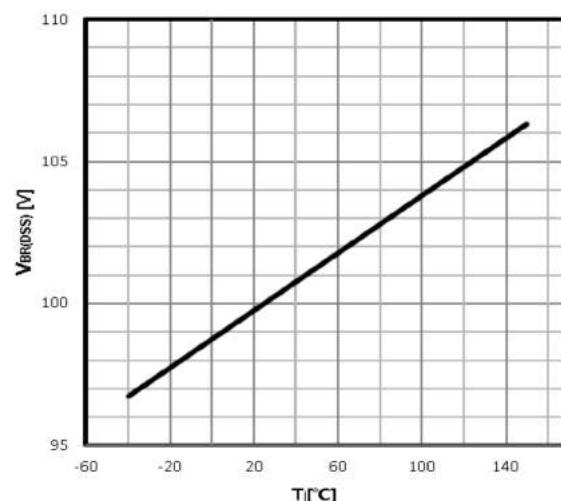


Figure 6 Drain-source breakdown voltage
 $V_{BR(DSS)}=f(T_J); I_D=250\mu\text{A}$



Typical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

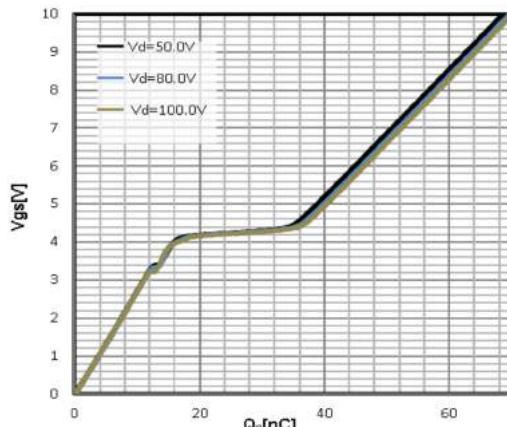


Figure 7 gate charge
 $V_{GS} = f(Q_g)$; $I_D = 20\text{A}$

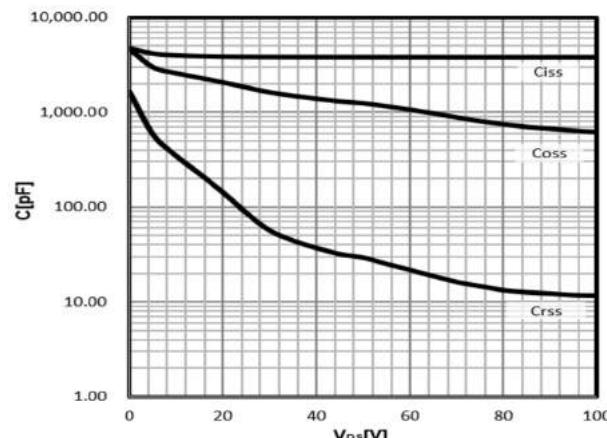


Figure 10 capacitances
 $C = f(V_{DS})$; $V_{GS} = 0\text{V}$; $f = 1\text{MHz}$

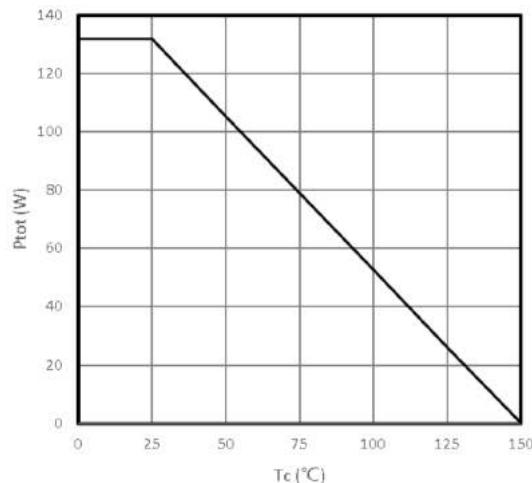


Figure 8 Power Dissipation
 $P_{tot} = f(T_c)$

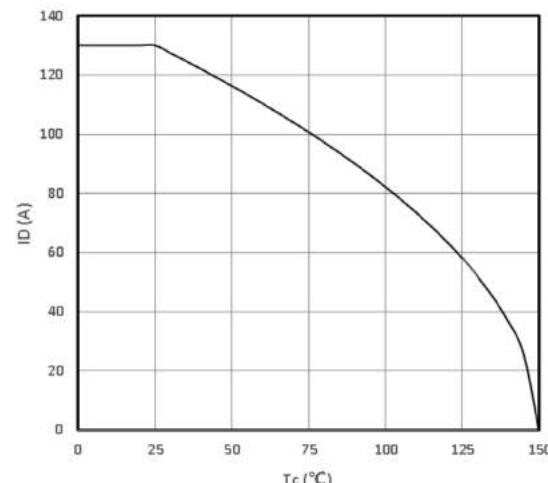


Figure 11 Maximum Drain Current
 $I_D = f(T_c)$

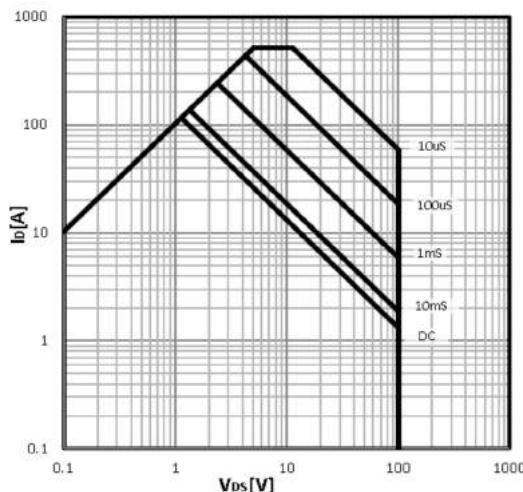


Figure 9 Safe operating area
 $I_D = f(V_{DS})$

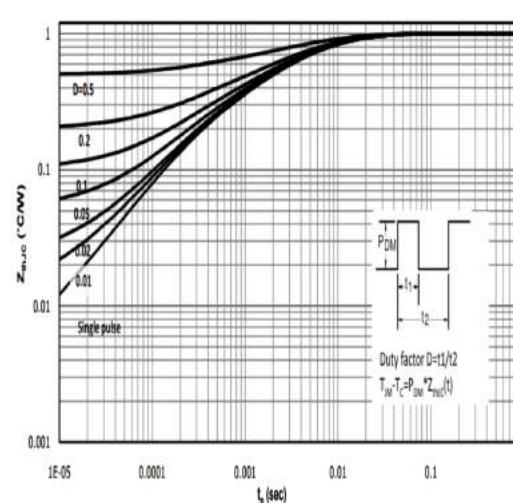


Figure 12 Max. transient thermal impedance
 $Z_{thJC} = f(t_p)$

Figure A: Gate Charge Test Circuit and Waveform

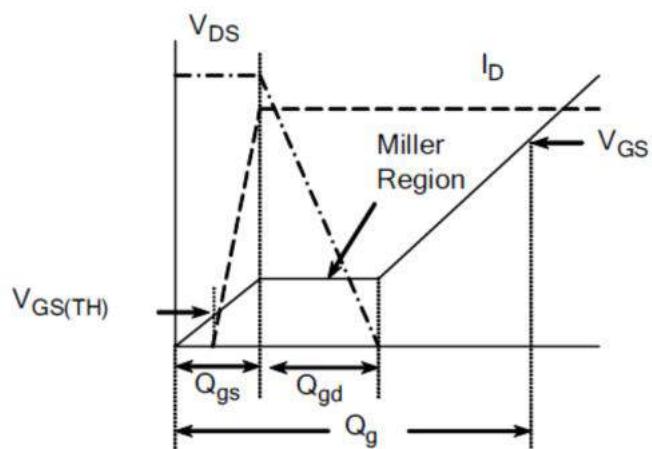
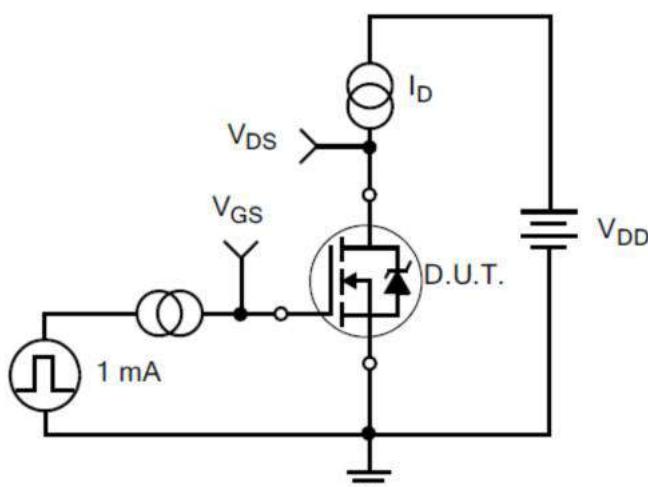


Figure B: Resistive Switching Test Circuit and Waveform

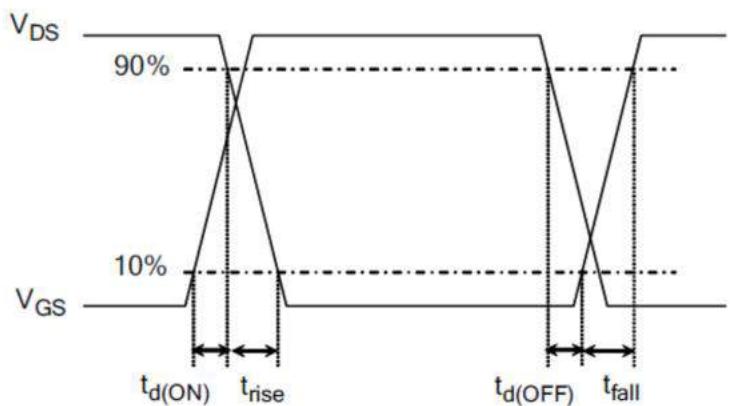
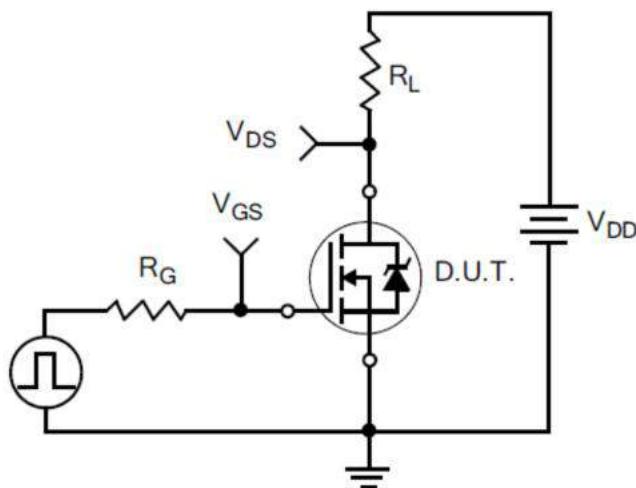
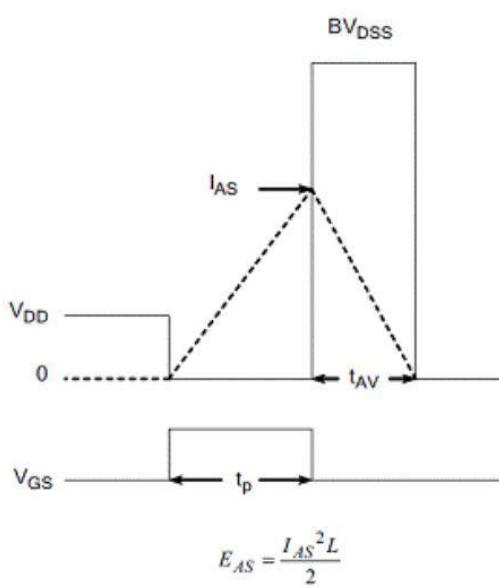
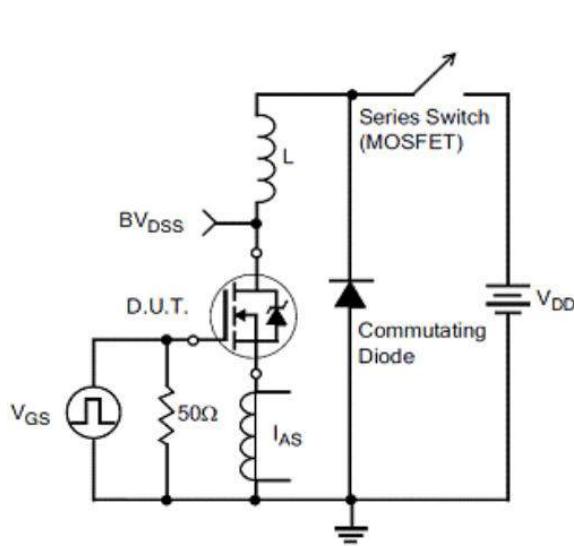
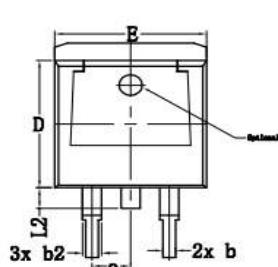


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

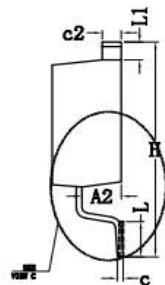




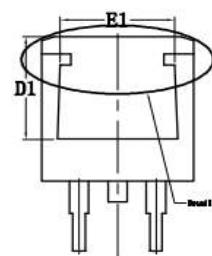
TO-263-2L Package Information



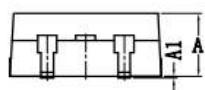
TOP VIEW



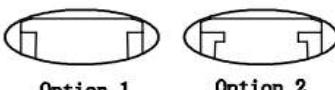
SIDE VIEW(Right)



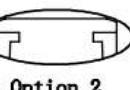
BOTTOM VIEW



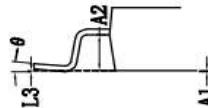
SIDE VIEW(Front)



Option 1



Detail D



VIEW C

SYMBOL	DIMENSIONS			
	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A	4.30	4.86	0.169	0.191
A1	0.00	0.25	0.00	0.010
A2	2.34	2.79	0.092	0.110
b	0.68	0.94	0.027	0.037
b2	1.15	1.35	0.045	0.053
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.38	9.45	0.330	0.372
D1	6.90	8.17	0.272	0.322
E	9.78	10.50	0.385	0.413
E1	6.50	8.60	0.256	0.339
H	14.61	15.88	0.575	0.625
e	2.54 BSC.		0.100 BSC.	
L	1.78	2.79	0.070	0.110
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
L3	0.254 BSC.		0.010 BSC.	
θ	0°	8°	0.00	0.315

Customer Service

Sales and Service:

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