

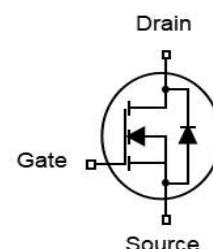


Features

- N-Channel
- Excellent gate charge $\times R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- 100% EAS Tested

V_{DS}	60	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	1.3	mΩ
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	1.9	mΩ
I_D	220	A

TO-220



Part ID	Package Type	Marking	Packing
ZTG013N06	TO-220	ZTG013N06	1000pcs/Reel

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	60	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-55 to 150	°C	
I_{DM}	Drain Current-Continuous@ Current-Pulsed	$T_c = 25^\circ\text{C}$	760	A
Mounted on Large Heat Sink				
I_D	Drain Current-Continuous	$T_c = 25^\circ\text{C}$	220	A
		$T_c = 100^\circ\text{C}$	126	A
P_D	Maximum Power Dissipation	150	W	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.85	°C/W	
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed (Note 1)	520	mJ	



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	--	--	V
Idss	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
IGSS	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.2	1.6	2.0	V
RDS(on)	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=50\text{A}$	--	1.3	1.9	$\text{m}\Omega$
RDS(on)	Drain-Source On-State Resistance	$V_{GS}=4.5\text{V}, I_D=45\text{A}$	--	1.9	2.4	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Ciss	Input Capacitance	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	7200	--	pF
Coss	Output Capacitance		--	1470	--	pF
Crss	Reverse Transfer Capacitance		--	30	--	pF
Rg	Gate Resistance	f=1MHz	--	1.5	--	Ω
Qg	Total Gate Charge	$V_{DS}=30\text{V}, I_D=50\text{A}, V_{GS}=4.5\text{V}$	--	49	--	nC
Qgs	Gate-Source Charge		--	23	--	nC
Qgd	Gate-Drain Charge		--	15	--	nC

Switching Characteristics (Note 2)

Td(on)	Turn-on Delay Time	$V_{DD}=30\text{V}, I_D=50\text{A}, R_G=2.5\Omega, V_{GS}=4.5\text{V}$	--	38	--	ns
Tr	Turn-on Rise Time		--	34	--	ns
Td(off)	Turn-Off Delay Time		--	52	--	ns
Tf	Turn-Off Fall Time		--	26	--	ns

Source-Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

Is	Diode Forward Current		--	--	220	A
VSD	Forward on voltage	$I_S=50\text{A}, V_{GS}=0\text{V}$	--	--	1.4	V
Trr	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_S=50\text{A}, V_R=50\text{V}, dI/dt=100\text{A}/\mu\text{s}$	--	68	--	ns
Qrr	Reverse Recovery Charge		--	122	--	nC

Notes:

- EAS condition : $T_J=25^\circ\text{C}, V_{DD}=50\text{V}, V_G=10\text{V}, L=0.5\text{mH}, R_g=25\Omega$
- Guaranteed by design, not subject to production
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_J(\text{MAX})=150^\circ\text{C}$. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

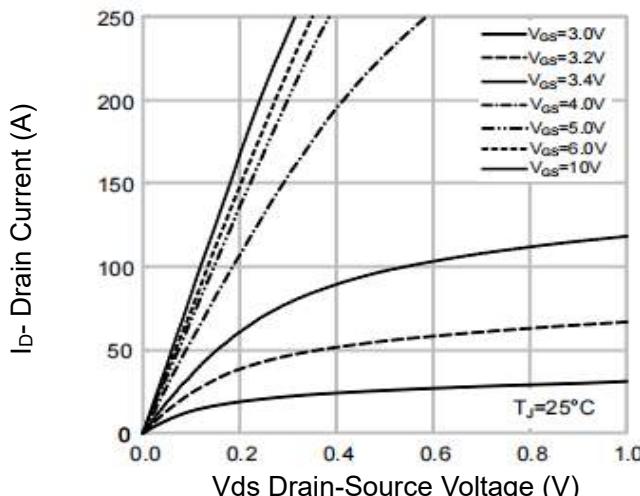


Figure 1 Output Characteristics

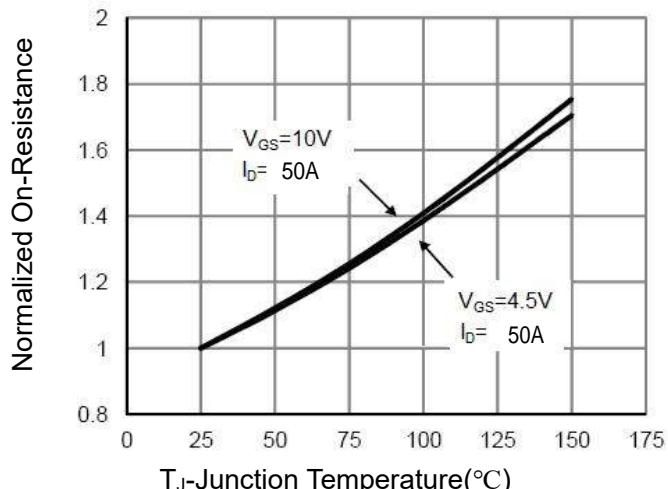


Figure 4 Rdson-JunctionTemperature

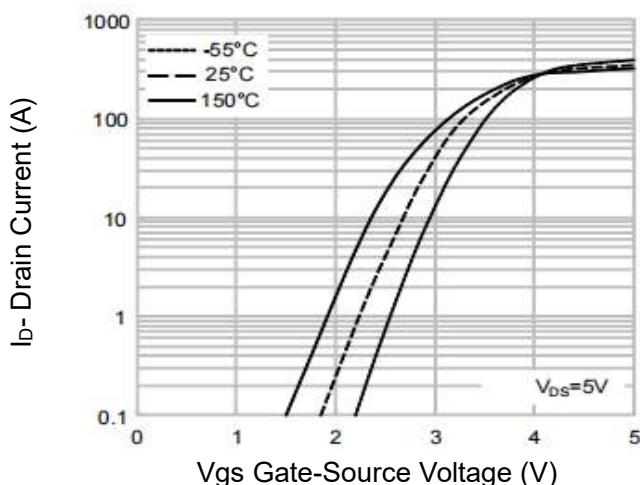


Figure 2 Transfer Characteristics

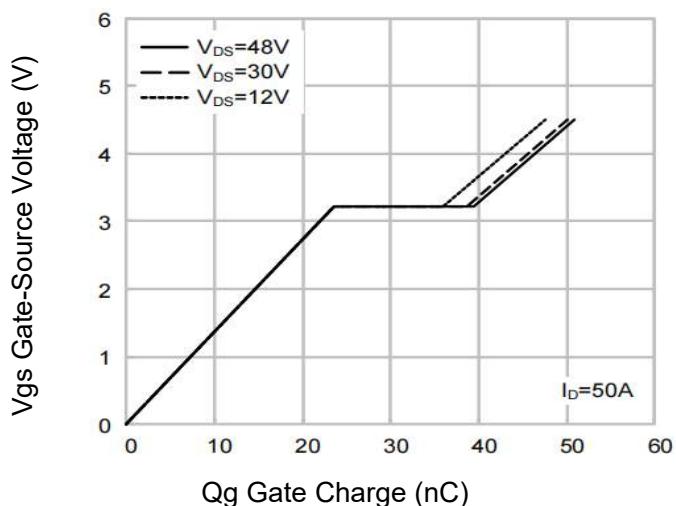


Figure 5 Gate Charge

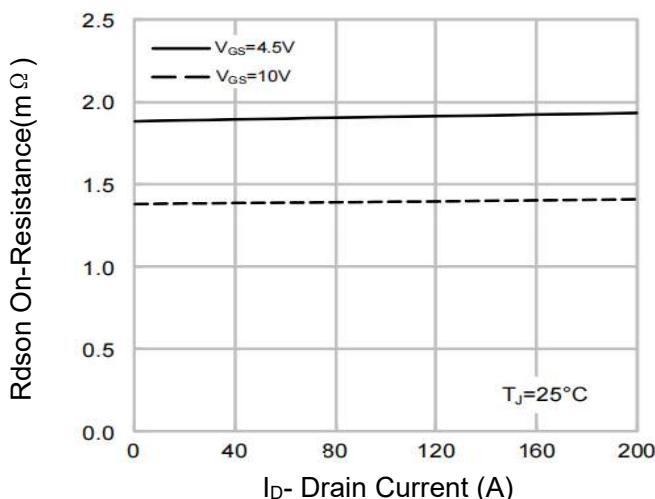


Figure 3 Rdson- Drain Current

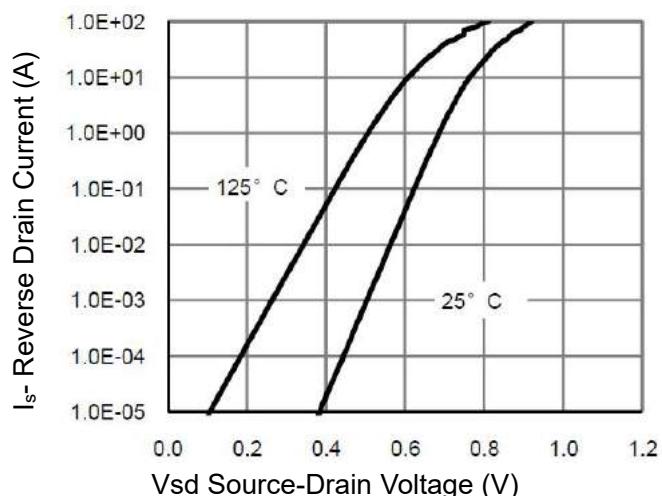


Figure 6 Source- Drain Diode Forward

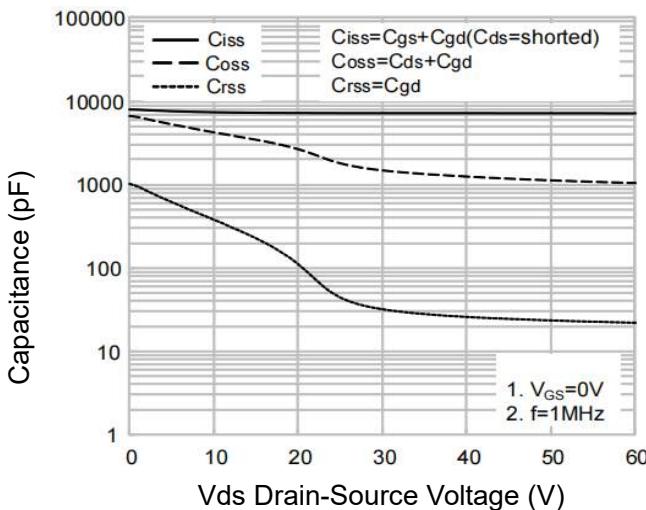


Figure 7 Capacitance vs Vds

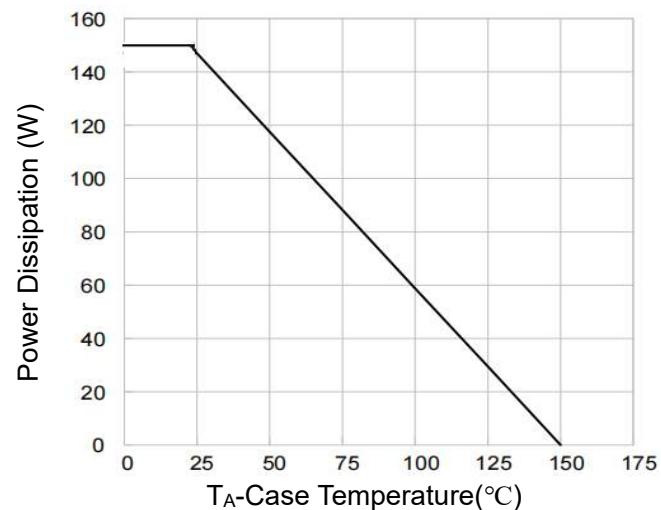


Figure 9 Power De-rating

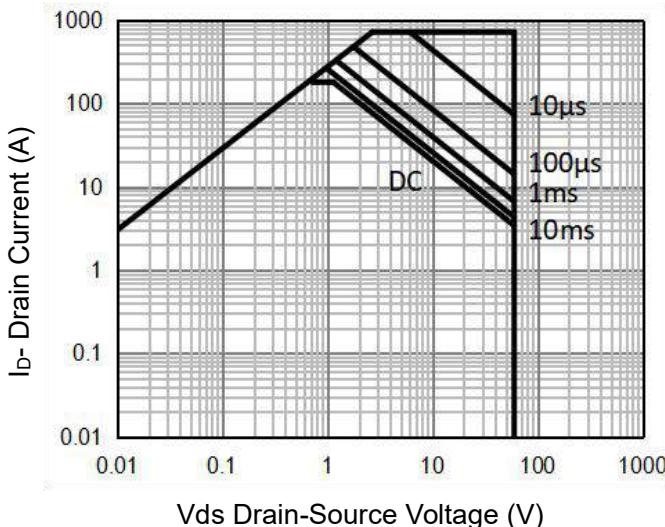


Figure 8 Safe Operation Area^(Note 3)

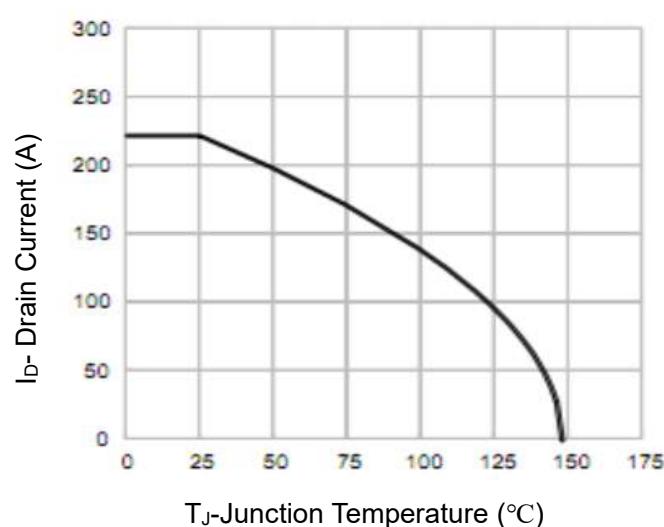


Figure 10 Current De-rating

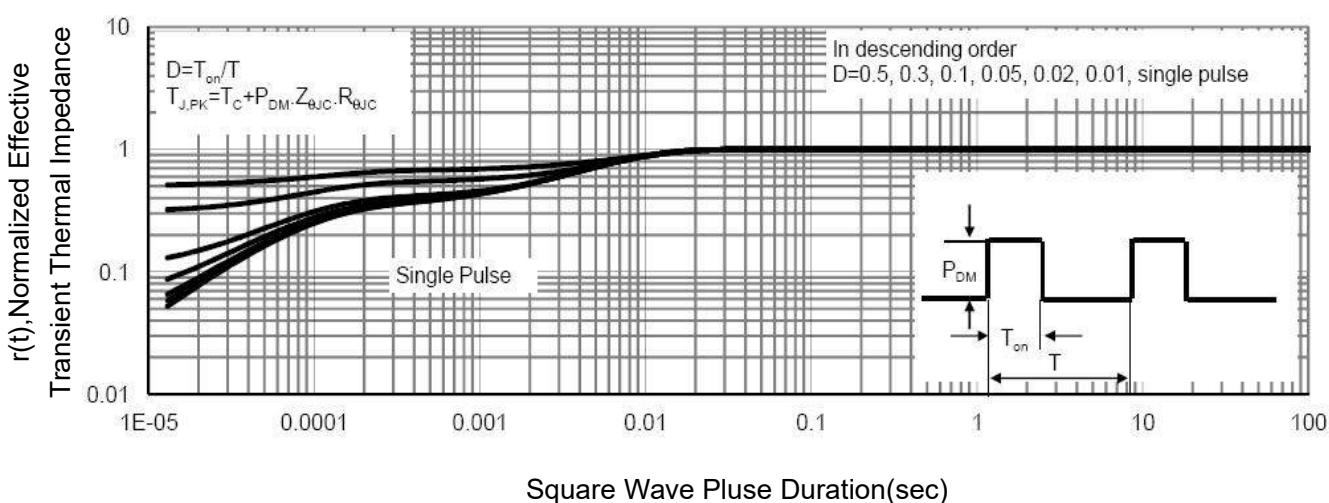
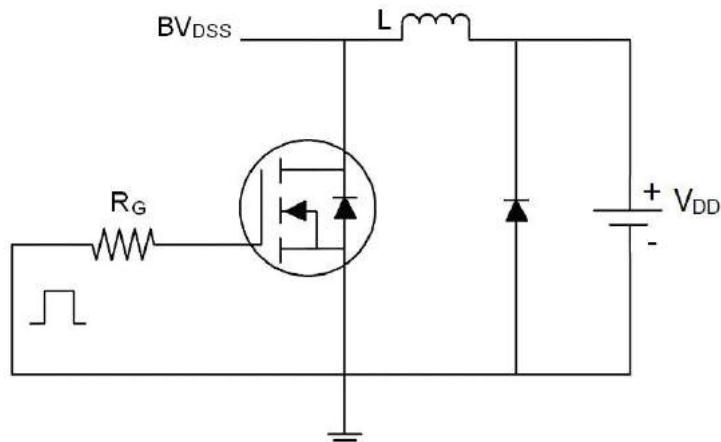


Figure 11 Normalized Maximum Transient Thermal Impedance

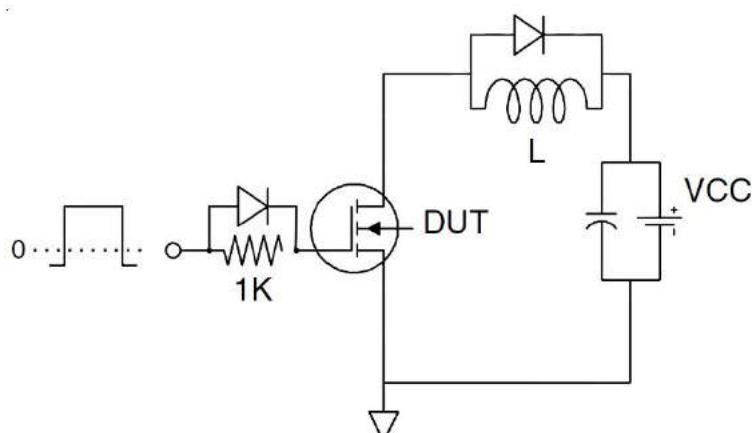


Test Circuit

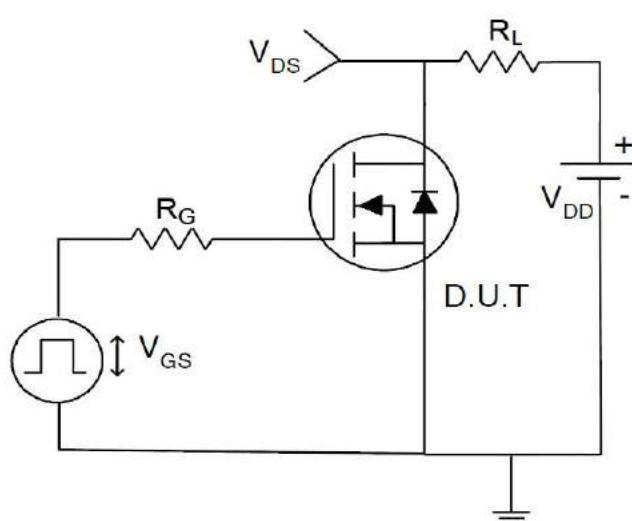
1) E_{AS} test Circuit



2) Gate charge test Circuit

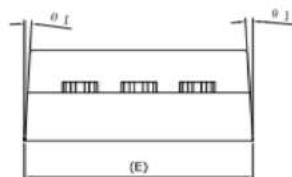
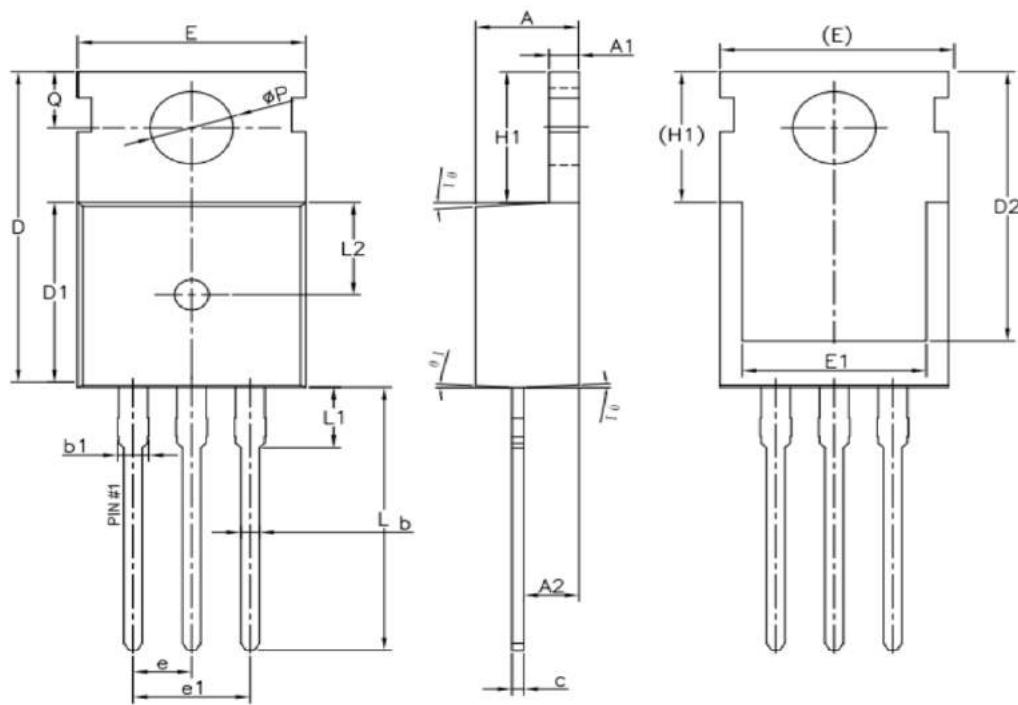


3) Switch Time Test Circuit





TO-220-3L Package Information



SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	—	0.90
b1	1.27	—	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	—	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	—	—	3.50
L2	4.60REF		
φP	3.55	3.60	3.65
Q	2.73	—	2.87
θ 1	1°	3°	5°

Customer Service

Sales and Service:

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