

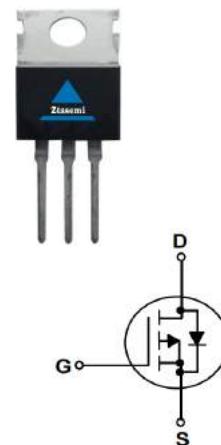


Features

- P-Channel
- Fast Switching
- High Power and current handing capability
- Lead free product is acquired
- 100% EAS Tested

V_{DS}	-60	V
$R_{DS(on),TYP}$ @ $V_{GS}=-10$ V	11	mΩ
$R_{DS(on),TYP}$ @ $V_{GS}=-4.5$ V	13	mΩ
I_D	-65	A

TO-220



Part ID	Package Type	Marking	Packing
ZT12P06	TO-220	ZT12P06	1000pcs/Tape

Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter		Rating	Unit
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage		± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage		-60	V
T_J	Maximum Junction Temperature		175	°C
T_{STG}	Storage Temperature Range		-55 to 175	°C
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c=25^\circ\text{C}$	-240	A
Mounted on Large Heat Sink				
I_D	Drain Current-Continuous	$T_c=25^\circ\text{C}$	-65	A
		$T_c=100^\circ\text{C}$	-42	A
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	107	W
		$T_c=100^\circ\text{C}$	53	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.4	°C/W
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed (Note 2)		484	mJ



Electrical Characteristics ($T_j=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-60	-72	--	V
I_{DS}	Zero Gate Voltage Drain Current	$V_{DS}=-60\text{V}, V_{GS}=0\text{V}$	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-1.0	-1.7	-2.6	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=-10\text{V}, I_D=-20\text{A}$	--	11	13	$\text{m}\Omega$
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=-4.5\text{V}, I_D=-15\text{A}$	--	13	17	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=-5\text{V}, I_D=-20\text{A}$	--	38	--	S

Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

C _{iss}	Input Capacitance	$V_{DS}=-25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	8700	--	pF
C _{oss}	Output Capacitance		--	290	--	pF
C _{rss}	Reverse Transfer Capacitance		--	210	--	pF
Q _g	Total Gate Charge	$V_{DS}=-30\text{V}, I_D=-20\text{A}, V_{GS}=-10\text{V}$	--	140	--	nC
Q _{gs}	Gate-Source Charge		--	19	--	nC
Q _{gd}	Gate-Drain Charge		--	28	--	nC

Switching Characteristics

T _{d(on)}	Turn-on Delay Time	$V_{DS}=-30\text{V}, R_L=1.5\Omega, R_G=3\Omega, V_{GS}=-10\text{V}$	--	26	--	ns
T _r	Turn-on Rise Time		--	21	--	ns
T _{d(off)}	Turn-Off Delay Time		--	138	--	ns
T _f	Turn-Off Fall Time		--	30	--	ns

Source-Drain Diode Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)

I _{SD}	Source-Drain Current (Body Diode)		--	--	-65	A
V _{SD}	Forward on voltage ^(Note 3)	$I_S=-20\text{A}, V_{GS}=0\text{V}$	--	--	-1.2	V
T _{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_F=-20\text{A}, V_{GS}=0\text{V}, \frac{dI}{dt}=100\text{A}/\mu\text{s}$	--	56	--	ns
Q _{rr}	Reverse Recovery Charge		--	63	--	nC

Notes :

1.Repetitive Rating: Pulse width limited by maximum junction temperature.

2.EAS condition: $T_j=25^\circ\text{C}, V_{DD}=40\text{V}, V_G=-10\text{V}, R_g=25\Omega, L=0.5\text{mH}$.

3.Repetitive Rating: Pulse width limited by maximum junction temperature.



Typical Electrical And Thermal Characteristics (Curves)

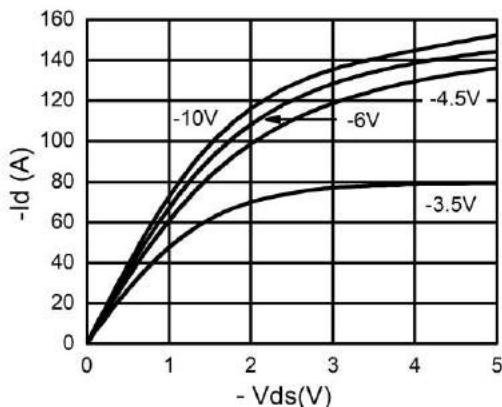


Figure 1. Output Characteristics

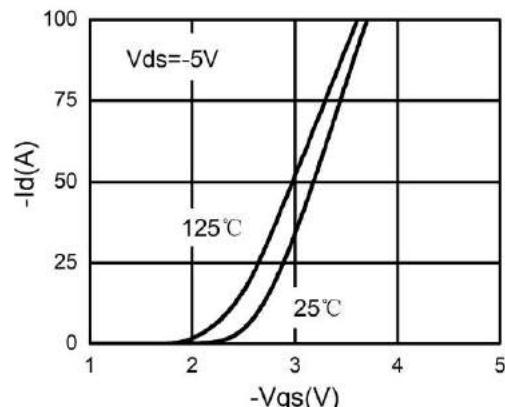


Figure 4. Transfer Characteristics

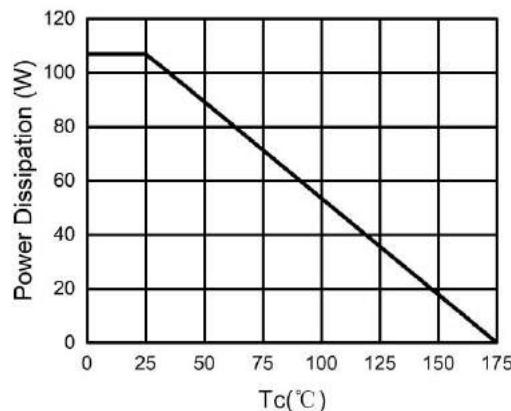


Figure 2. Power Dissipation

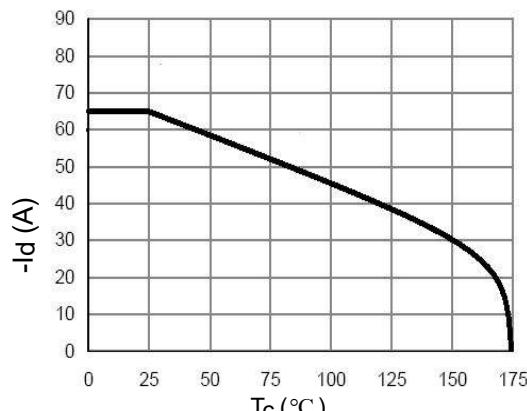


Figure 5. Drain Current

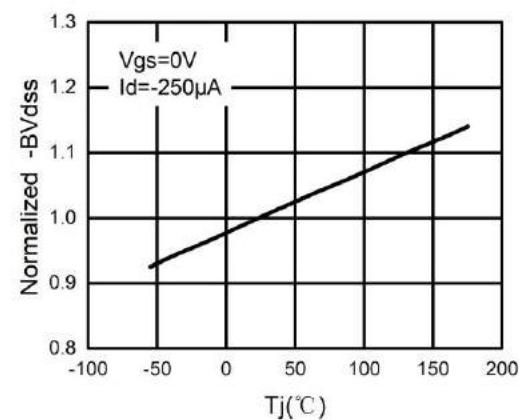


Figure 3. BV_{DSS} vs Junction Temperature

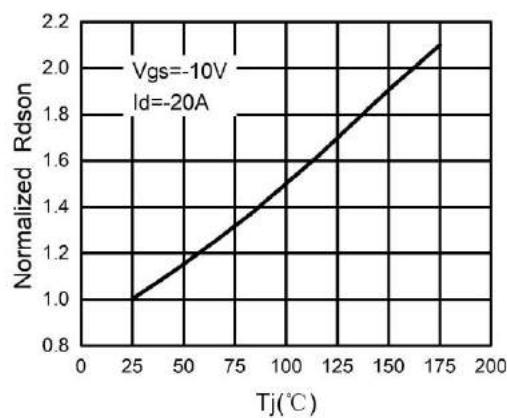


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

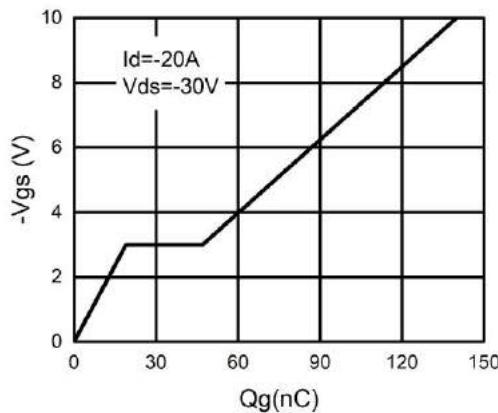


Figure 7. Gate Charge Waveforms

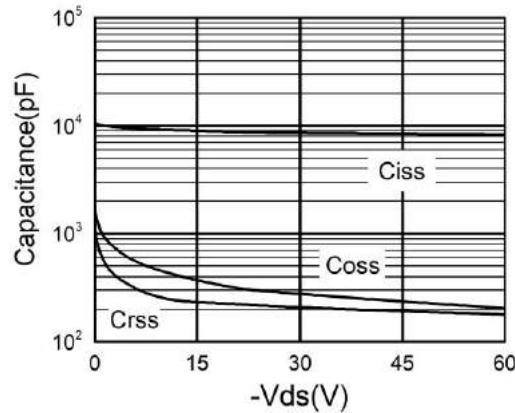


Figure 9. Capacitance

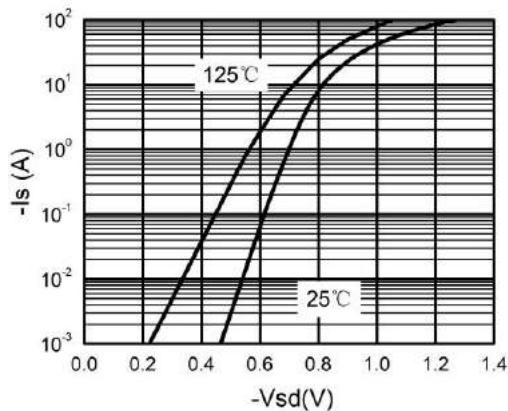


Figure 8. Body-Diode Characteristics

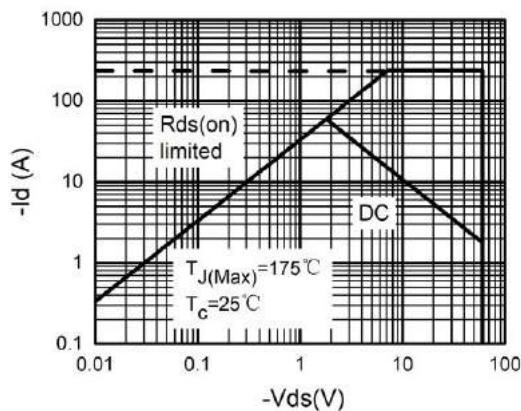
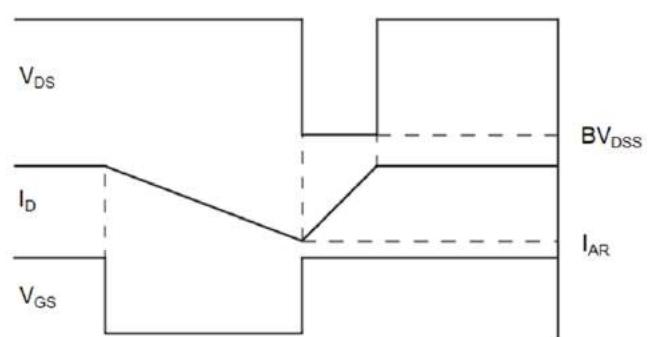
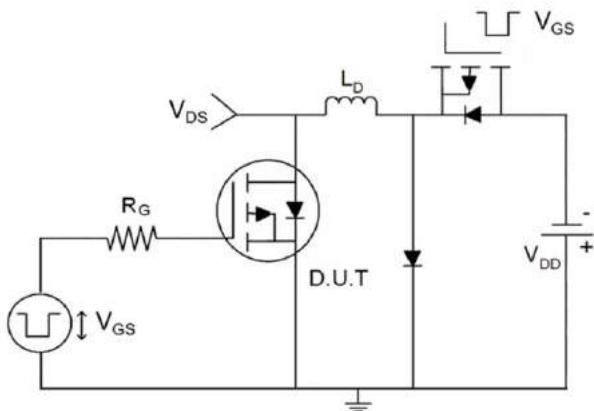


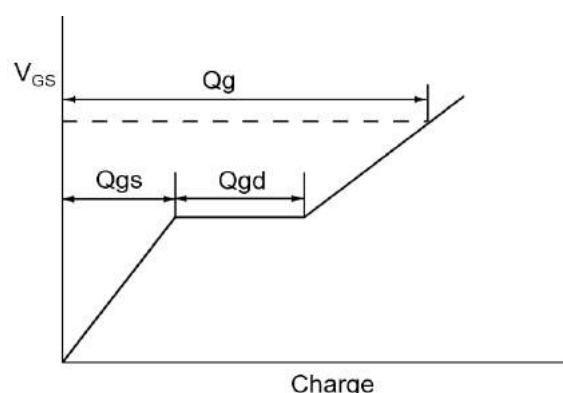
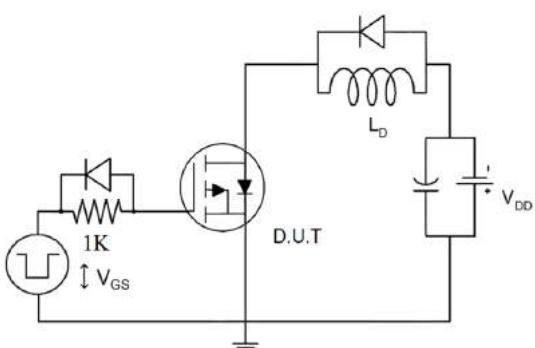
Figure 10. Maximum Safe Operating Area

Test Circuit

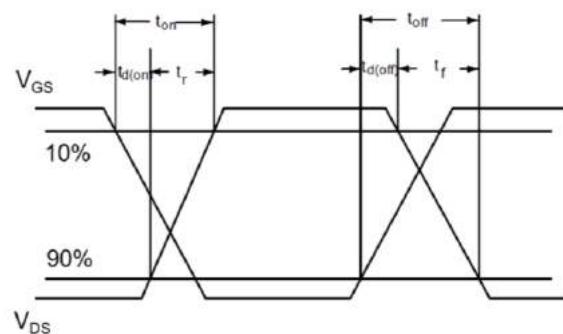
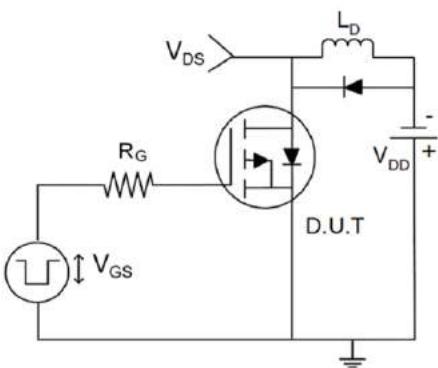
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit

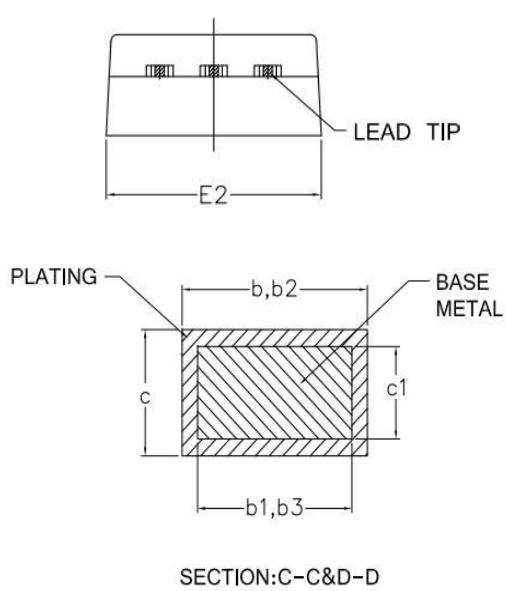
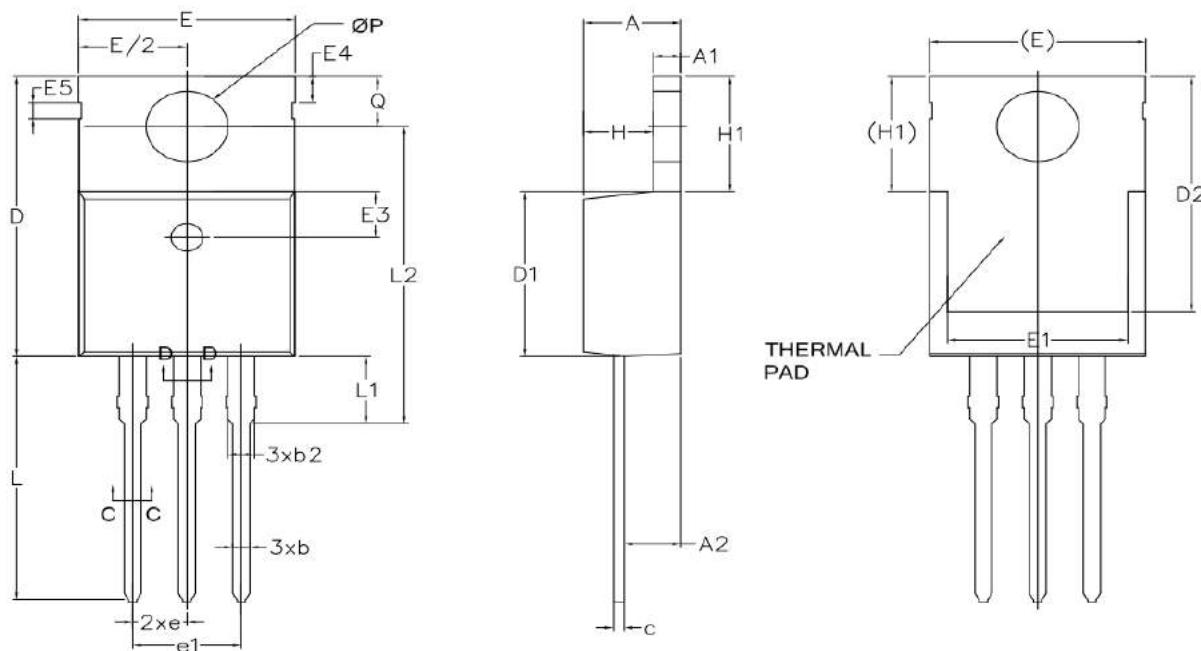


3) Switch Time Test Circuit





TO-220-3L Package Information



SYMBOLS	COMMON		
	MM		
	MIN.	NOM.	MAX.
A	4.47	4.57	4.67
A1	1.20	1.30	1.40
A2	2.35	2.67	2.90
b	0.71	0.80	0.91
b1	0.71	0.80	0.86
b2	1.22	1.27	1.36
b3	1.22	1.27	1.31
c	0.47	0.50	0.60
c1	0.47	0.50	0.55
D	14.70	15.30	15.80
D1	8.90	9.00	9.47
D2	11.75	/	13.60
E	9.70	/	10.37
E1	7.00	8.44	8.89
E2	9.80	10.11	10.20
E3	2.40	2.50	2.60
E4	1.27	1.42	1.57
E5	0.90TYP		
e	2.54BSC		
e1	5.08BSC		
H	3.00	3.27	3.40
H1	6.15	6.30	6.45
L	12.90	13.45	14.80
L1	2.54	3.69	3.84
L2	12.13	16.25	16.5
ØP	3.60	3.84	3.90
Q	2.65	2.74	2.95

Customer Service

Sales and Service:

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