



Features

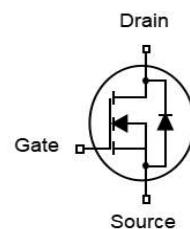
- N-Channel
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5\text{ V}$
- High Power and current handing capability
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant
- 100% EAS Tested

V_{DS}	40	V
$R_{DS(on),TYP}$ @ $V_{GS}=10\text{ V}$	2.5	$\text{m}\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5\text{ V}$	4.0	$\text{m}\Omega$
I_D	125	A

TO-252



Part ID	Package Type	Marking	Packing
ZT025N04D	TO-252	ZT025N04D	2500pcs/reel



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
Common Ratings (T_c=25°C Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	40	V
T_J	Maximum Junction Temperature	175	°C
T_{STG}	Storage Temperature Range	-55 to 175	°C
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c=25^\circ\text{C}$	500
			A
Mounted on Large Heat Sink			
I_D	Drain Current-Continuous	$T_c=25^\circ\text{C}$	125
		$T_c=100^\circ\text{C}$	88
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	107
		$T_c=100^\circ\text{C}$	53
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.4	°C/W
Drain-Source Avalanche Ratings			
EAS	Avalanche Energy, Single Pulsed (Note 2)	576	mJ



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	40	--	--	V
I_{DS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
I_{GS}	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.7	2.5	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=20\text{A}$	--	2.5	3.6	$\text{m}\Omega$
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=4.5\text{V}, I_D=20\text{A}$	--	4.0	5.3	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}, I_D=20\text{A}$	--	38	--	S

Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

C _{iss}	Input Capacitance	$V_{DS}=20\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	6412	--	pF
C _{oss}	OutputCapacitance		--	464	--	pF
C _{rss}	ReverseTransferCapacitance		--	280	--	pF
R _g	Gate Resistance	f=1MHz	--	0.67	--	Ω
Q _g	Total Gate Charge	$V_{DS}=20\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$	--	110	--	nC
Q _{gs}	Gate-SourceCharge		--	16.1	--	nC
Q _{gd}	Gate-DrainCharge		--	26.9	--	nC

Switching Characteristics

T _{d(on)}	Turn-on Delay Time	$V_{DD}=20\text{V}, R_L=1\Omega, R_G=3\Omega, V_{GS}=10\text{V}$	--	18	--	ns
T _r	Turn-on Rise Time		--	4.4	--	ns
T _{d(off)}	Turn-Off Delay Time		--	67	--	ns
T _f	Turn-Off Fall Time		--	9.4	--	ns

Source- Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

I _{SD}	Source-Drain Current (Body Diode)		--	--	125	A
V _{SD}	Forward on voltage ^(Note 3)	$I_S = 20\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
T _{rr}	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_F = 20\text{A}, V_{GS}=0\text{V}$ $di/dt=500\text{A}/\mu\text{s}$	--	6	--	ns
Q _{rr}	Reverse Recovery Charge		--	14	--	nC

Notes :

1.Repetitive Rating: Pulse width limited by maximum junction temperature.

2.E_{AS} condition: $T_J=25^\circ\text{C}, V_{DD}=40\text{V}, V_G=10\text{V}, R_G=25\Omega, L=0.5\text{mH}$.

3.Repetitive Rating: Pulse width limited by maximum junction temperature.



Typical Electrical And Thermal Characteristics (Curves)

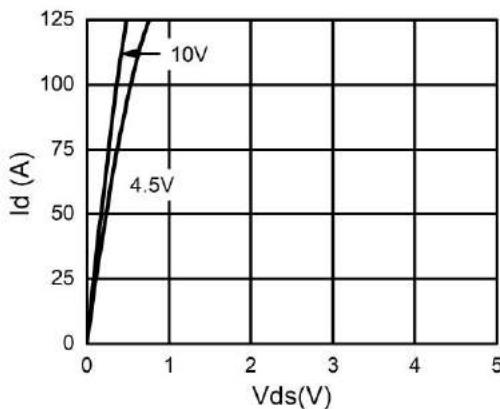


Figure 1. Output Characteristics

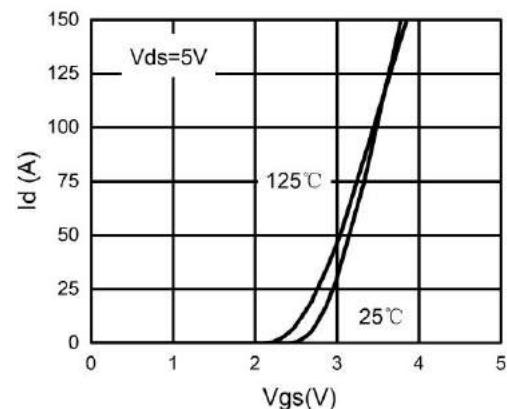


Figure 4. Transfer Characteristics

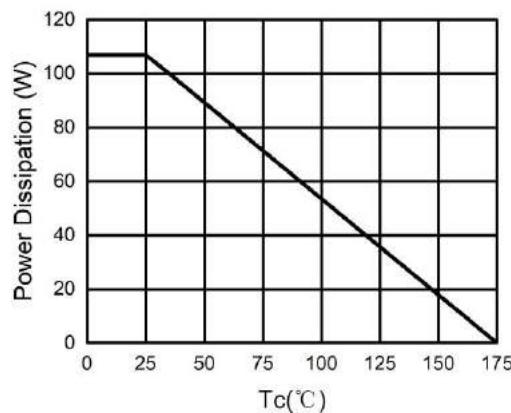


Figure 2. Power Dissipation

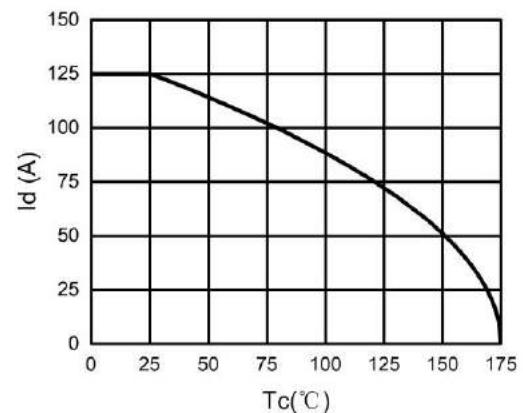


Figure 5. Drain Current

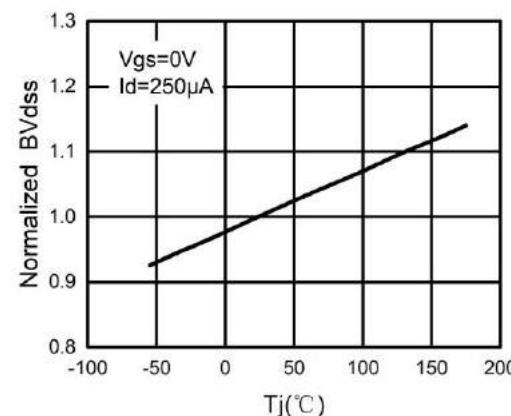


Figure 3. BV_{DSS} vs Junction Temperature

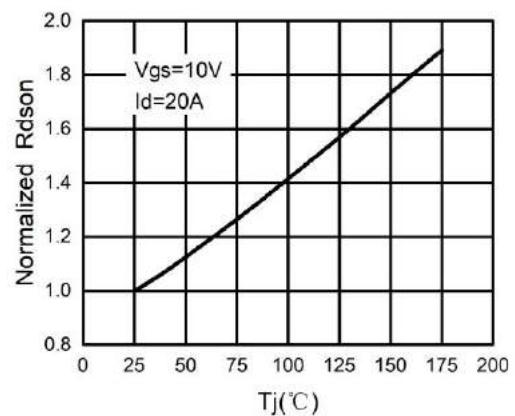


Figure 6. R_{DSON} vs Junction Temperature

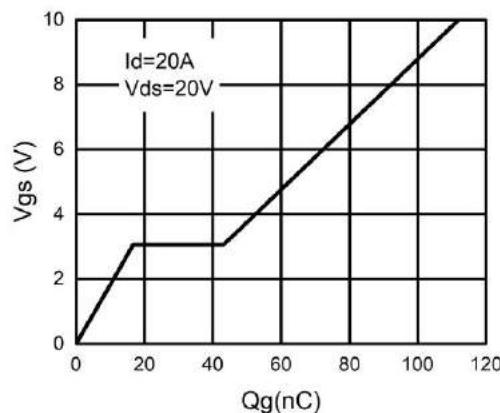


Figure 7. Gate Charge Waveforms

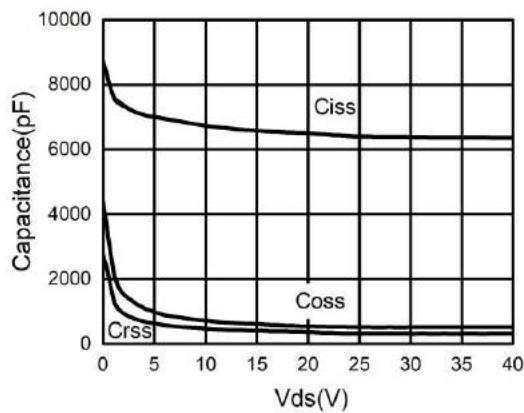


Figure 9. Capacitance

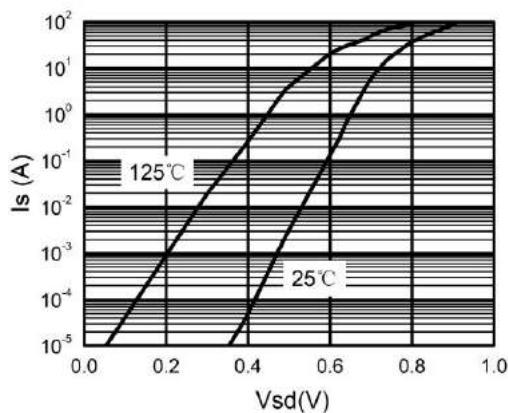


Figure 8. Body-Diode Characteristics

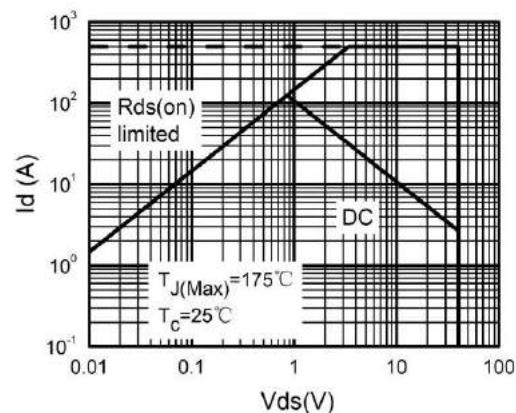
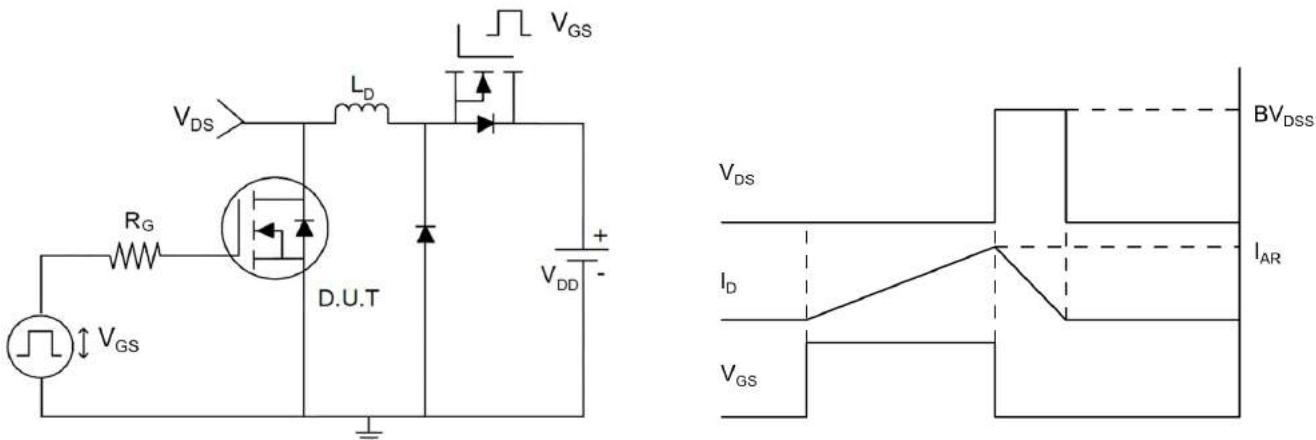


Figure 10. Maximum Safe Operating Area

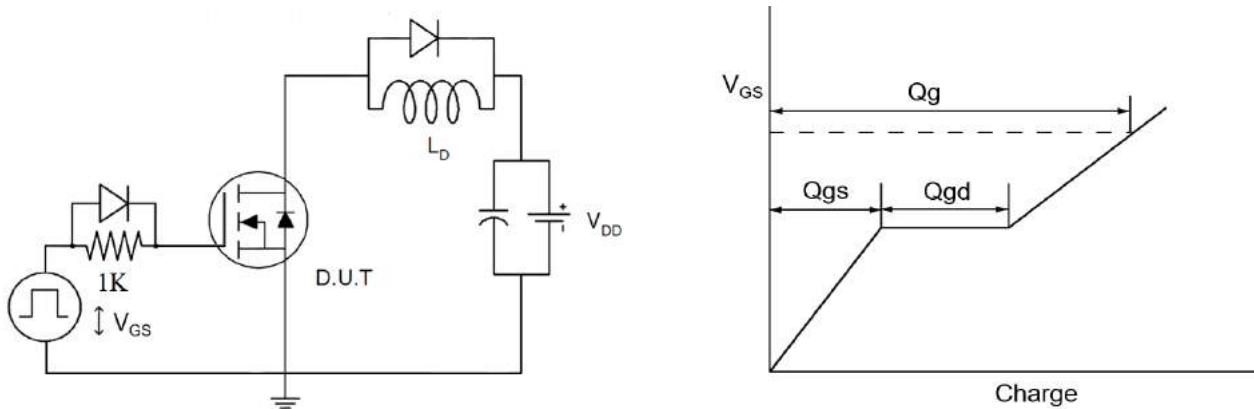


Test Circuit

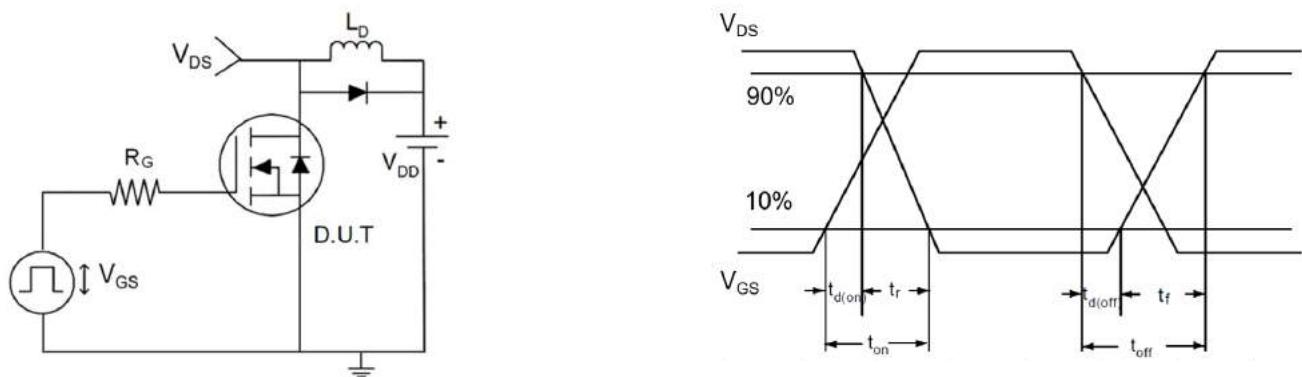
1) E_{AS} Test Circuits



2) Gate Charge Test Circuit

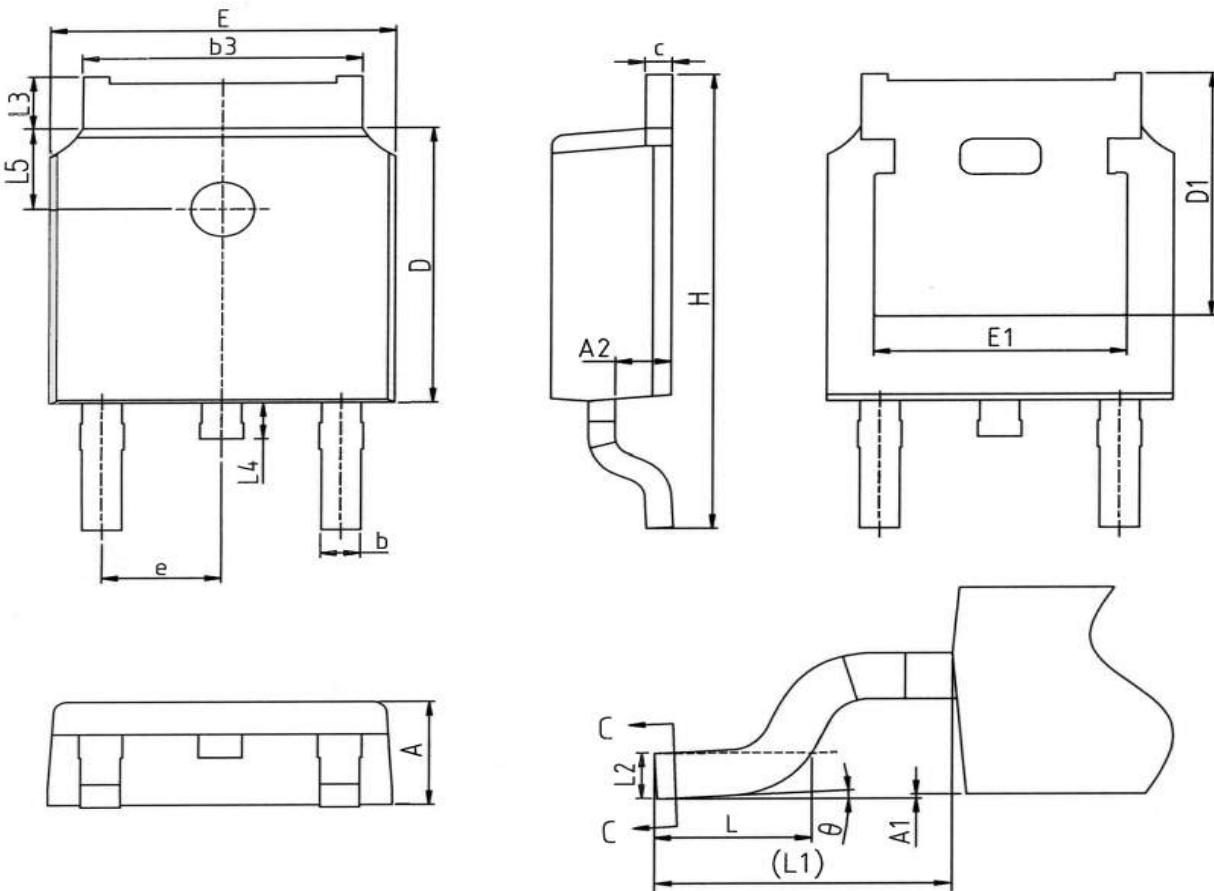


3) Switch Time Test Circuit





TO-252 Package Information



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.12
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°

Customer Service

Sales and Service:

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