

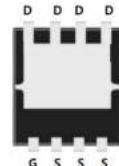


Features

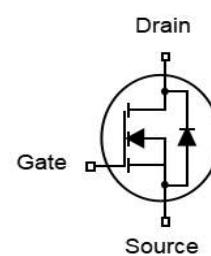
- N-Channel
- Very low FOM $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant
- 100% EAS Tested

V_{DS}	650	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	310	$\text{m}\Omega$
I_D	11	A

DFN5x6



Part ID	Package Type	Marking	Packing
ZT65R360G	DFN5x6	ZT65R360G	5000pcs/Reel



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 30	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	650	V	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c=25^\circ\text{C}$	33	A

Mounted on Large Heat Sink

I_D	Drain Current-Continuous (Note 1)	$T_c=25^\circ\text{C}$	11	A
		$T_c=100^\circ\text{C}$	6.6	A
P_D	Maximum Power Dissipation	83		W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	4		$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	80		$^\circ\text{C}/\text{W}$

Drain-Source Avalanche Ratings

EAS	Avalanche Energy, Single Pulsed	215	mJ
EAR	Repetitive Avalanche Energy	0.32	mJ
dv/dt	Reverse Diode dv/dt (Note 3)	15	V/ns



Electrical Characteristics ($T_j=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise stated)						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	650	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=650\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
I_{GSS}	Gate-Body Leakage Current	$V_{GS}=\pm 30\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.5	3.6	4.5	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=5.5\text{A}$	--	310	360	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{DS}=100\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	811	--	pF
C_{oss}	OutputCapacitance		--	29	--	pF
C_{rss}	ReverseTransferCapacitance		--	2.1	--	pF
R_g	Gate Resistance	$f=1\text{MHz}$	--	4.8	--	Ω
Q_g	Total Gate Charge	$V_{DD}=520\text{V}, I_D=11\text{A}, V_{GS}=10\text{V}$	--	23.1	--	nC
Q_{gs}	Gate-SourceCharge		--	6.6	--	nC
Q_{gd}	Gate-DrainCharge		--	9.0	--	nC
Switching Characteristics (Note 2)						
$T_{d(on)}$	Turn-on Delay Time	$V_{DD}=400\text{V}, I_D=11\text{A}, R_G=15\Omega, V_{GS}=10\text{V}$	--	15	--	ns
T_r	Turn-on Rise Time		--	25	--	ns
$T_{d(off)}$	Turn-Off Delay Time		--	80	--	ns
T_f	Turn-Off Fall Time		--	35	--	ns
Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_S=5.5\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
T_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_F=5.5\text{A}, V_R=400\text{V}, di/dt=100\text{A}/\mu\text{s}$	--	330	--	ns
Q_{rr}	Reverse Recovery Charge		--	2.8	--	nC
I_{rrm}	Peak Reverse Recovery Current		--	17	--	A

Notes:

1. Limited by maximum junction temperature.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. Identical low side and high side switch with identical R_G .

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

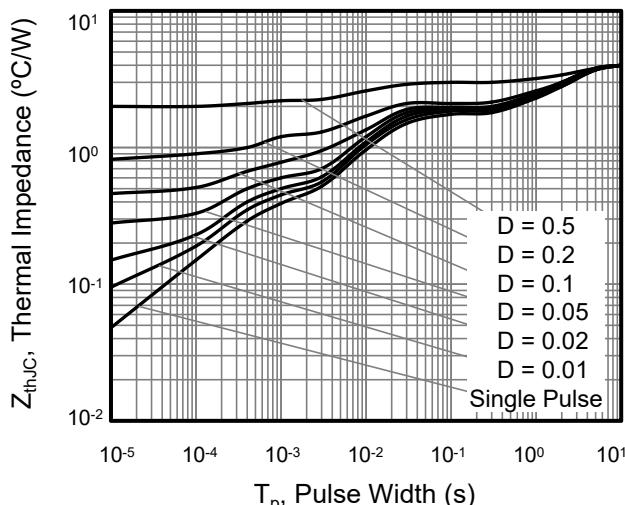


Figure 1. Transient Thermal Impedance

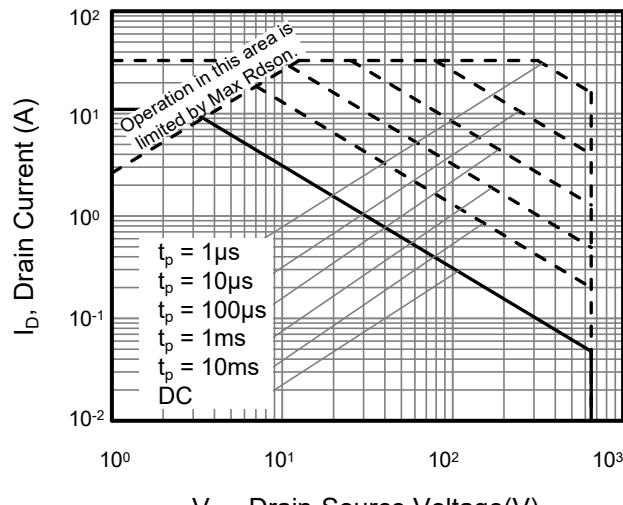


Figure 4. Safe Operation Area

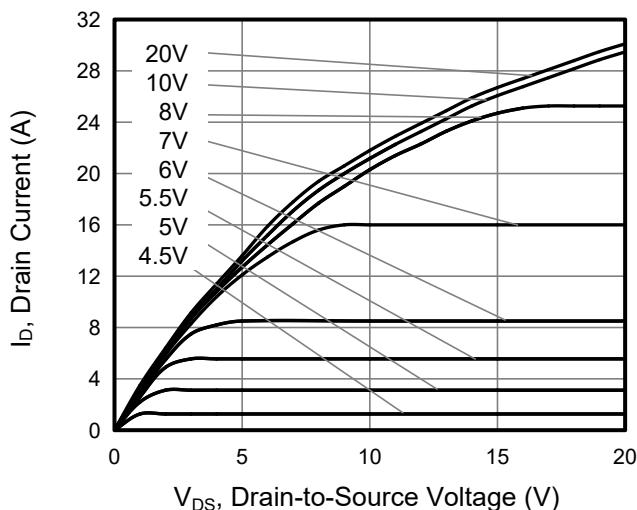


Figure 2. Output Characteristics

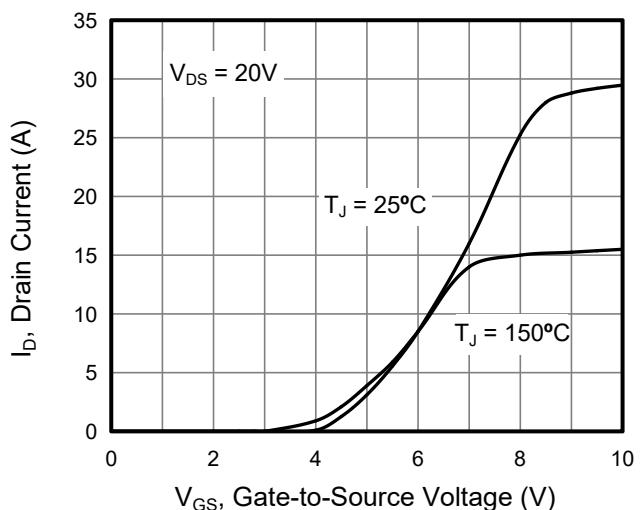


Figure 5. Transfer Characteristics

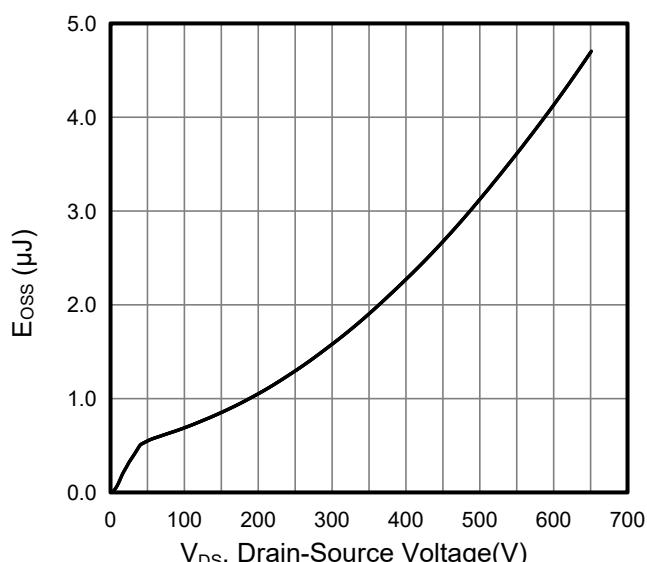


Figure 3 . Typ. Coss Stored Energy

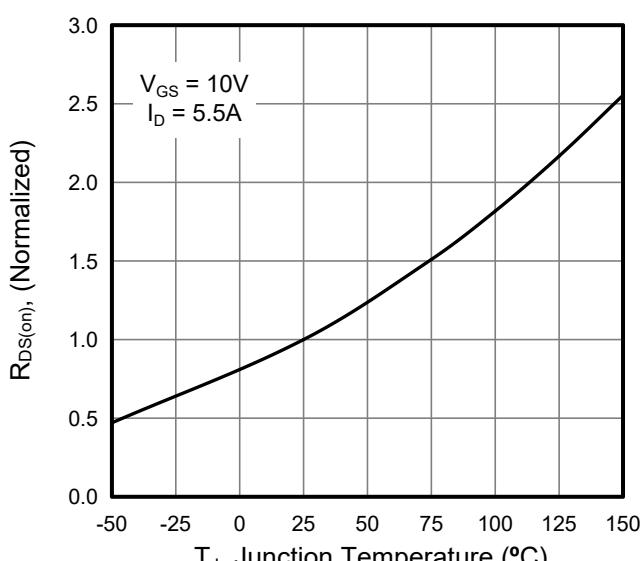


Figure 6. On-Resistance vs Temperature

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

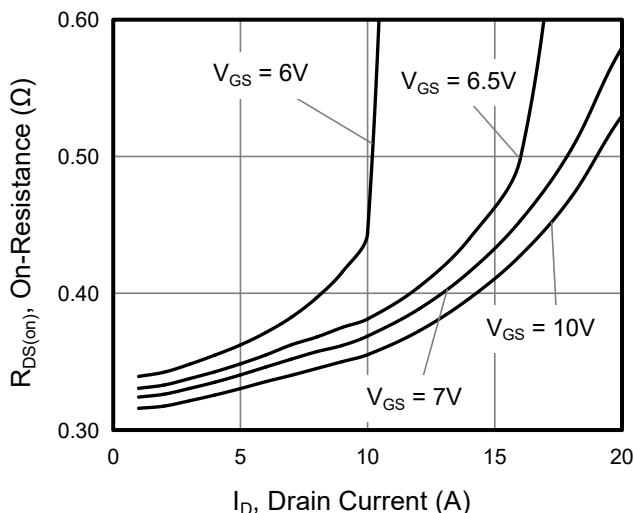


Figure 7. On-Resistance vs Drain Current

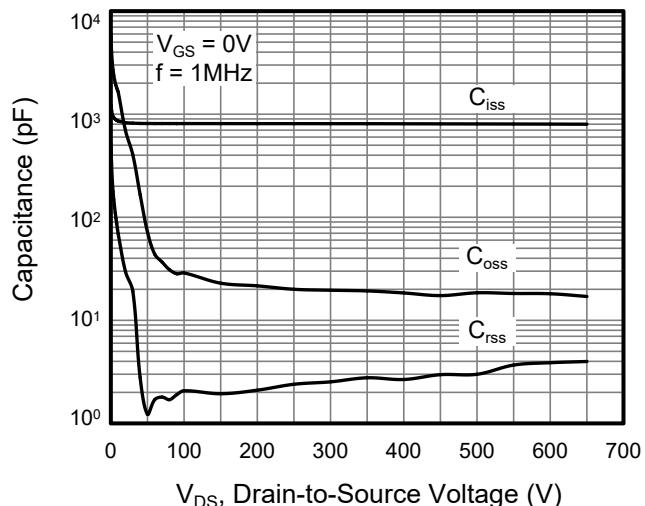


Figure 9. Capacitance

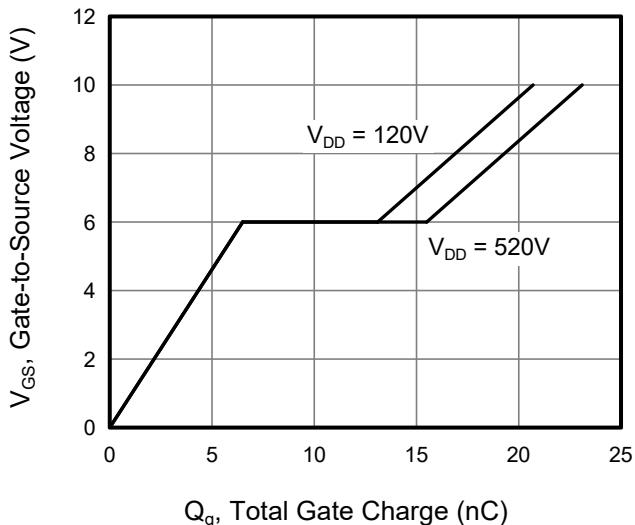


Figure 8. Gate Charge

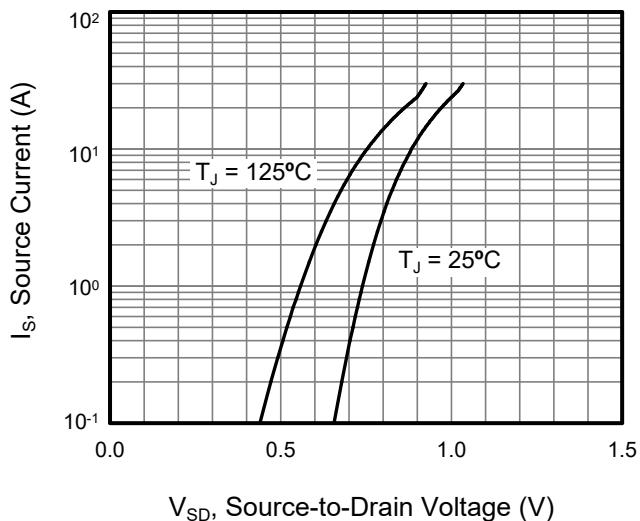


Figure 10. Body Diode Forward Voltage

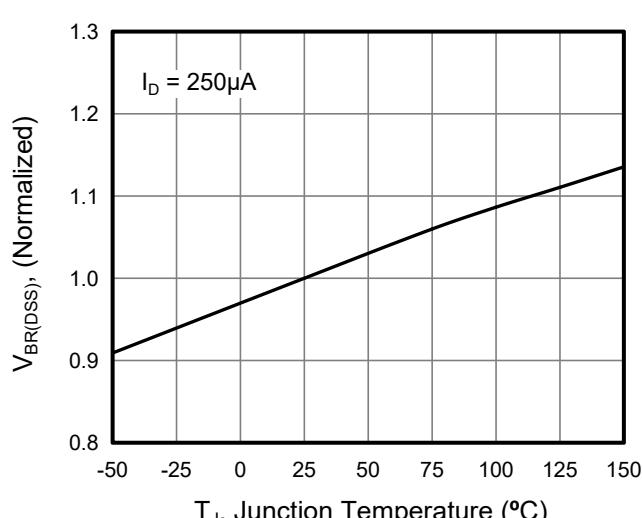


Figure 11. Breakdown Voltage vs Junction Temperature

Figure A: Gate Charge Test Circuit and Waveform

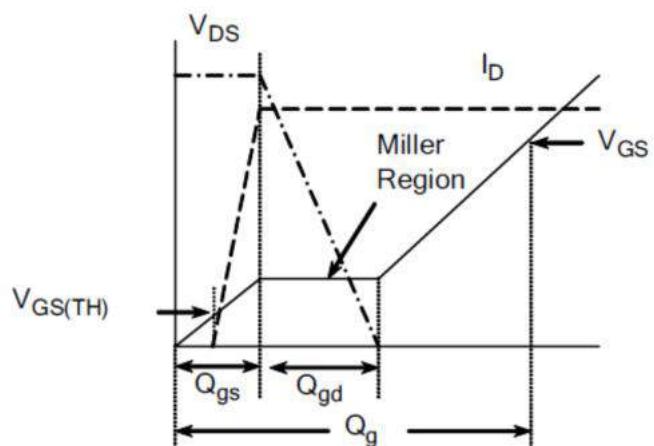
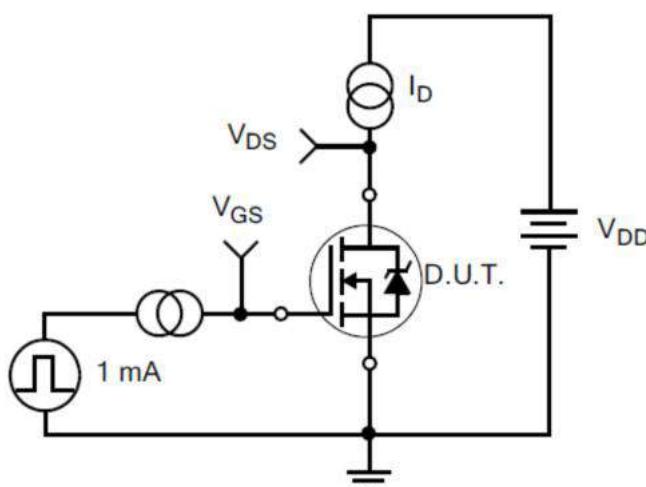


Figure B: Resistive Switching Test Circuit and Waveform

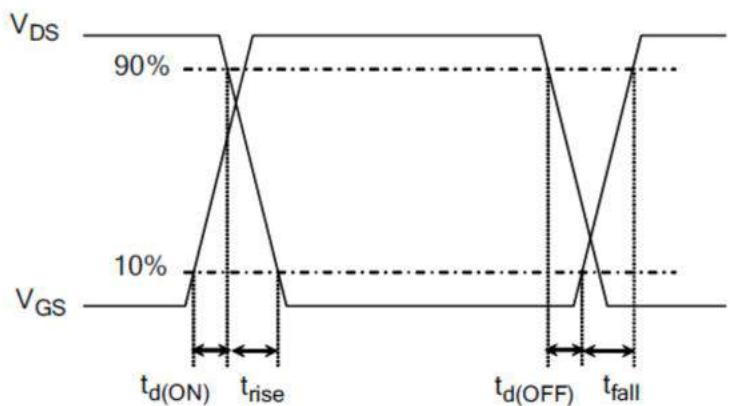
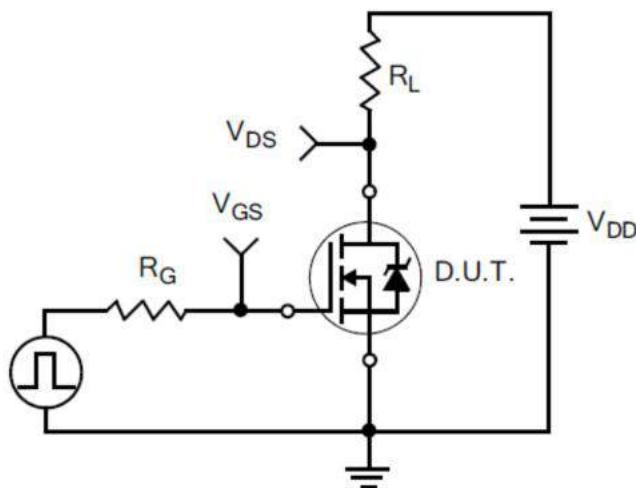
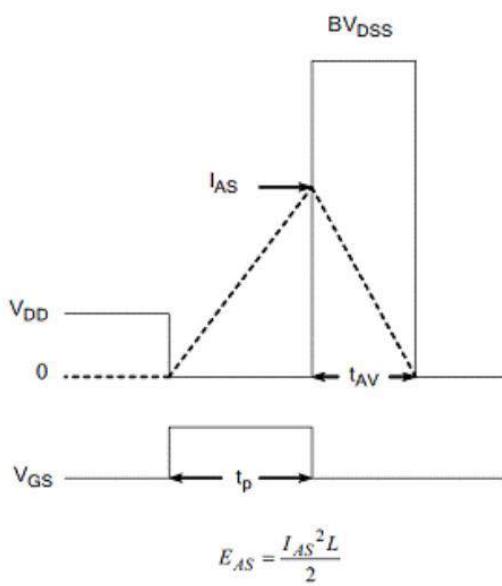
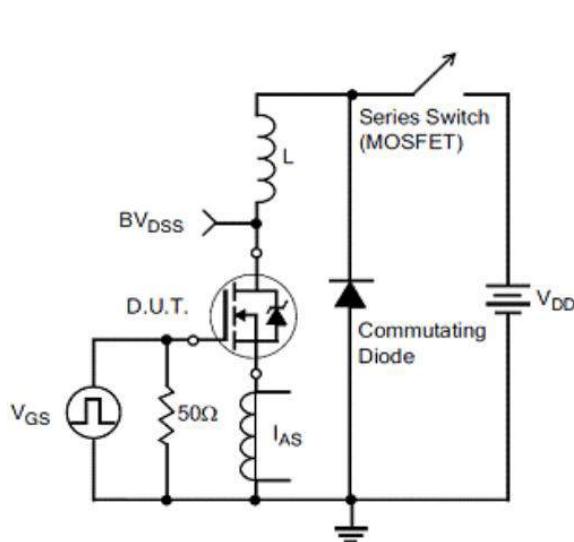
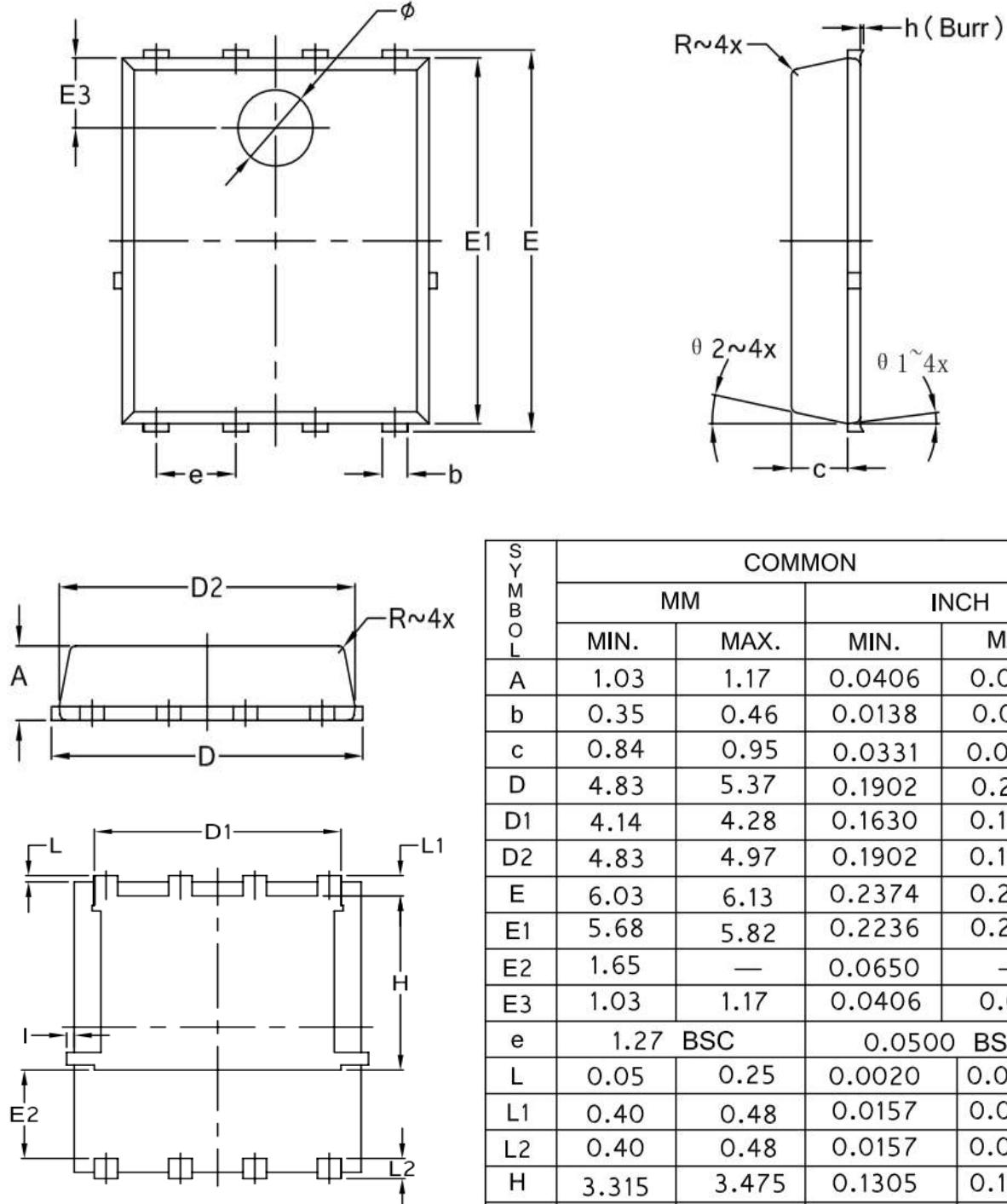


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





DFN5x6-8L Package Information



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.35	0.46	0.0138	0.0181
c	0.84	0.95	0.0331	0.0374
D	4.83	5.37	0.1902	0.2114
D1	4.14	4.28	0.1630	0.1685
D2	4.83	4.97	0.1902	0.1957
E	6.03	6.13	0.2374	0.2413
E1	5.68	5.82	0.2236	0.2291
E2	1.65	—	0.0650	—
E3	1.03	1.17	0.0406	0.0461
e	1.27	BSC	0.0500	BSC
L	0.05	0.25	0.0020	0.0098
L1	0.40	0.48	0.0157	0.0189
L2	0.40	0.48	0.0157	0.0189
H	3.315	3.475	0.1305	0.1368
I	—	0.16	—	0.0063
φ	1.13	1.27	0.0445	0.0500
R	0.10		0.0039	
θ1	7° REF		7° REF	
θ2	12° REF		12° REF	
h	0.08 MAX		0.0031	

Customer Service

Sales and Service:

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