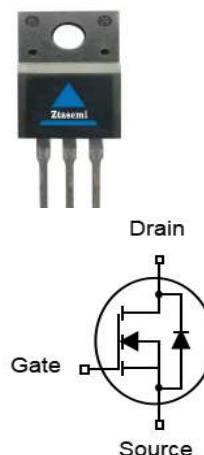


## Features

- N-Channel
- Very low FOM  $R_{DS(on)} \times Q_g$
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant
- 100% EAS Tested

$V_{DS}$	650	V
$R_{DS(on),TYP}@ V_{GS}=10V$	110	m $\Omega$
$I_D$	30	A

TO-220F



Part ID	Package Type	Marking	Packing
ZT65R128FF	TO-220F	ZT65R128FF	1000pcs/Tape

## Absolute Maximum Ratings $T_A=25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
<b>Common Ratings (<math>T_c=25^\circ\text{C}</math> Unless Otherwise Noted)</b>				
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	650	V	
$T_J$	Maximum Junction Temperature	150	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
$I_{DM}$	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c=25^\circ\text{C}$ 90	A	
<b>Mounted on Large Heat Sink</b>				
$I_D$	Drain Current-Continuous (Note 1)	$T_c=25^\circ\text{C}$	30	A
		$T_c=100^\circ\text{C}$	18	A
$P_D$	Maximum Power Dissipation	34	W	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	3.65	$^\circ\text{C/W}$	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	80	$^\circ\text{C/W}$	
<b>Drain-Source Avalanche Ratings</b>				
EAS	Avalanche Energy, Single Pulsed	636	mJ	
EAR	Repetitive Avalanche Energy	0.96	mJ	
dv/dt	Reverse Diode dv/dt (Note 3)	50	V/ns	

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub>=25°C (unless otherwise stated)</b>						
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650	--	--	V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V, T <sub>J</sub> = 25°C	--	--	3	μA
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V, T <sub>J</sub> = 150°C	--	--	3000	μA
IGSS	Gate-Body Leakage Current	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	3.0	4.0	5.0	V
RDS(on)	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	--	110	128	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
Ciss	Input Capacitance	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, f=1MHz	--	2536	--	pF
Coss	Output Capacitance		--	92	--	pF
Crss	Reverse Transfer Capacitance		--	2.5	--	pF
Rg	Gate Resistance	f=1MHz	--	4	--	Ω
Qg	Total Gate Charge	V <sub>DD</sub> =520V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V	--	57	--	nC
Qgs	Gate-Source Charge		--	17	--	nC
Qgd	Gate-Drain Charge		--	23	--	nC
V <sub>Plateau</sub>	Gate Plateau Voltage		--	6.5	--	V
<b>Switching Characteristics (Note 2)</b>						
Td(on)	Turn-on Delay Time	V <sub>DD</sub> =400V, I <sub>D</sub> =30A, R <sub>G</sub> =15Ω, V <sub>GS</sub> =10V	--	15	--	ns
Tr	Turn-on Rise Time		--	10	--	ns
Td(off)	Turn-Off Delay Time		--	60	--	ns
Tf	Turn-Off Fall Time		--	10	--	ns
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
VSD	Forward on voltage	I <sub>S</sub> =15A, V <sub>GS</sub> =0V	--	1.0	1.5	V
Trr	Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>F</sub> =15A, V <sub>R</sub> =400V	--	170	--	ns
Qrr	Reverse Recovery Charge	di/dt=100A/μs	--	1.1	--	μC
Irrm	Peak Reverse Recovery Current		--	13	--	A

**Notes:**

- Limited by maximum junction temperature.
- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Identical low side and high side switch with identical R<sub>G</sub>.

Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

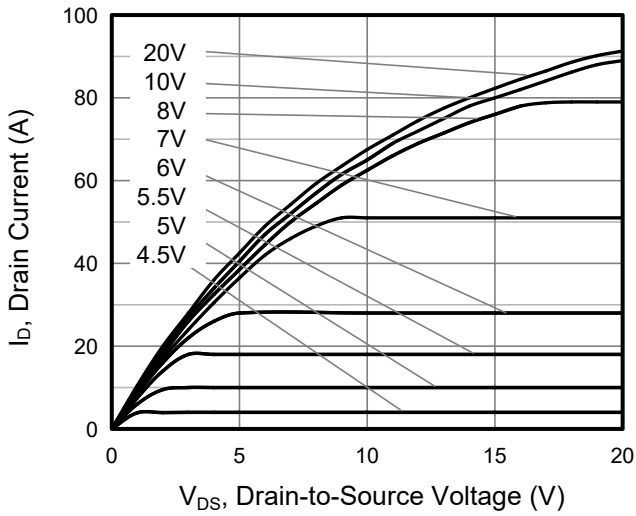


Figure 1. Output Characteristics

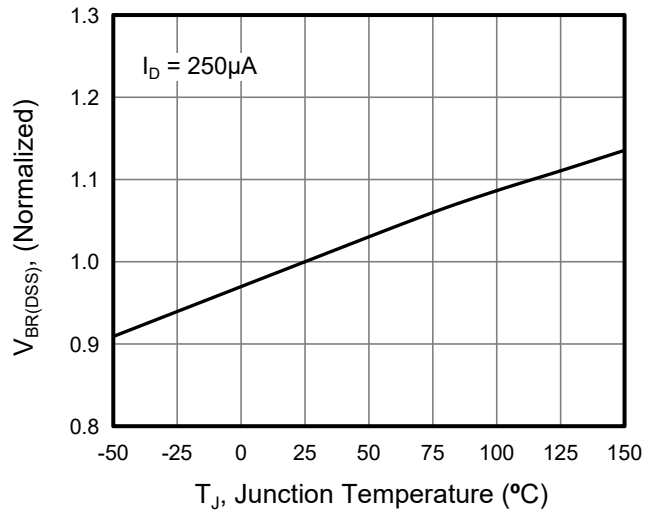


Figure 4. Breakdown Voltage vs. Junction Temperature

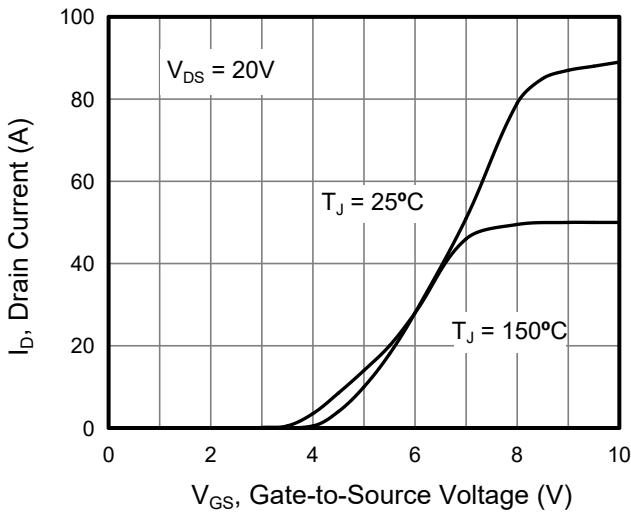


Figure 2. Transfer Characteristics

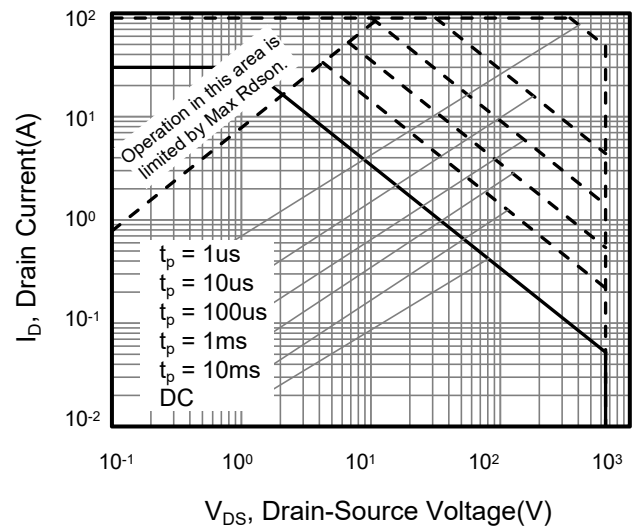


Figure 5. Safe Operation Area

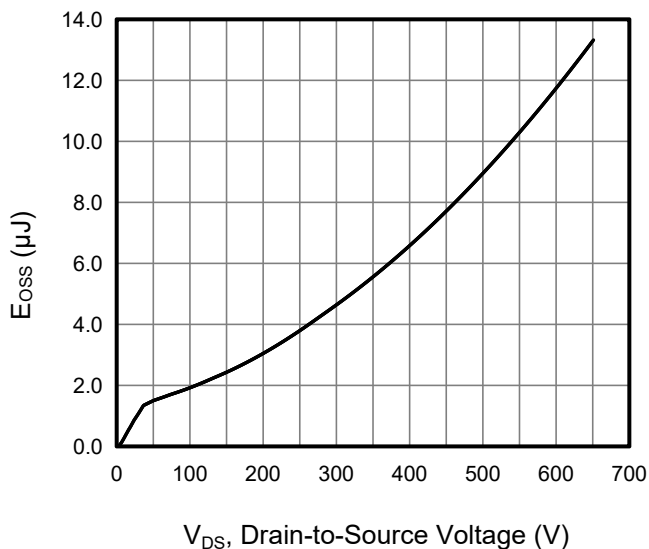


Figure 3. Typ.  $C_{oss}$  Stored Energy

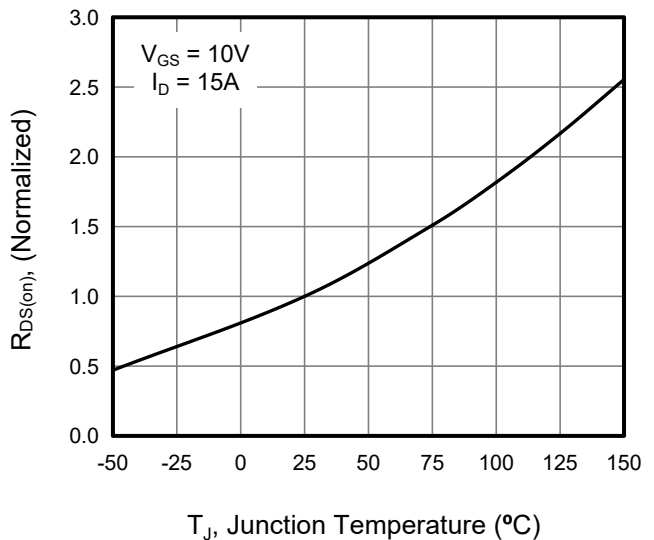


Figure 6. On-Resistance vs. Temperature

Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

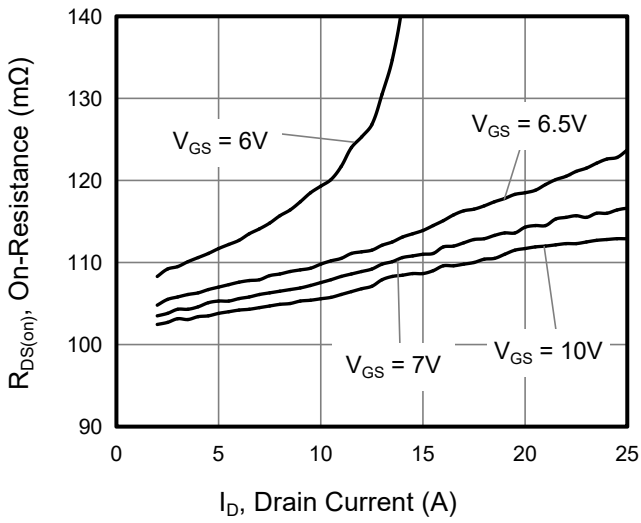


Figure 7. On-Resistance vs. Drain Current

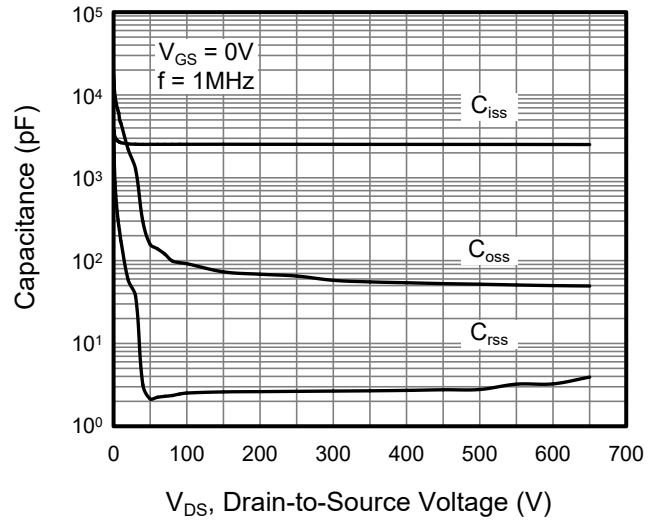


Figure 9. Capacitance

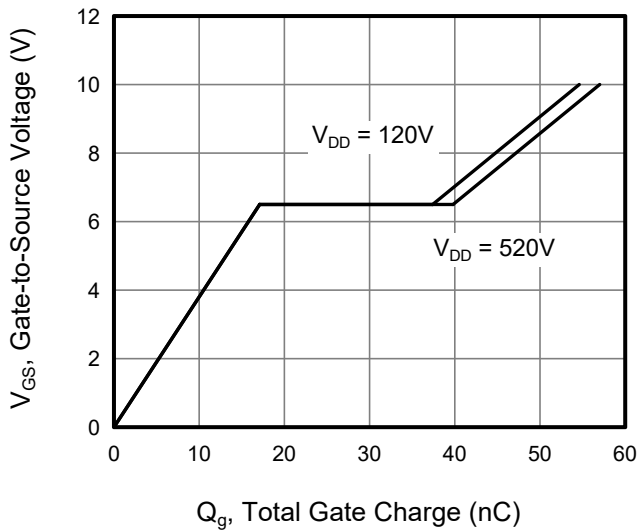


Figure 8. Gate Charge

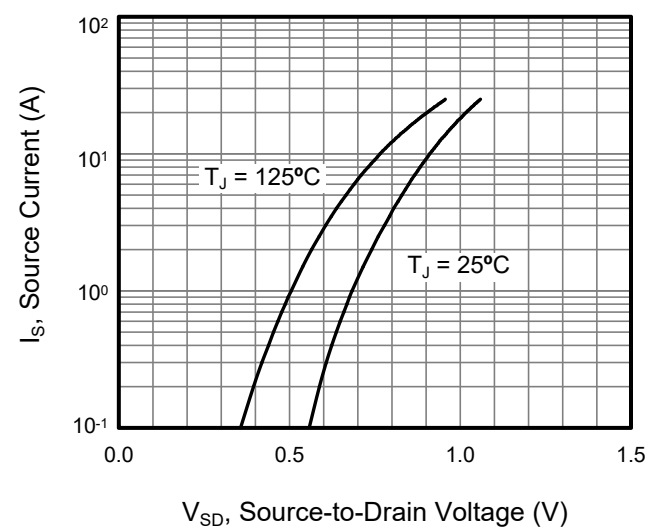


Figure 10. Body Diode Forward Voltage

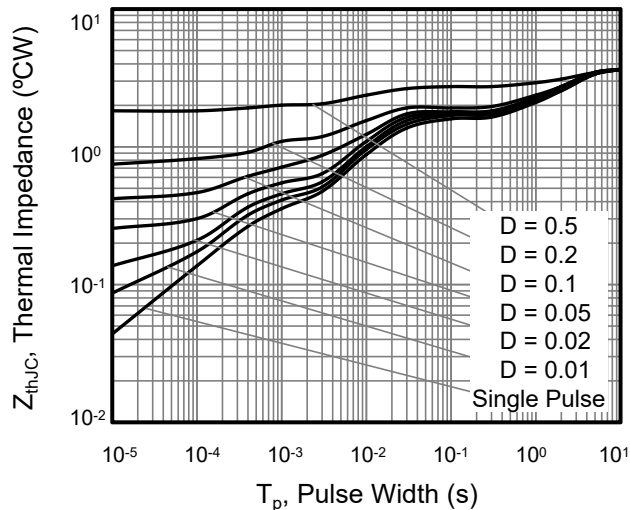


Figure 11. Transient Thermal Impedance

Figure A: Gate Charge Test Circuit and Waveform

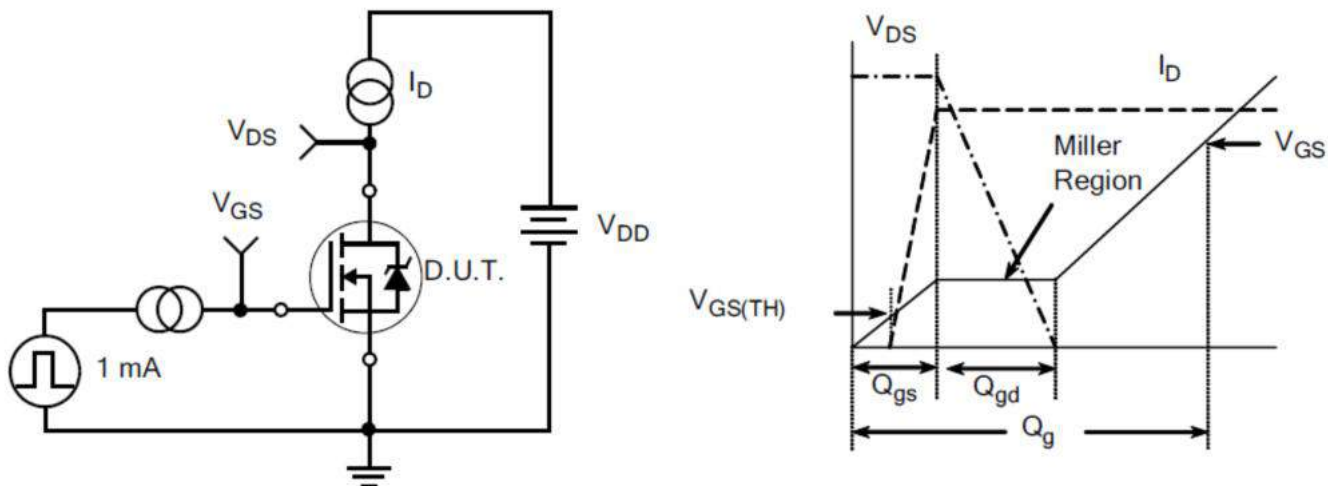


Figure B: Resistive Switching Test Circuit and Waveform

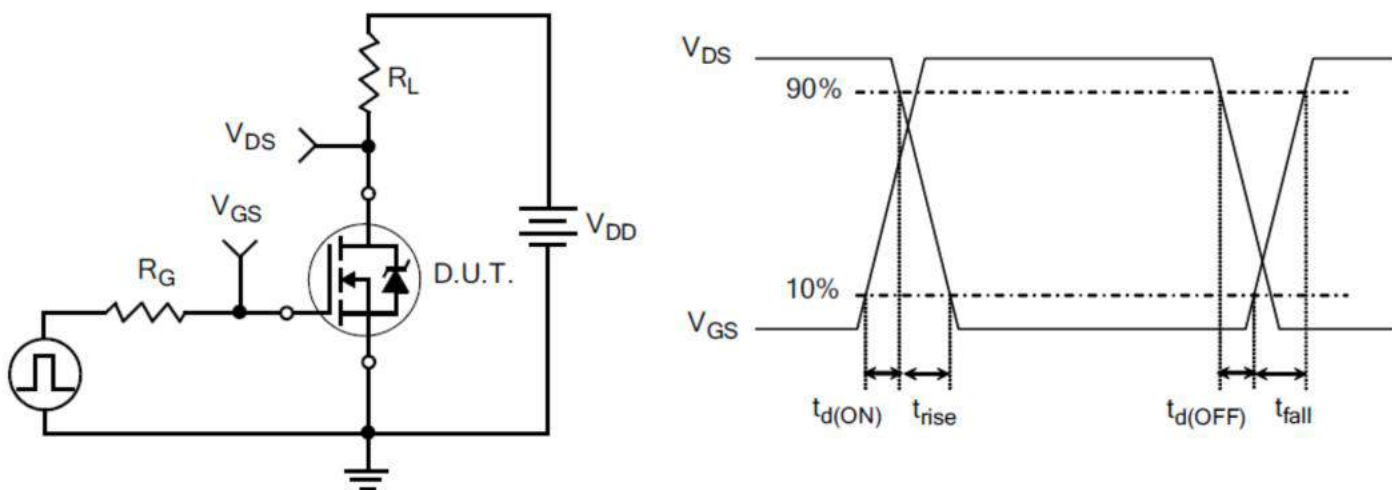
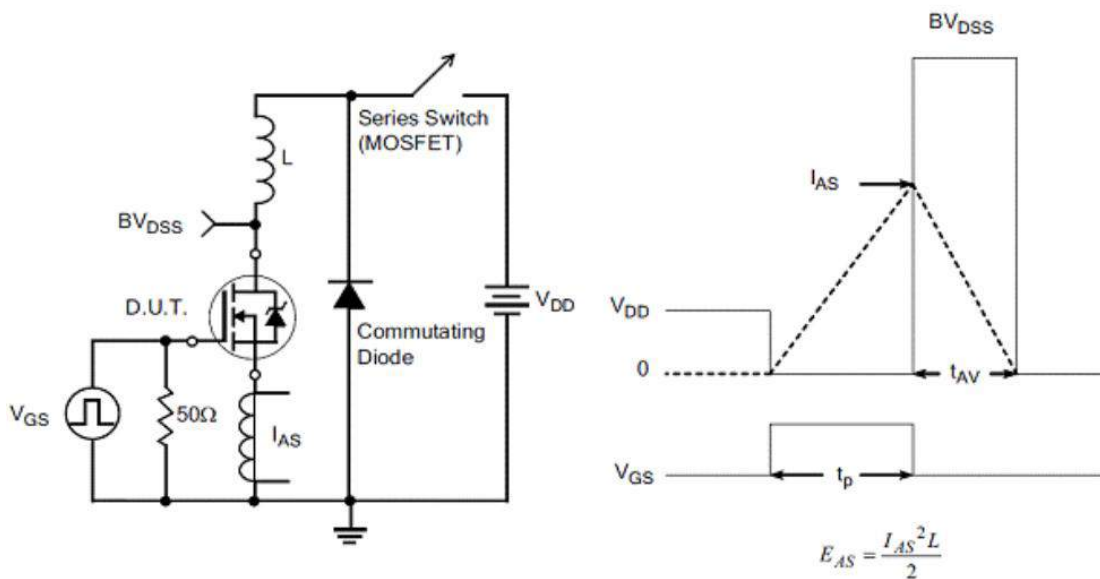
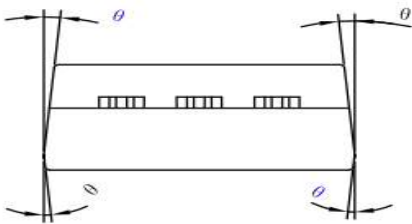
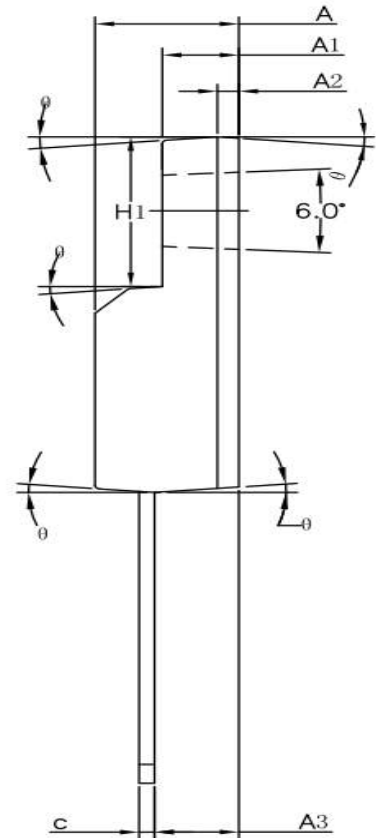
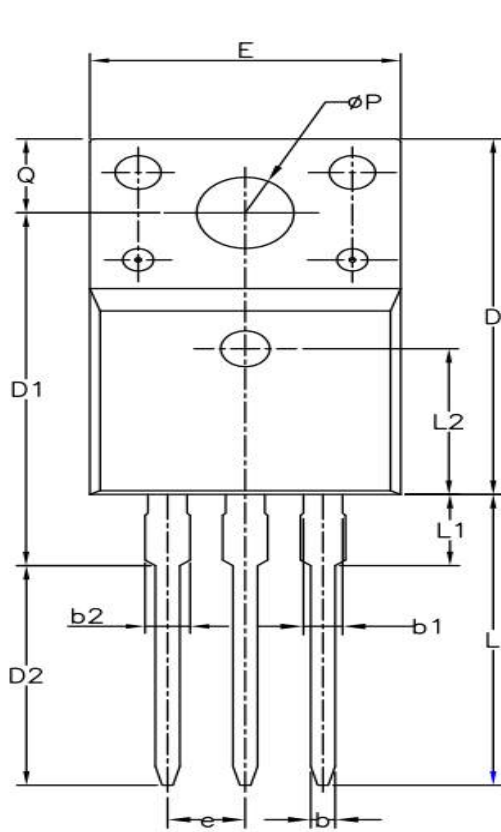


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



## TO-220-3L Package Information



SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.93
b	0.70	—	0.90
b1	1.18	—	1.38
b2	—	—	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.0
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	—	—	3.50
L2	6.50REF		
∅P	3.08	3.18	3.28
Q	3.20	—	3.40
θ 1	1°	3°	5°

## Customer Service

Sales and Service:

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