

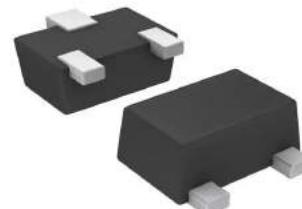


## Features

- N-Channel
- ESD protection
- SOT-723 package

$V_{DS}$	60	V
$R_{DS(on),TYP}$ @ $V_{GS}=10\text{ V}$	1.6	$\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5\text{ V}$	1.7	$\Omega$
$I_D$	0.3	A

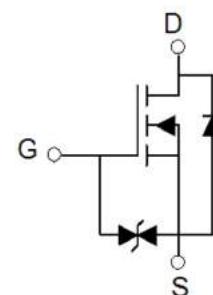
SOT-723



RoHS



Part ID	Package Type	Marking	Packing
ZT7002RK	SOT-723	RK	8000pcs/Reel



Absolute Maximum Ratings  $T_A=25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ( $T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	60	V	
$T_J$	Maximum Junction Temperature	150	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
$I_{DM}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_c=25^\circ\text{C}$	1.2	A
Mounted on Large Heat Sink				
$I_D$	Drain Current-Continuous	$T_c=25^\circ\text{C}$	0.3	A
		$T_c=70^\circ\text{C}$	0.25	A
$P_D$	Maximum Power Dissipation	0.15	W	
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	830	$^\circ\text{C}/\text{W}$	



**Electrical Characteristics ( $T_j=25^\circ\text{C}$  unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ <math>T_j=25^\circ\text{C}</math> (unless otherwise stated)</b>						
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=48\text{V}, V_{GS}=0\text{V}$	--	--	1	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	--	--	$\pm 10$	$\mu\text{A}$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=0.3\text{A}$	--	1.6	1.9	$\Omega$
$R_{DS(\text{on})}$	Drain-Source On-State Resistance	$V_{GS}=4.5\text{V}, I_D=0.2\text{A}$	--	1.7	2.2	$\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	26	--	pF
C <sub>oss</sub>	Output Capacitance		--	2.6	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	1.6	--	pF
Q <sub>g</sub>	Total Gate Charge	$V_{DD}=30\text{V}, I_D=1\text{A}, V_{GS}=10\text{V}$	--	1.7	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	0.4	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	0.3	--	nC
<b>Switching Characteristics</b>						
T <sub>d(on)</sub>	Turn-on Delay Time	$V_{DD}=30\text{V}, I_D=0.3\text{A}, R_G=10\Omega, V_{GS}=10\text{V}$	--	1	--	ns
T <sub>r</sub>	Turn-on Rise Time		--	19	--	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		--	23	--	ns
T <sub>f</sub>	Turn-Off Fall Time		--	20	--	ns
<b>Source-Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
I <sub>SD</sub>	Source-Drain Current (Body Diode)		--	--	0.3	A
V <sub>SD</sub>	Forward on voltage	$I_S=0.15\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
T <sub>rr</sub>	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_F=0.1\text{A}, dI/dt=100\text{A}/\mu\text{s}$	--	7.4	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	2.3	--	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

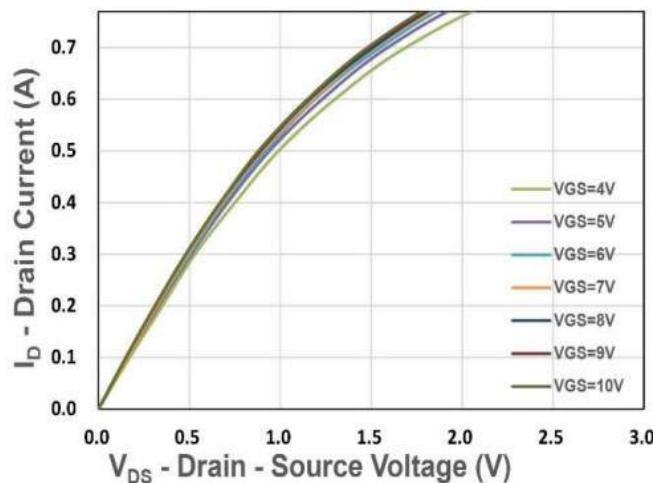
Typical performance characteristics ( $T_A=25^\circ\text{C}$  unless otherwise Specified)

Figure 1. Output Characteristics

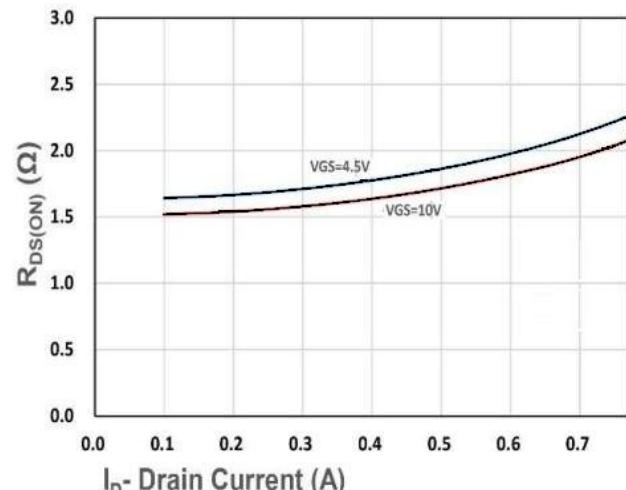


Figure 2. On-Resistance vs. ID

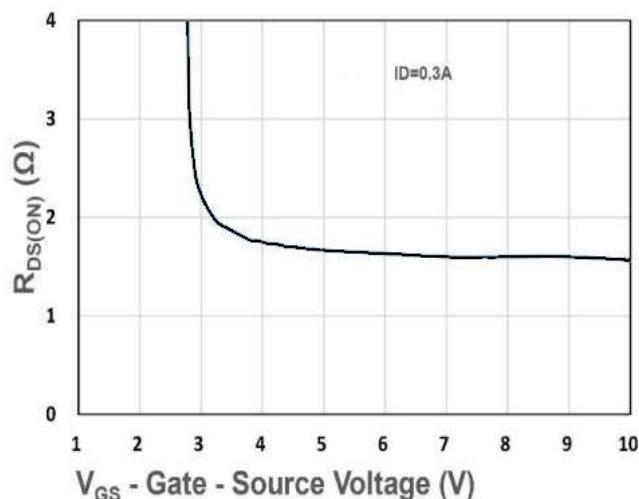


Figure 3. On-Resistance vs. VGS

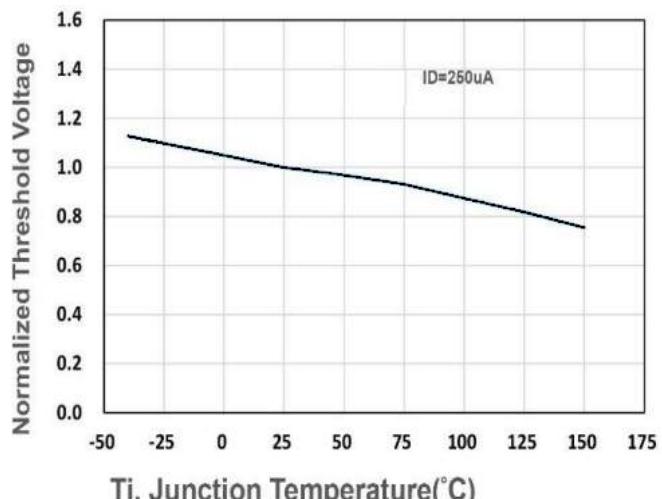


Figure 4. Gate Threshold Voltage

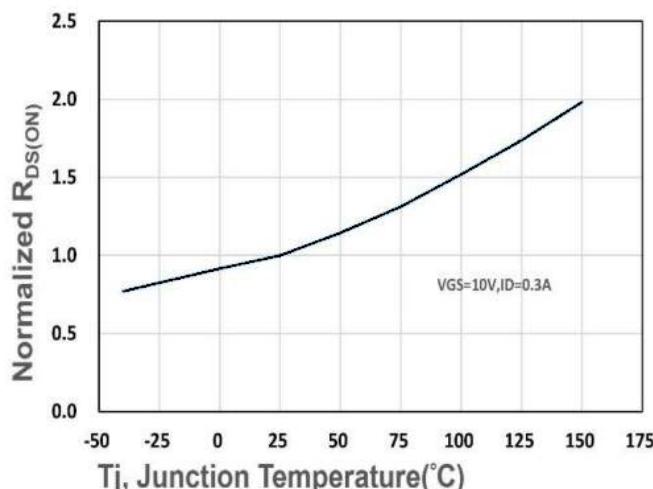


Figure 5. Drain-Source On Resistance

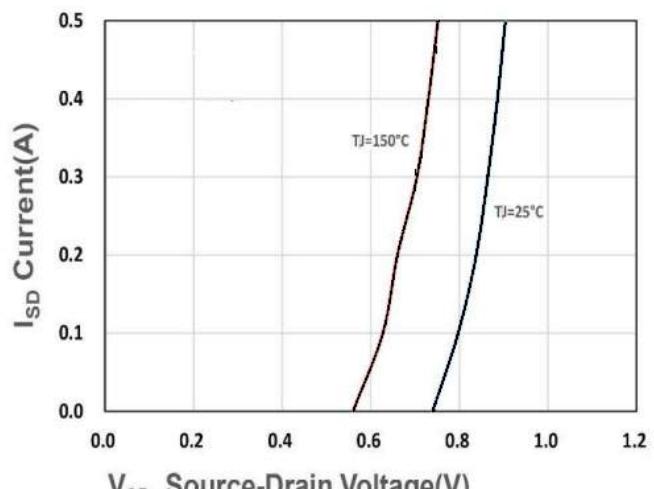


Figure 6. Source-Drain Diode Forward

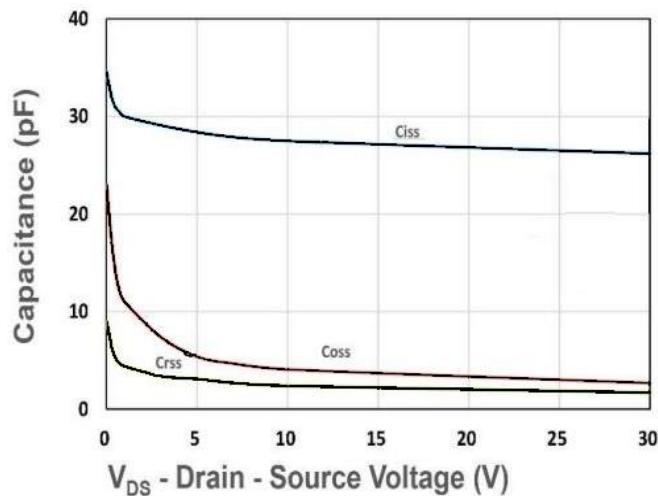
Typical performance characteristics ( $T_A=25^\circ\text{C}$  unless otherwise Specified)

Figure 7. Capacitance

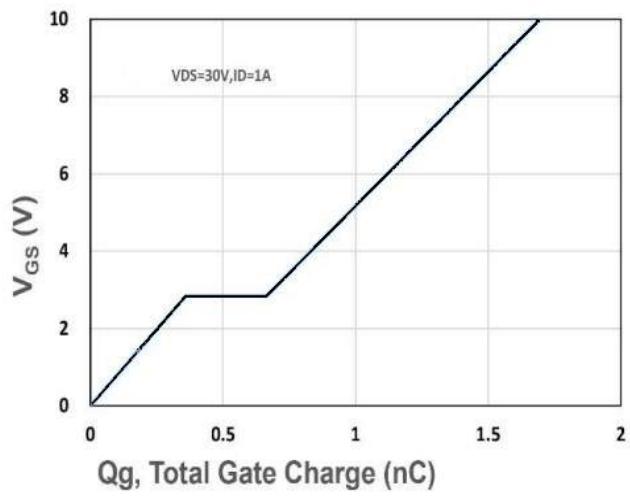


Figure 8. Gate Charge Characteristics

## Test Circuit

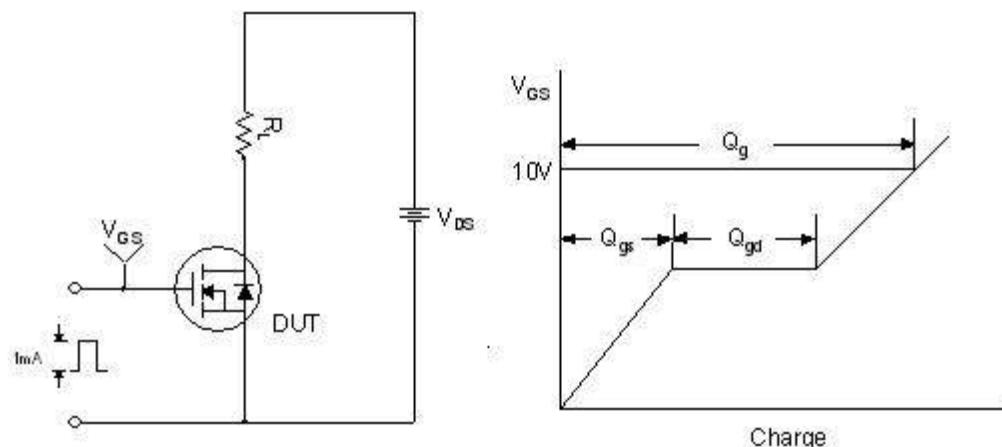


Figure 1. Gate Charge Test Circuit & Waveform

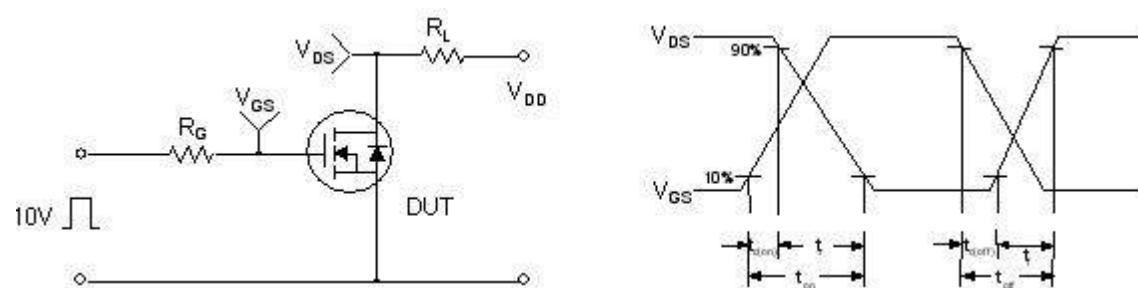


Figure 2. Resistive Switching Test Circuit & Waveforms

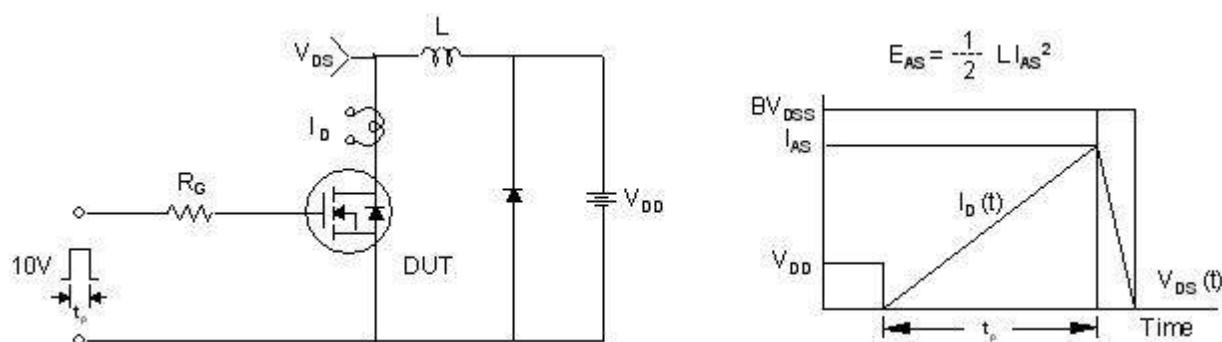
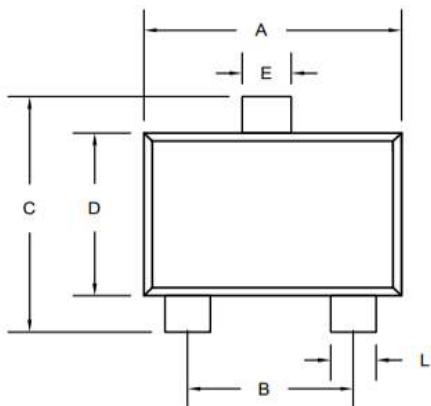


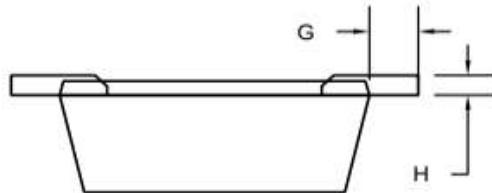
Figure 3. Unclamped Inductive Switching Test Circuit & Waveforms



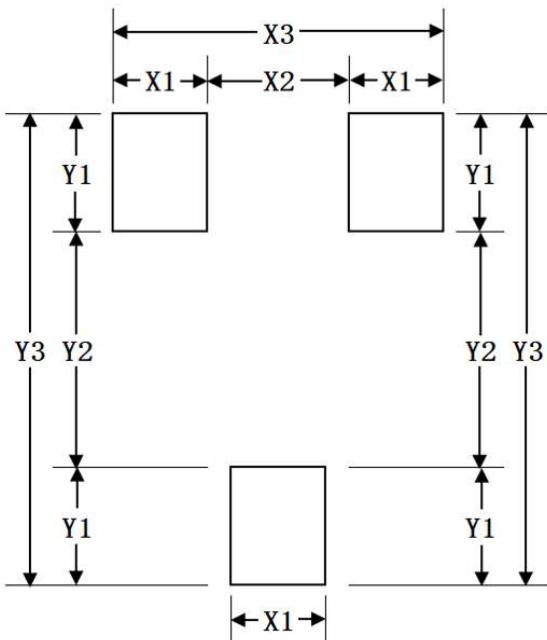
## SOT-723 Package Information



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	1.10	1.20	1.30	0.0433	0.0472	0.0512
B		0.80 typ			0.0315 typ	
C	1.10	1.20	1.30	0.0433	0.0472	0.0512
D	0.70	0.80	0.90	0.0276	0.0315	0.0354
E	0.20	0.25	0.30	0.0079	0.0098	0.0118
F	0.40	0.45	0.50	0.0157	0.0177	0.0197
G	0.15	0.20	0.25	0.0059	0.0079	0.0098
H	0.06	0.11	0.16	0.0024	0.0043	0.0063
L	0.15	0.20	0.25	0.0059	0.0079	0.0098



## Suggested Pad Layout



SYM	DIMENSIONS	
	MILLIMETER	INCHES
X1	0.40	0.016
X2	0.55	0.022
X3	1.10	0.043
Y1	0.50	0.020
Y2	0.60	0.024
Y3	1.60	0.063

## Customer Service

### Sales and Service:

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