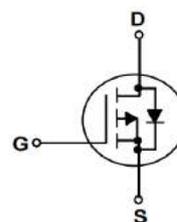
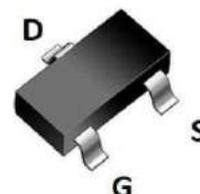


## Features

- P-Channel
- Small Signal MOSFETs
- Trench LV MOSFET Technology

$V_{DS}$	-30	V
$R_{DS(on),TYP@ V_{GS}=-10V}$	40	m $\Omega$
$R_{DS(on),TYP@ V_{GS}=-4.5V}$	46	m $\Omega$
$I_D$	-4.2	A

**SOT-23**


Part ID	Package Type	Marking	Packing
ZT3401A	SOT-23	X1xG1x	3000pcs/Reel

### Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
<b>Common Ratings (<math>T_C=25^\circ\text{C}</math> Unless Otherwise Noted)</b>			
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	-30	V
$T_J$	Maximum Junction Temperature	150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$I_{DM}$	Drain Current-Continuous@ Current-Pulsed (Note 1)	$T_C = 25^\circ\text{C}$ -25	A
<b>Mounted on Large Heat Sink</b>			
$I_D$	Drain Current-Continuous	$T_C = 25^\circ\text{C}$ -4.2	A
$P_D$	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$ 1.2	W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient (Note 2)	104	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub>=25°C (unless otherwise stated)</b>						
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	--	--	-1	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.5	-0.9	-1.3	V
R <sub>DS(on)</sub>	Drain-Source On-State Resistance (Note 3)	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A	--	40	55	mΩ
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	--	46	63	mΩ
R <sub>DS(on)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A	--	55	80	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated) (Note 4)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	--	830	--	pF
C <sub>oss</sub>	Output Capacitance		--	70	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	57	--	pF
R <sub>g</sub>	Gate Resistance f=1MHz	f=1MHz	--	2.2	--	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V, I <sub>D</sub> =-4.2A, V <sub>GS</sub> =-4.5V	--	8	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	1.8	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	2.7	--	nC
<b>Switching Characteristics</b>						
T <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =-15V, I <sub>D</sub> =4.2A, R <sub>G</sub> =3Ω, V <sub>GS</sub> =-10V	--	7	--	ns
T <sub>r</sub>	Turn-on Rise Time		--	3	--	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		--	30	--	ns
T <sub>f</sub>	Turn-Off Fall Time		--	12	--	ns
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
I <sub>SD</sub>	Source-Drain Current (Body Diode)		--	--	-4.2	A
V <sub>SD</sub>	Forward on voltage (Note 3)	I <sub>S</sub> = -4.2A, V <sub>GS</sub> =0V	--	--	1.2	V

**Notes:**

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C
2. The data tested by surface mounted on a 1 inch2 FR-4 board with 20Z copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

## Typical Characteristics

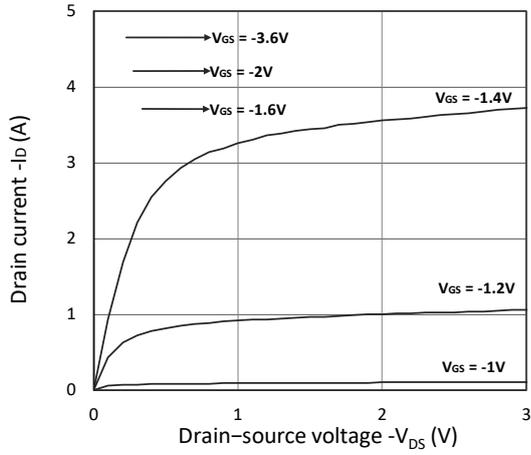


Figure 1. Output Characteristics

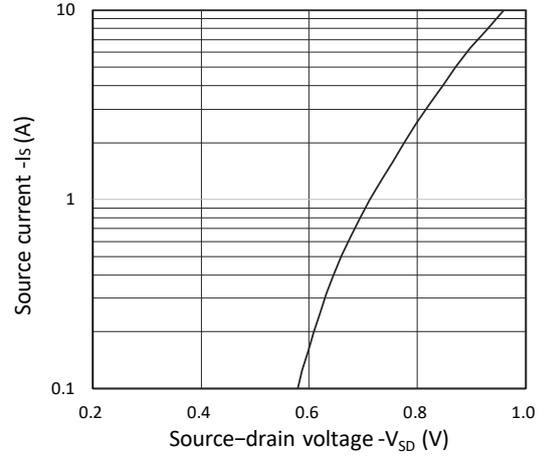


Figure 4. Forward Characteristics of Reverse

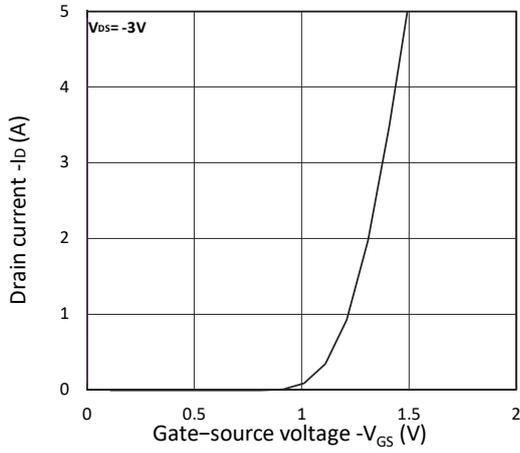


Figure 2. Transfer Characteristics

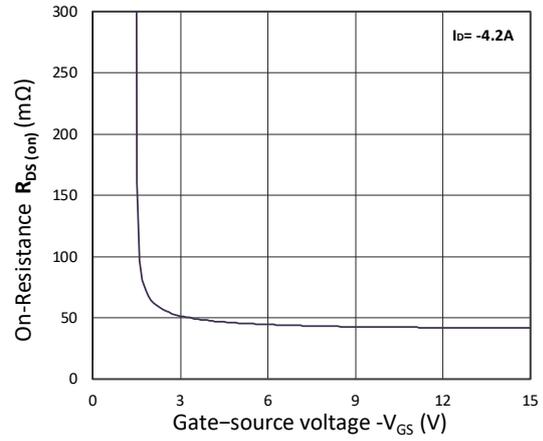


Figure 5.  $R_{DS(on)}$  vs.  $V_{GS}$

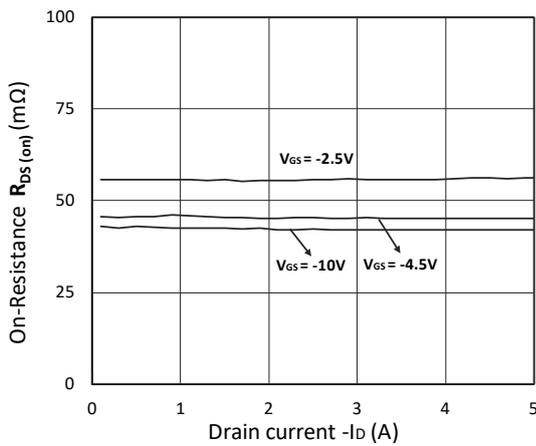


Figure 3.  $R_{DS(on)}$  vs.  $I_D$

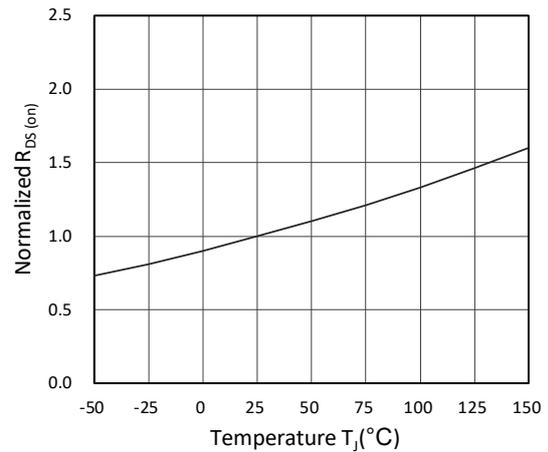


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

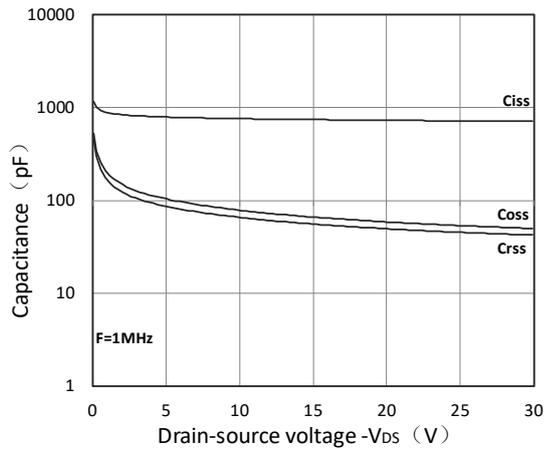


Figure 7. Capacitance Characteristics

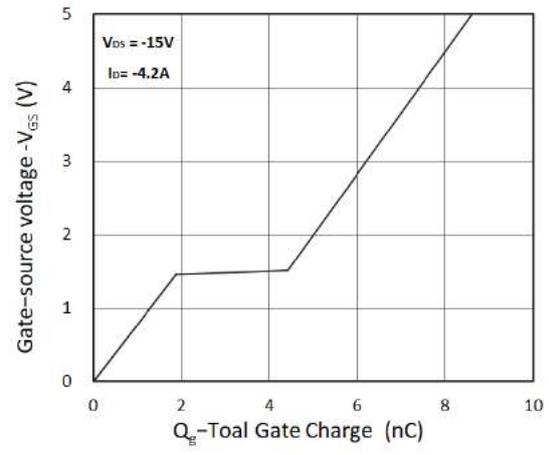
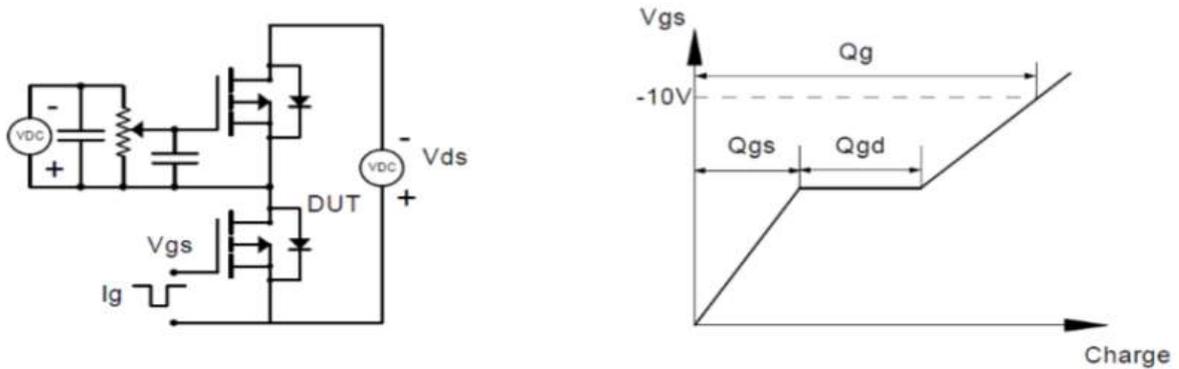


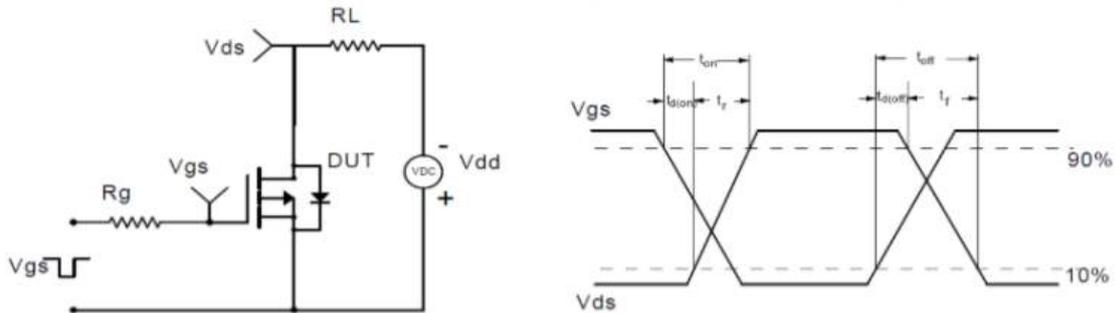
Figure 8. Gate Charge Characteristics

## Test Circuit

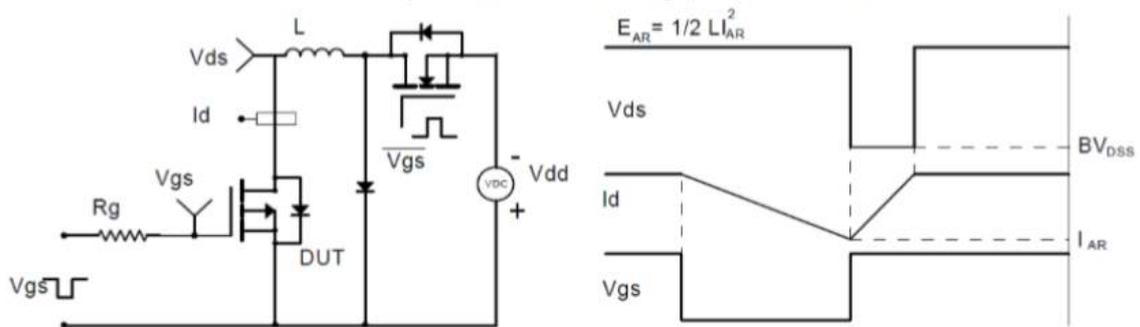
Gate Charge Test Circuit & Waveform



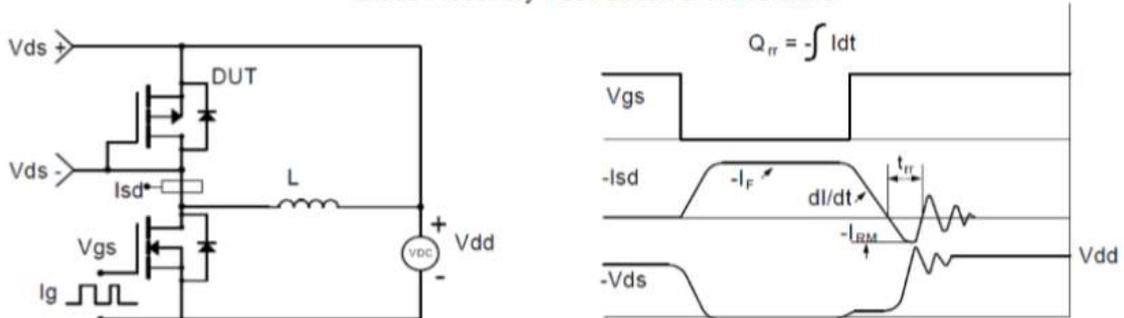
Resistive Switching Test Circuit & Waveforms



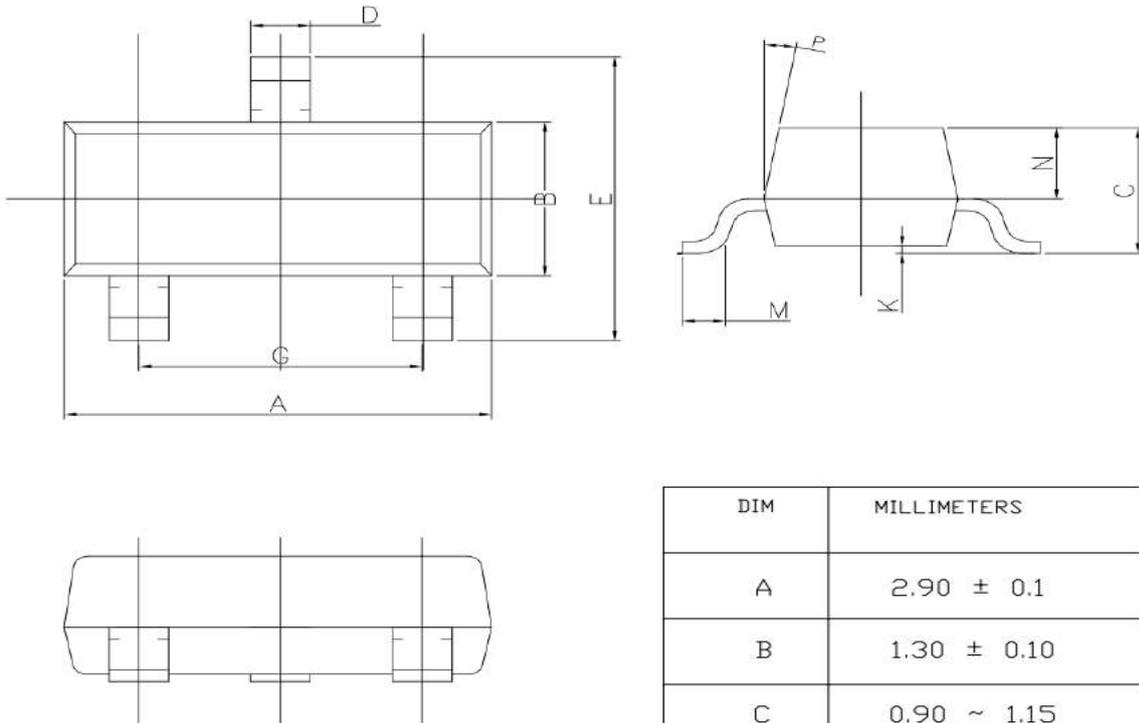
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## SOT-23 Package Information



DIM	MILLIMETERS
A	2.90 ± 0.1
B	1.30 ± 0.10
C	0.90 ~ 1.15
D	0.40 ± 0.1
E	2.40 ± 0.15
G	1.90 ± 0.10
K	0.00~0.10
M	0.30MIN
N	0.60 ± 0.10
P	10°TYP

## Customer Service

### Sales and Service:

[zj@ztasemi.com](mailto:zj@ztasemi.com)