



Features

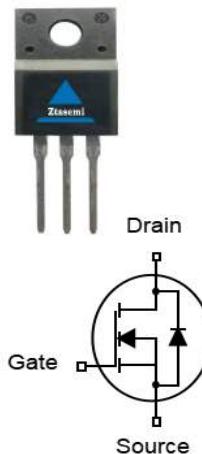
- N-Channel
- Low Gate Charge
- 100% avalanche tested
- Low ON Resistance
- RoHS compliant
- Improved dv/dt Capability
- 100% EAS Tested



Part ID	Package Type	Marking	Packing
ZT9N90F	TO-220F	ZT9N90F	1000pcs/Tape

V_{DS}	900	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	1.2	Ω
I_D	9	A

TO-220F



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 30	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	900	V	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
I_{DM}	Drain Current-Continuous@ Current-Pulsed (Note 2)	$T_c=25^\circ\text{C}$	36	A

Mounted on Large Heat Sink

I_D	Drain Current-Continuous (Note 1)	$T_c=25^\circ\text{C}$	9	A
		$T_c=100^\circ\text{C}$	5.6	A
P_D	Total power dissipation	$T_c=25^\circ\text{C}$	25	W
	Derating Factor above 25 °C		0.2	W/ $^\circ\text{C}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case		5	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		69	$^\circ\text{C}/\text{W}$

Drain-Source Avalanche Ratings

EAS	Avalanche Energy, Single Pulsed (Note 3)	486	mJ
dv/dt	Reverse Diode dv/dt (Note 4)	5	V/ns



Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J=25^\circ\text{C}$ (unless otherwise stated)						
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	900	--	--	V
BVDSS/TJ	Breakdown voltage temperature coefficient	$I_D=250\mu\text{A}$, referenced to 25°C	--	0.82	--	V/ $^\circ\text{C}$
IDSS	Zero Gate Voltage Drain Current	$V_{DS}=900\text{V}, V_{GS}=0\text{V}$	--	--	1	μA
IGSS	Gate-Body Leakage Current	$V_{GS}=\pm 30\text{V}, V_{DS}=0\text{V}$	--	--	± 100	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	3.0	4.0	5.0	V
RDS(on)	Drain-Source On-State Resistance	$V_{GS}=10\text{V}, I_D=4.5\text{A}$	--	1.2	1.5	Ω
Gfs	Forward Transconductance	$V_{GS}=10\text{V}, I_D=4.5\text{A}$	--	7.2	--	S
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
Ciss	Input Capacitance	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	--	2050	--	pF
Coss	Output Capacitance		--	144	--	pF
Crss	Reverse Transfer Capacitance		--	11	--	pF
Rg	Gate Resistance	$V_{DS}=0\text{V}$ Scan F mode	--	2.3	--	Ω
Qg	Total Gate Charge	$V_{DS}=720\text{V}, I_D=9\text{A}, V_{GS}=10\text{V}$	--	42.1	--	nC
Qgs	Gate-Source Charge		--	13.1	--	nC
Qgd	Gate-Drain Charge		--	18.6	--	nC
Switching Characteristics (Note 2)						
Td(on)	Turn-on Delay Time	$V_{DS}=450\text{V}, I_D=9\text{A}, R_G=25\Omega, V_{GS}=10\text{V}$	--	49	--	ns
Tr	Turn-on Rise Time		--	64	--	ns
Td(off)	Turn-Off Delay Time		--	85	--	ns
Tf	Turn-Off Fall Time		--	33	--	ns
Source-Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
VSD	Forward on voltage	$I_S=9\text{A}, V_{GS}=0\text{V}$	--	--	1.3	V
Trr	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_S=9\text{A}, V_{GS}=0\text{V}, di/dt=100\text{A}/\mu\text{s}$	--	656	--	ns
Qrr	Reverse Recovery Charge		--	9.2	--	uC
Irrm	Peak Reverse Recovery Current		--	28	--	A

Notes

1. Drain current is limited by maximum junction temperature.
2. Repetitive rating : pulse width limited by junction temperature.
3. $L = 12\text{mH}, I_{AS} = 9\text{A}, V_{DD} = 50\text{V}, R_G = 25\Omega$, Starting at $T_J = 25^\circ\text{C}$
4. $I_{SD} \leq I_D$, $di/dt = 100\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting at $T_J = 25^\circ\text{C}$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

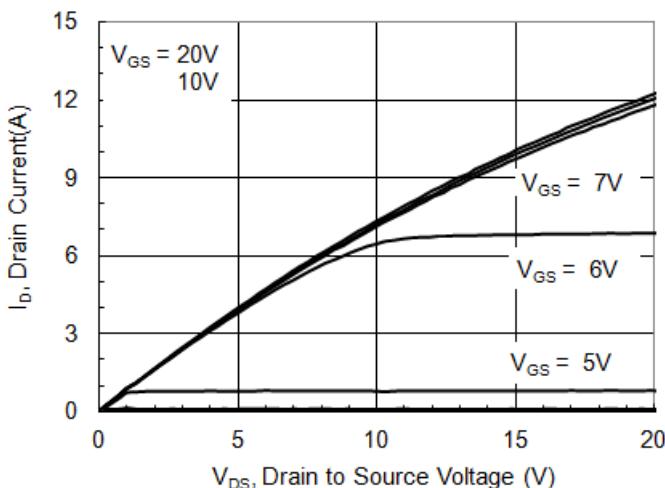


Fig1. Output characteristics

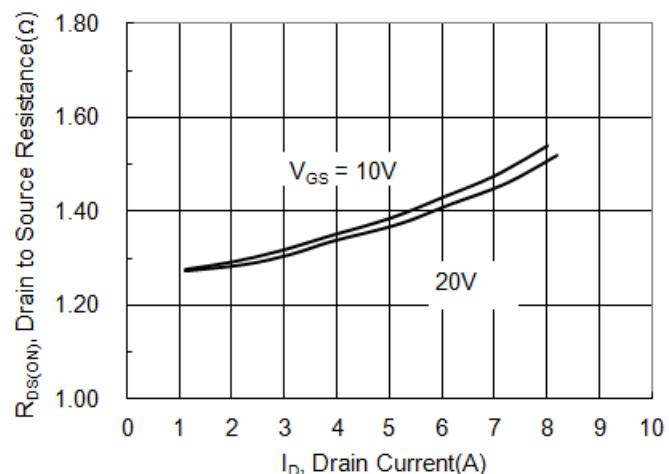


Fig4. Drain-source on-state resistance

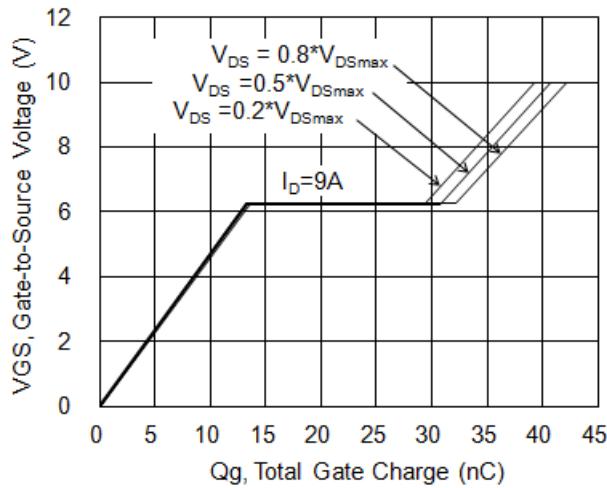


Fig2. Gate charge characteristics

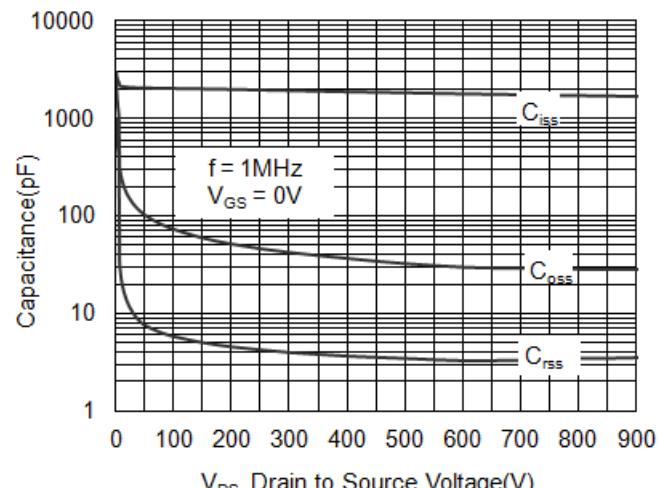


Fig5. Capacitance Characteristics

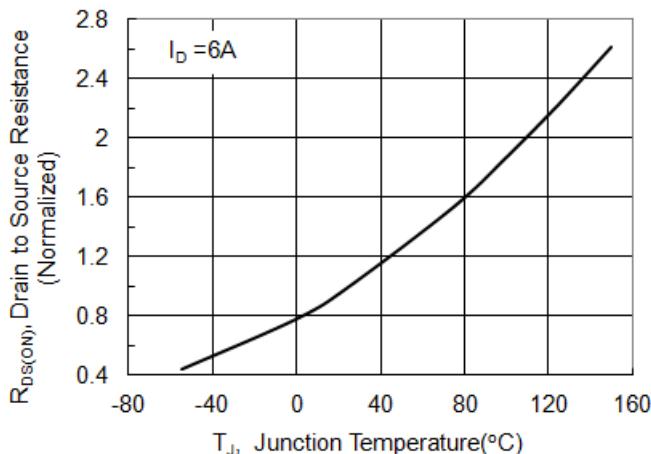


Fig3. $R_{DS(ON)}$ vs junction temperature

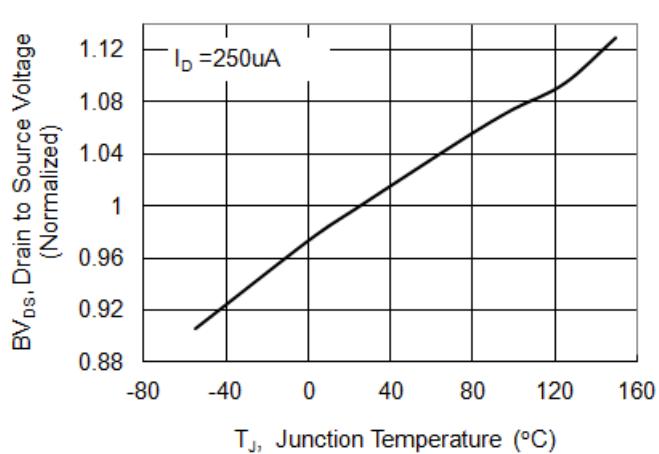


Fig6. BV_{DS} vs junction temperature

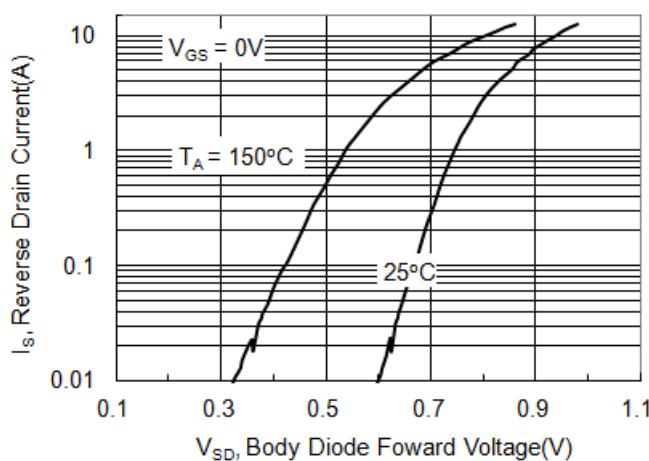


Fig 7. Forward characteristics of reverse diode

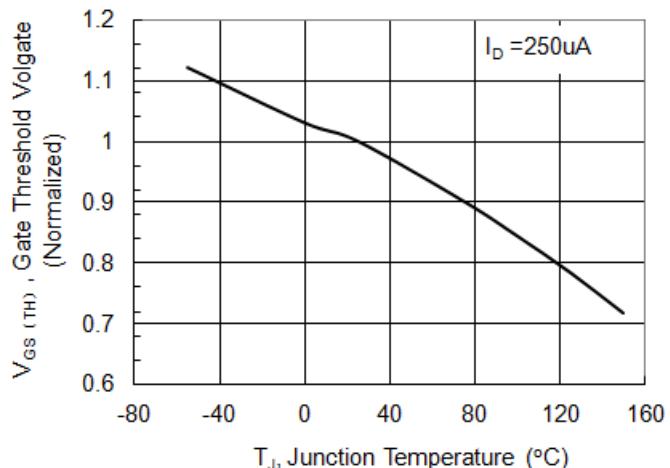


Fig 9. $V_{GS(TH)}$ vs junction temperature

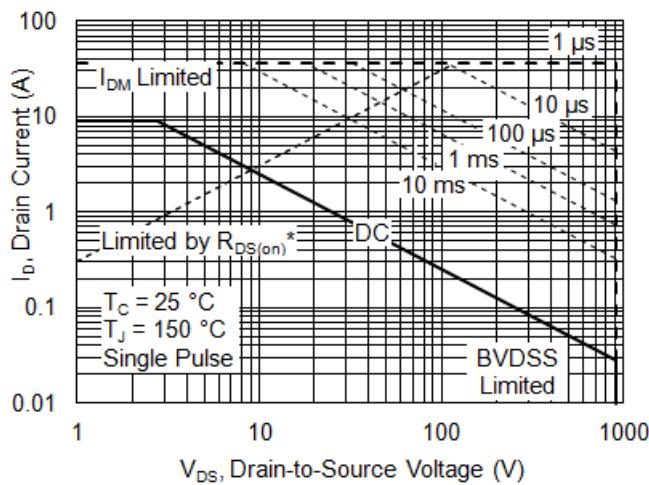


Fig 8 . Safe operating area (TO-220F)

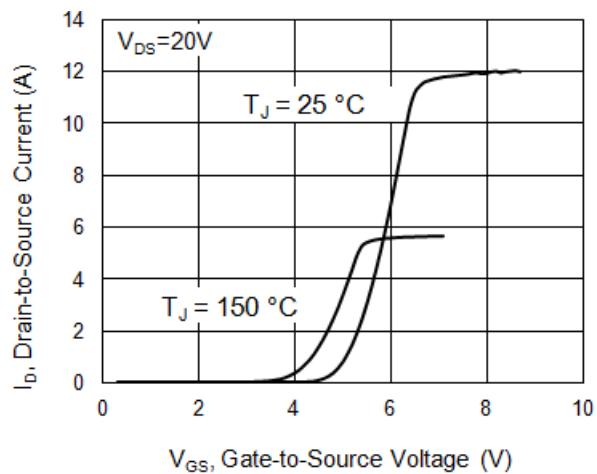


Fig 10. Transfer characteristics

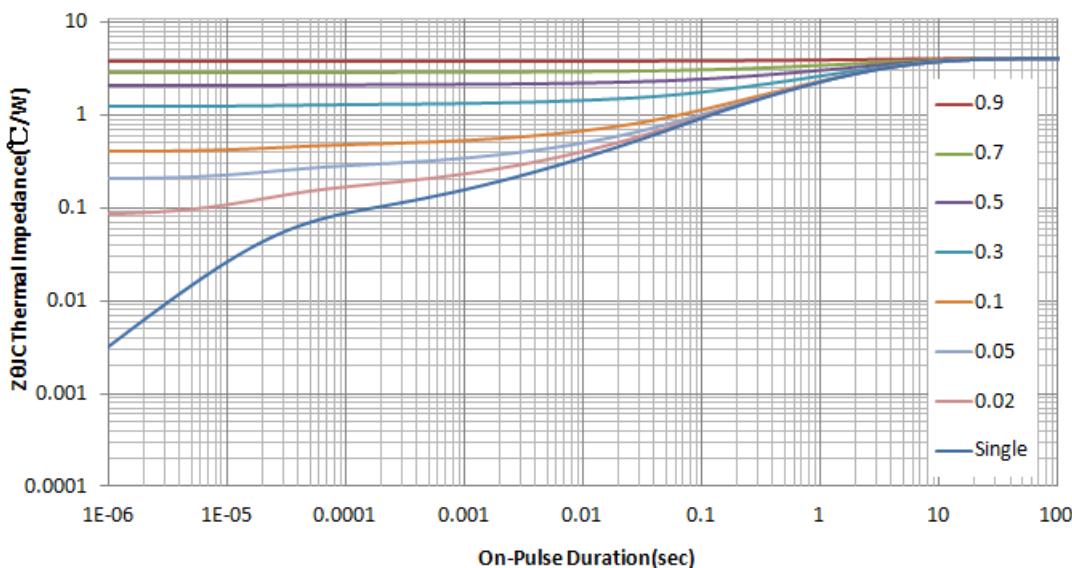


Fig 11 . Transient thermal impedance(TO-220F)

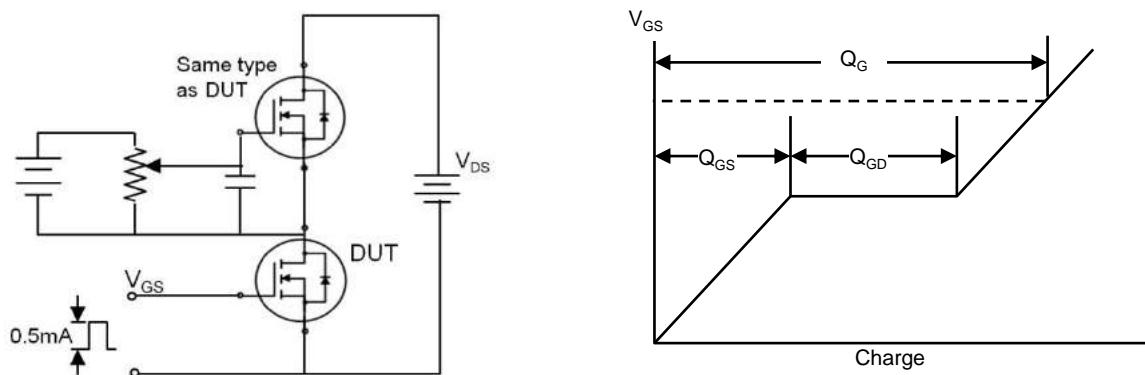


Figure A: Gate charge test circuit & waveform

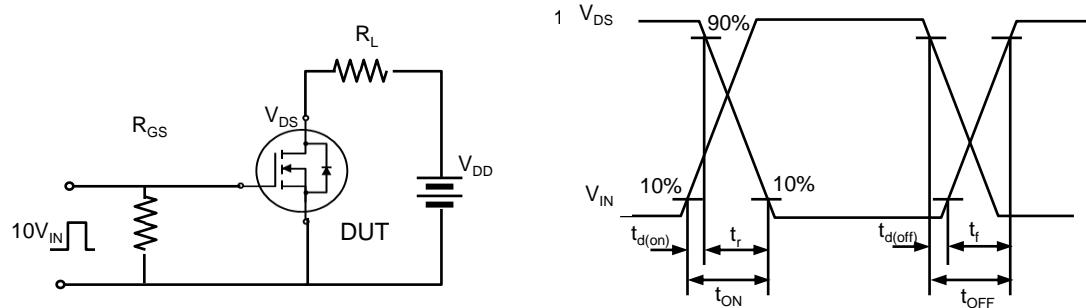


Figure B: Switching time test circuit & waveform

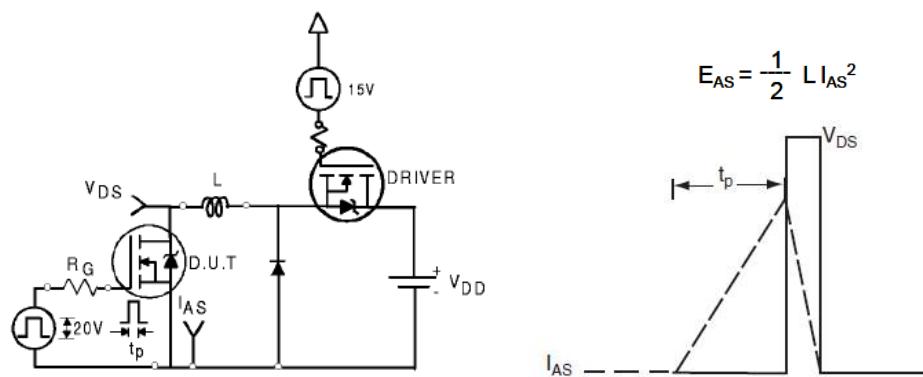
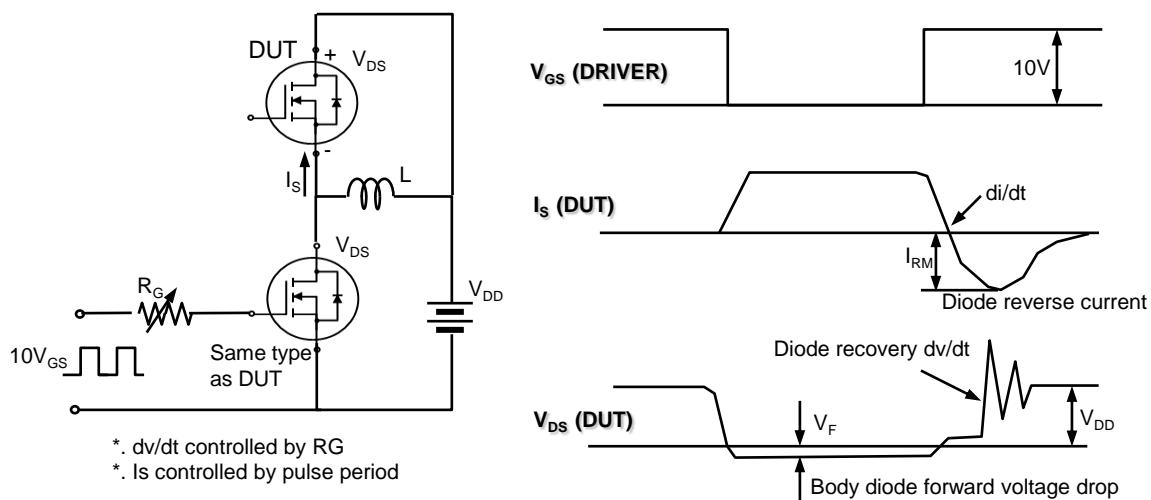
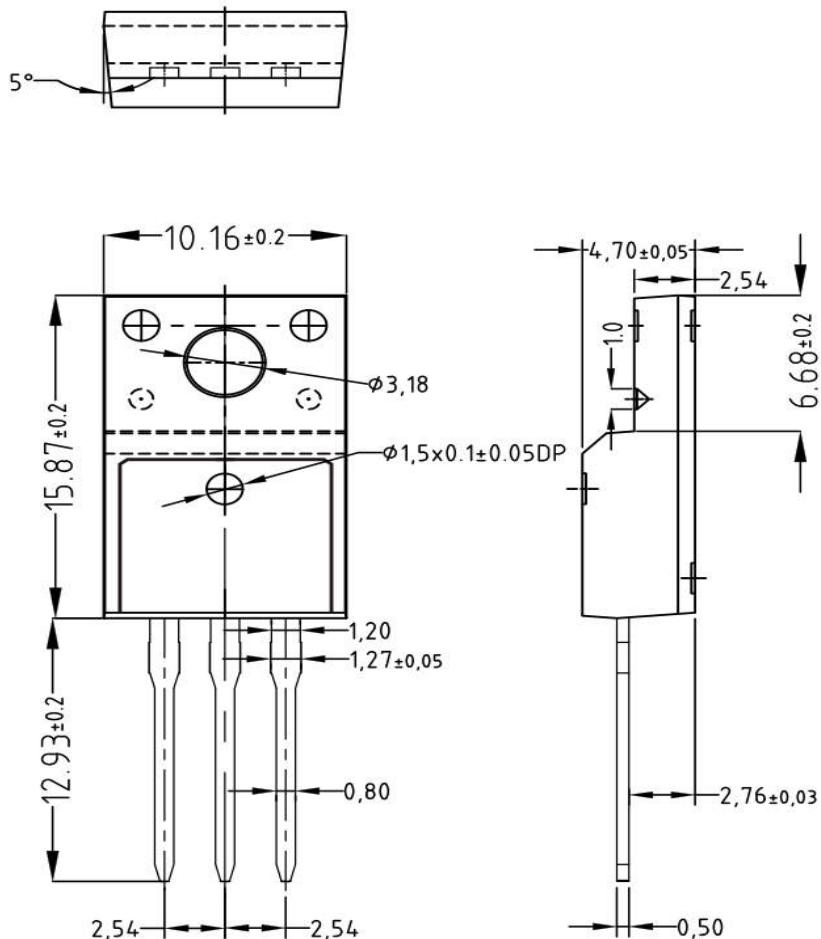


Figure C: Unclamped Inductive switching test circuit & waveform





TO-220F Package Information



Customer Service

Sales and Service:

zj@ztasemi.com